

ISL95855B

3+2+1 Voltage Regulator for IMVP8™ Desktop CPUs with Non-SVID VccSA Support

FN8844  
Rev 0.00  
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Compliant with IMVP8™, the [ISL95855B](#) provides a complete power solution for Intel microprocessors supporting core, graphics, and system agent rails. The controller provides control and protection for three Voltage Regulator (VR) outputs. The VR A output can be configured for 3-, 2-, or 1-phase operation. VR B is configurable for 2- or 1-phase operation and VR C supports 1-phase operation. The programmable address options for these three outputs allow for maximum flexibility in support of the IMVP8™ CPU. All three VRs share a common serial control bus to communicate with the CPU and achieve lower cost and smaller board area compared with a two-chip approach. VR C is a fixed non-SVID rail for VccSA in support of desktop CPUs.

Based on Intersil's Robust Ripple Regulator (R3™) technology, the R3™ modulator has many advantages compared to traditional modulators. These include faster transient settling time, variable switching frequency in response to load transients, and improved light-load efficiency due to diode emulation mode with load-dependent low switching frequency.

The ISL95855B has several other key features. The controller provides PWM outputs, which support Intel DrMOS power stages (or similar) and discrete power stages using the Intersil ISL95808 high voltage synchronous rectified buck MOSFET driver. The ISL95855B supports the system input power monitor (PSYS) option. The controller supports either DCR current sensing with a single NTC thermistor for DCR temperature compensation, or more precision through resistor current sensing if desired. All three outputs feature remote voltage sense, programmable I<sub>MAX</sub>, adjustable switching frequency, OC protection, and a single VR\_READY power-good indicator.

Features

- Supports Intel serial data bus interface and programmable SVID addresses for each output
  - VR C non-SVID VccSA support
  - IA, GT and GTUS SVID support
- System input power monitor (PSYS) supported
- Three output controllers
  - VR A configurable for 3-, 2-, 1-phase VR design
  - VR B configurable for 2-, 1-phase VR design
  - VR C supports 1-phase VR design
- 0.5% system accuracy over temperature
- Supports multiple current sensing methods
  - Lossless inductor DCR current sensing
  - Precision resistor current sensing
- Differential remote voltage sensing
- Programmable V<sub>BOOT</sub> voltage at start-up
- Resistor programmable address selection, I<sub>MAX</sub>, slew rate, switching frequency, and droop
- Adaptive body diode conduction time reduction

Applications

- IMVP8 compliant desktops only

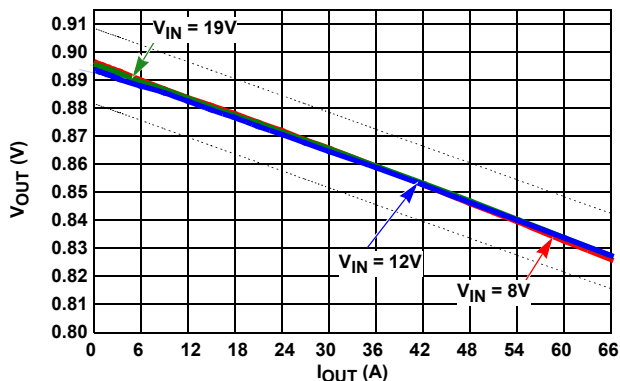


FIGURE 1. LOAD LINE REGULATION

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