



## Product Description

GRF2072 is a broadband, linear, ultra-low noise amplifier designed for small cell, wireless infrastructure and other high performance RF applications requiring ultra-low NF, high gain and linearity.

Configured as a first stage LNA, linear driver or cascaded gain block, it offers high levels of reuse both within a design and across platforms.

GRF2072 is a member of a family of pin compatible, ultra low noise devices which cover a wide range of frequency bands with industry leading NF and gain:

**GRF2070:** 0.4 to 1.5 GHz

**GRF2071:** 0.7 to 2.7 GHz

**GRF2072:** 2.3 to 3.8 GHz

**GRF2073:** 3.0 to 5.0 GHz

Consult with the GRF applications engineering team for application notes, custom tuning/evaluation board data and device s-parameters.

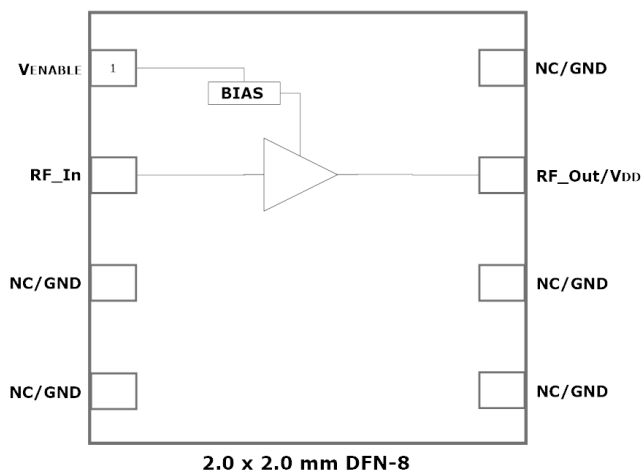
## Features

Reference: 5V/70mA/2.5 GHz

- Gain: 19.8 dB
- Eval Board NF: 0.55 dB
- OP1dB: 20.0 dBm
- OIP3: 37.5 dBm
- Flexible Bias Voltage and Current
- Process: GaAs pHEMT

## Applications

- Cellular Infrastructure
- Small Cells and Cellular Repeaters
- Distributed Antenna Systems



## Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V <sub>DD</sub>	0	6.0	V
RF Input Power: (Load VSWR < 2:1; V <sub>D</sub> : 5.0 volts)	P <sub>IN MAX</sub>		23	dBm
Operating Temperature (Package Heat Sink)	T <sub>AMB</sub>	-40	105	°C
Maximum Channel Temperature (MTTF > 10 <sup>6</sup> Hours)	T <sub>MAX</sub>		170	°C
Maximum Dissipated Power	P <sub>DISS MAX</sub>		500	mW
<b>Electrostatic Discharge:</b>				
Charged Device Model:	CDM	1500		V
Human Body Model:	HBM	500		V
<b>Storage:</b>				
Storage Temperature	T <sub>STG</sub>	-65	150	°C
Moisture Sensitivity Level	MSL		1	--



**Caution!** ESD Sensitive Device

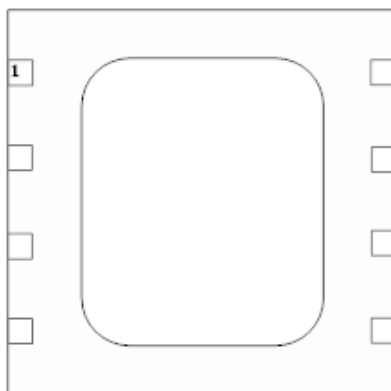


Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

**Note:** For package dimensions and manufacturing information, see the [Guerrilla-RF.com](http://Guerrilla-RF.com) website for the following document located on the GRF2072 landing page: **Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification.**

[Link to manufacturing note](#)

### Pin Out (Top View)



### Pin Assignments:

Pin	Name	Description	Note
1	VENABLE	Enable Voltage Input	VENABLE and series resistor set I <sub>DDQ</sub> . VENABLE < =0.2 volts disables device. On -die pull-down resistor will turn the part off if this node is allowed to float.
2	RF_In	RF Input	External match must provide DC block
3	NC/GND	No Connect or Ground	No internal connection to die
4	NC/GND	No Connect or Ground	No internal connection to die
5	NC/GND	No Connect or Ground	No internal connection to die
6	NC/GND	No Connect or Ground	No internal connection to die
7	RF_Out/VDD	RF Output	Provide device V <sub>DD</sub> via external bias inductor
8	NC/GND	No Connect or Ground	No internal connection to die
<b>PKG BASE</b>	<b>GND</b>	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



Preliminary

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Ultra-Low Noise Amplifier  
Tuning Range: 2.3 – 3.8 GHz

## Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
<b>Gain Mode (Venable high)</b>						$V_{DD} = 5.0\text{ V}$ , $T_A = 25\text{ }^\circ\text{C}$
Test Frequency	$F_{TEST}$		2.5		GHz	2.5 to 2.7 GHz Tune
Evaluation Board Gain	S21		19.8		dB	
Evaluation Board Noise Figure	NF		0.55		dB	Evaluation Board SMA to SMA
Output 3rd Order Intercept Point	OIP3		37.5		dBm	+3 dBm $P_{OUT}$ per tone at 2 MHz Spacing (2499 and 2501 MHz)
Output 1dB Compression Point	OP1dB		20.0		dBm	
Switching Rise Time	$T_{RISE}$		500		ns	
Switching Fall Time	$T_{FALL}$		500		ns	
Supply Current	$I_{DD}$		70		mA	Adjustable for optimal IP3
Enable Current	$I_{ENABLE}$		3.5		mA	
<b>Thermal Data</b>						
Thermal Resistance (measured via IR scan)	$\Theta_{jc}$		54		$^\circ\text{C}/\text{W}$	On standard evaluation board
Channel Temperature @ +85 C Reference (Package Heat Sink)	$T_{CHANNEL}$		104		$^\circ\text{C}$	$V_{DD}: 5.0\text{ V}$ ; $I_{DDQ}: 70\text{ mA}$ ; No RF; $P_{DISS}: 350\text{ mW}$

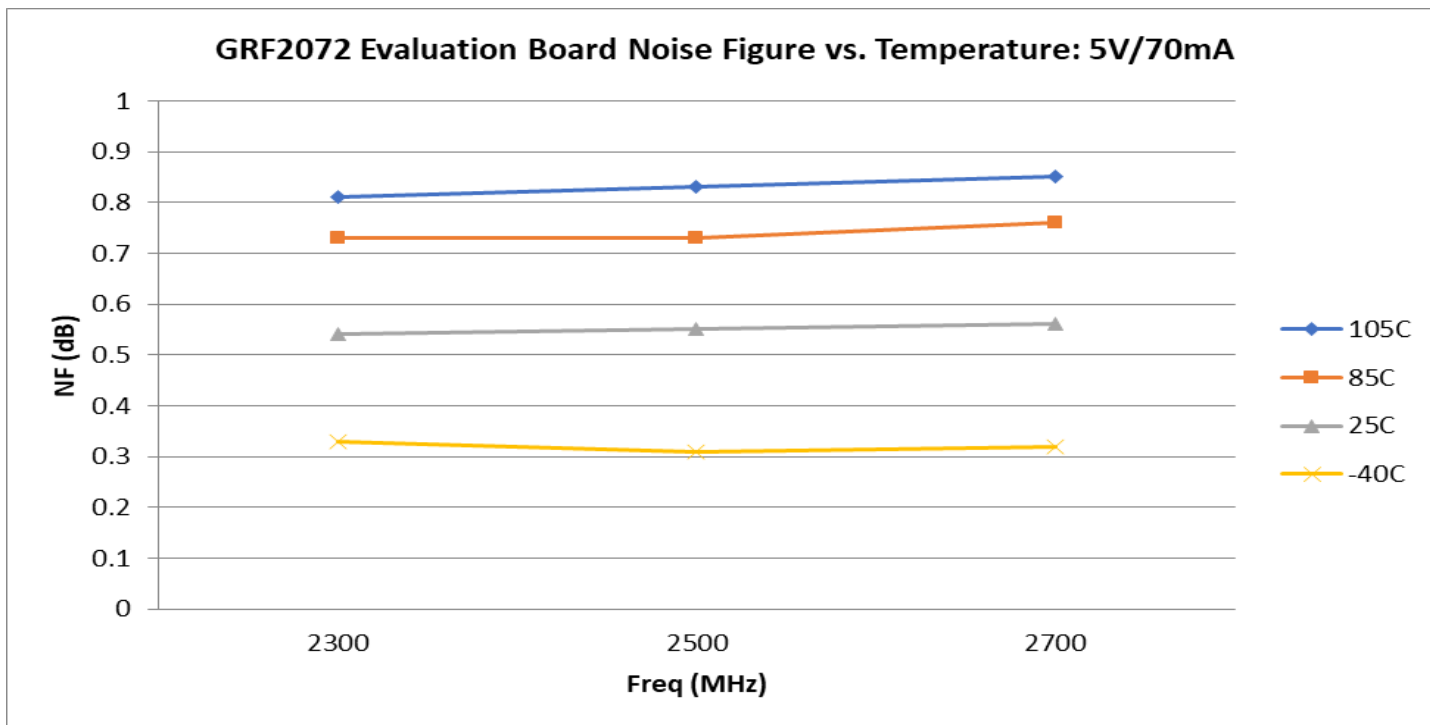
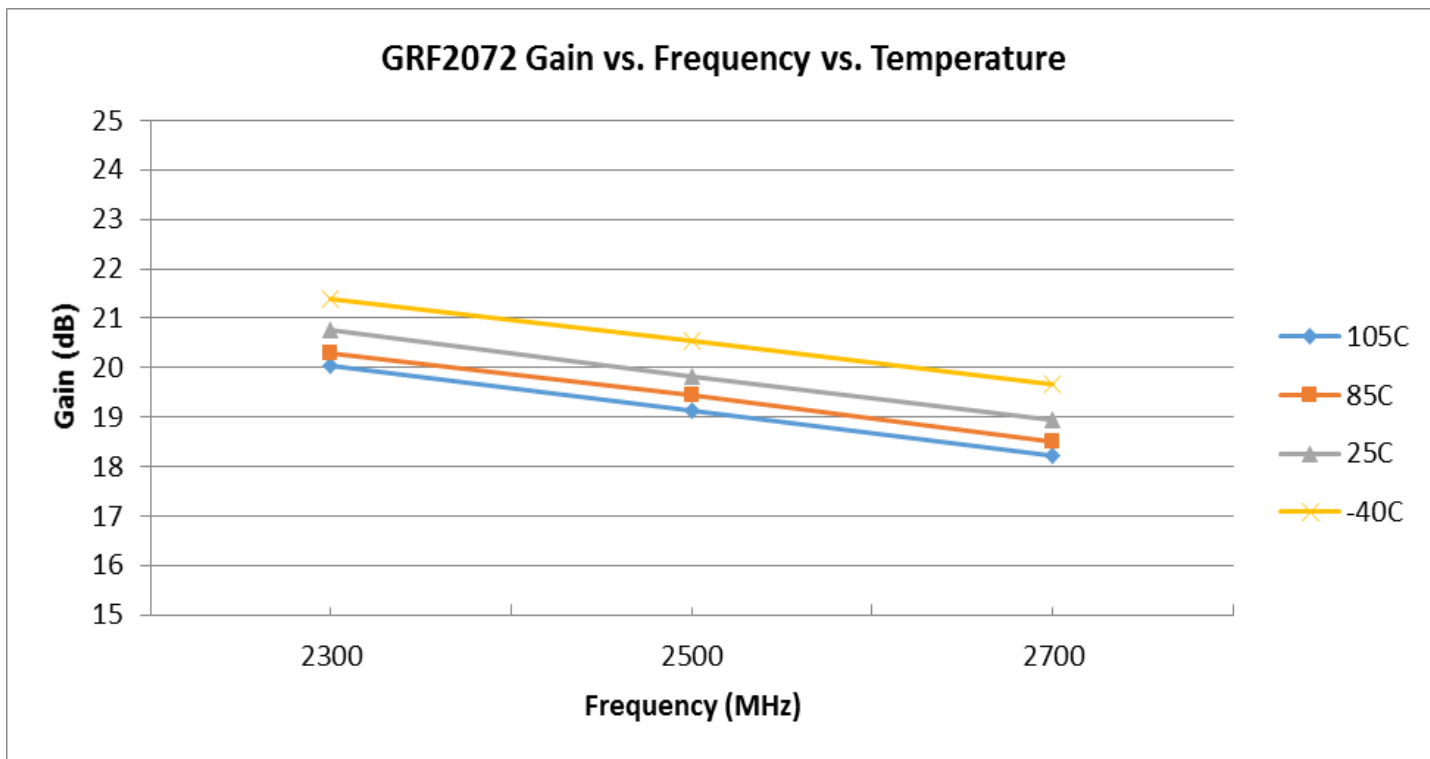


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## GRF2072 Evaluation Board Measured Data:



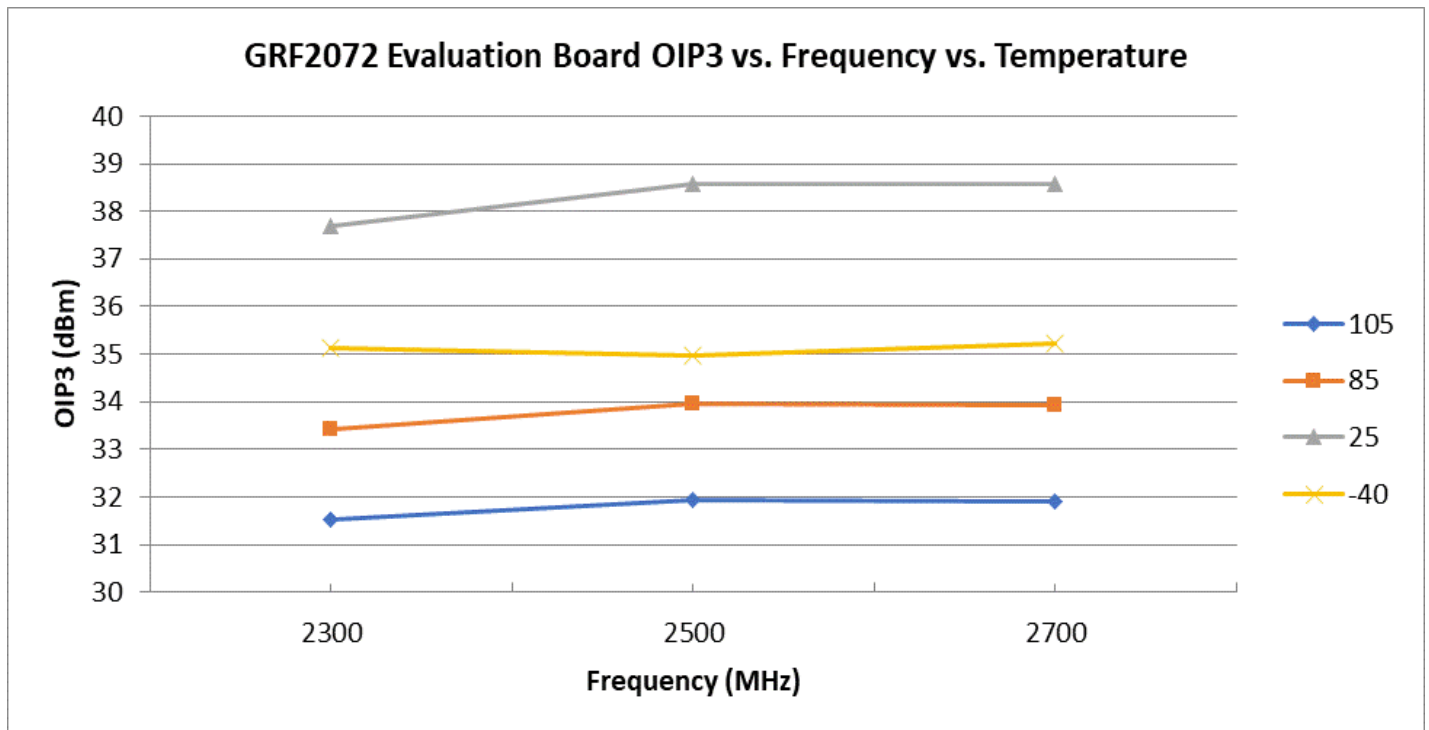
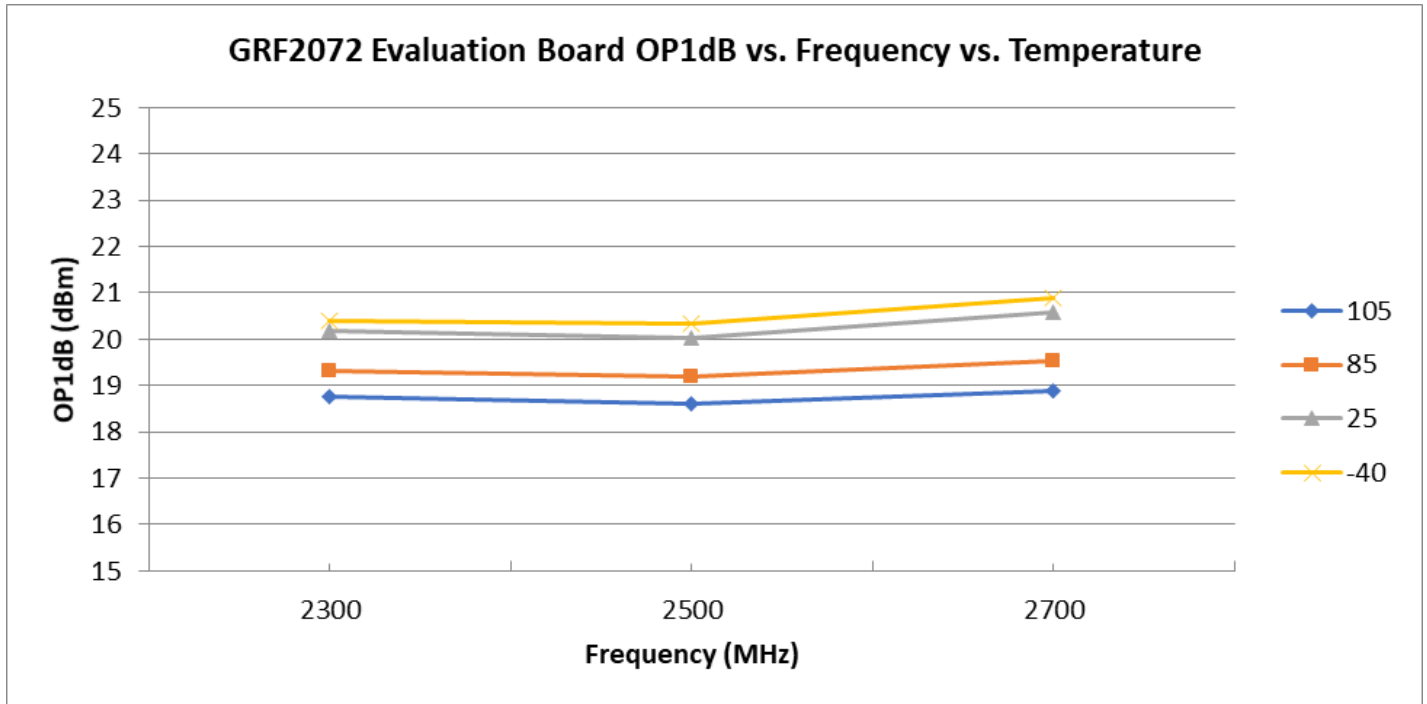


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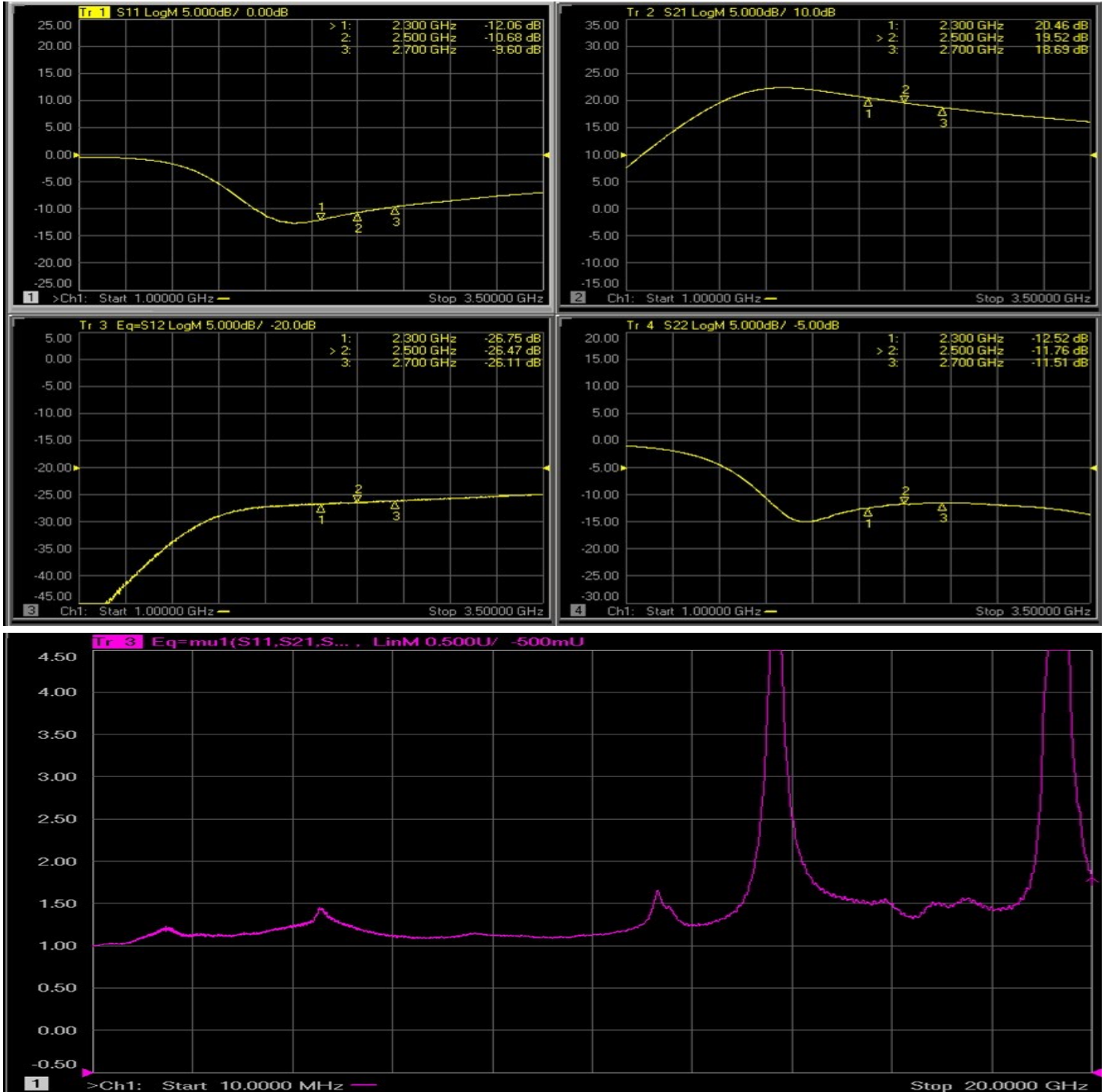


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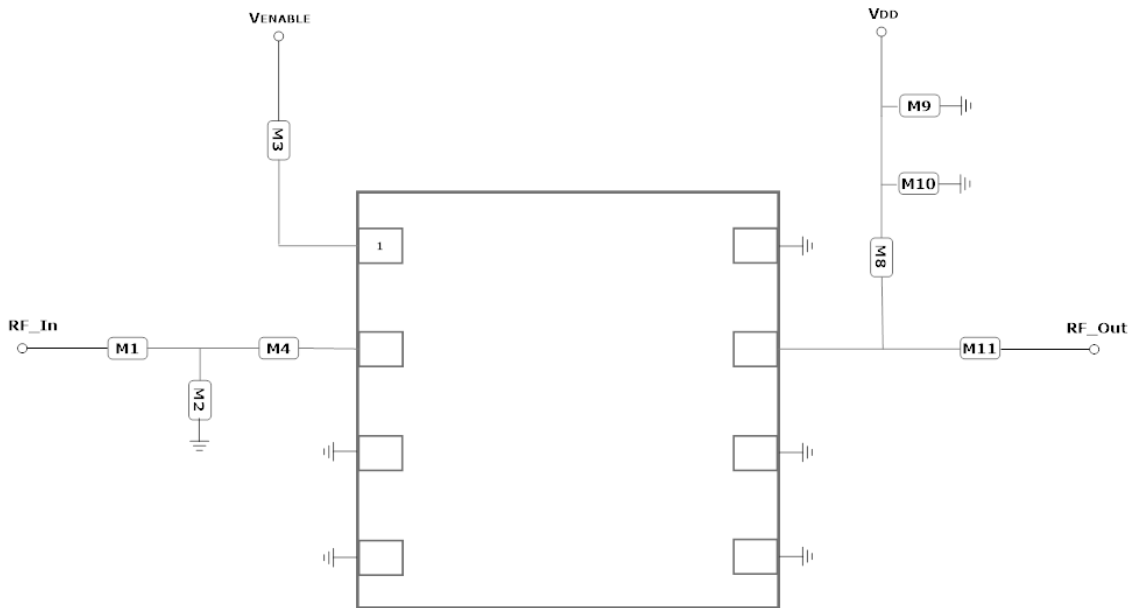
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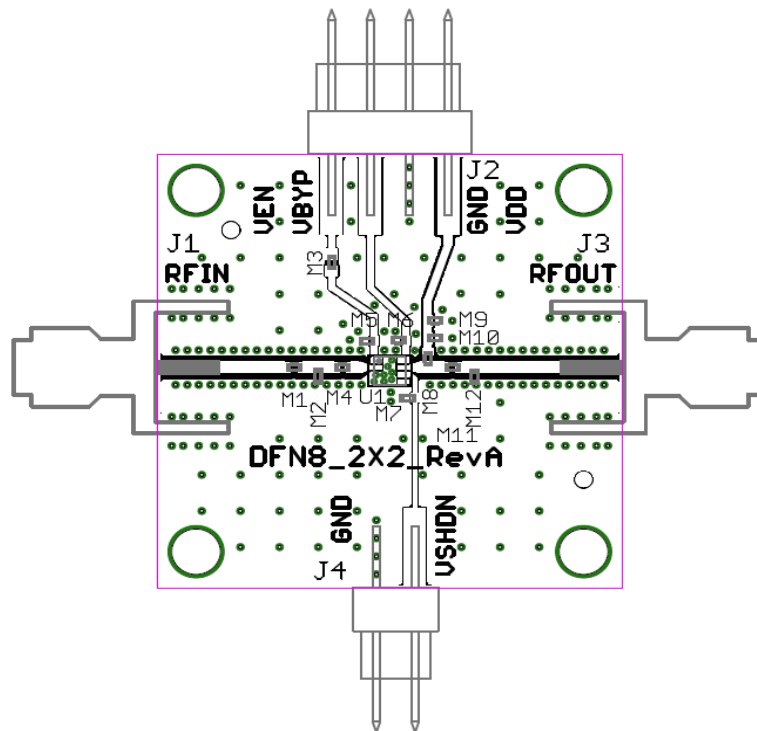
## GRF2072 Evaluation Board S-Parms: (2.5 to 2.7 GHz Match)



Note: Mu factor  $\geq 1.0$  implies unconditional stability.



GRF2072 Application Schematic



GRF2072 EVB Assembly Drawing





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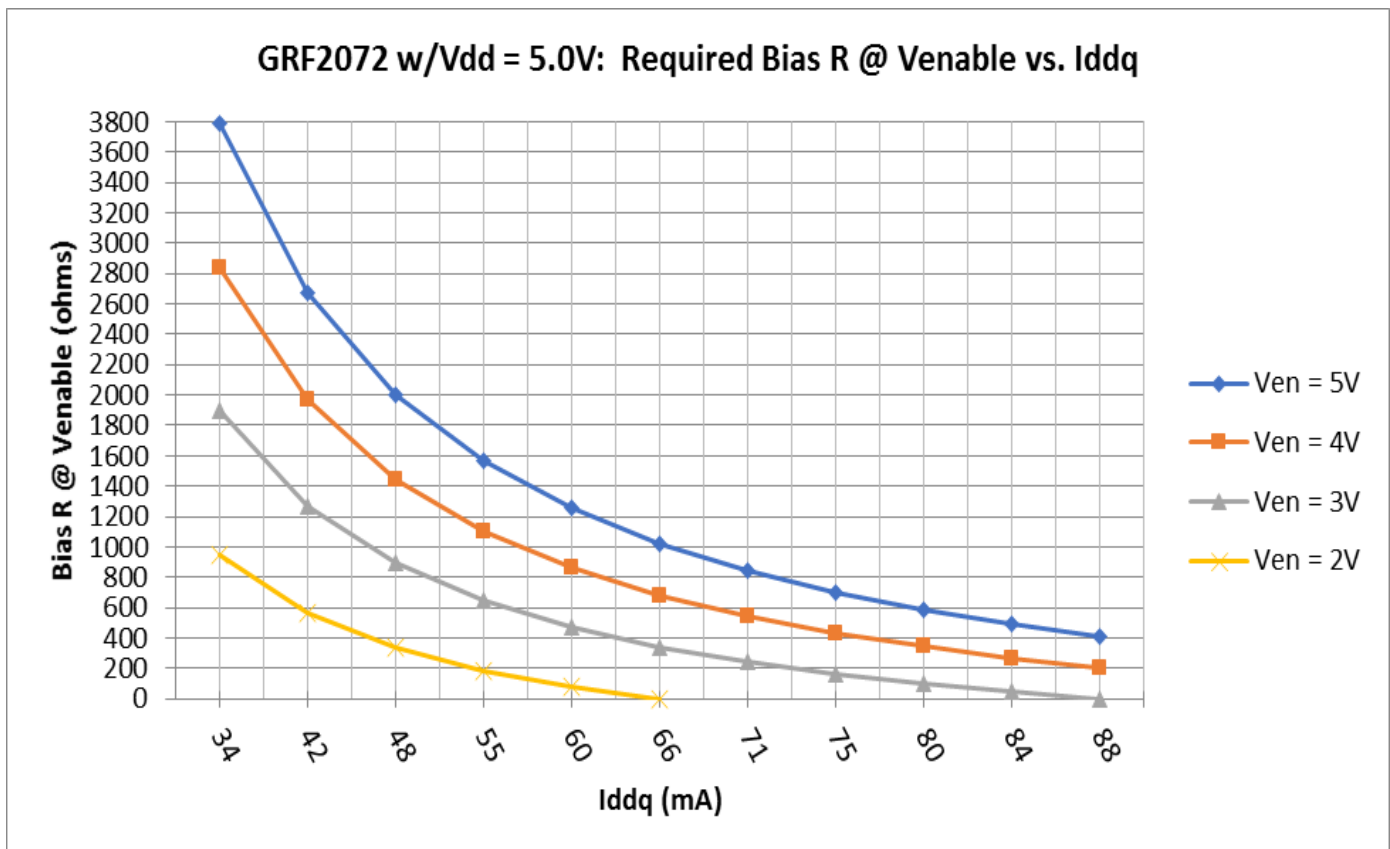
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## GRF2072 Standard Evaluation Board BOM: (2.5 to 2.7 GHz Tune)

Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M1	Capacitor	Murata	GJM	20 pF	0402	ok
M2	Inductor	Murata	LQG	2.7 nH	0402	ok
M3 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M4	Capacitor	Murata	GRM	2.2 pF	0402	ok
M8	Inductor	Murata	LQG	2.7 nH	0402	ok
M9	Capacitor	Murata	GRM	0.1 uF	0402	ok
M10	Capacitor	Murata	GRM	100 pF	0402	ok
M11	Capacitor	Murata	GRM	2.0 pF	0402	ok
Evaluation Board	DFN8_2x2_RevA	—	—	—	—	—

## GRF2072 Bias Resistor Selection Curves:





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Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

Information in this datasheet is specific to the Guerrilla RF, Inc. ("Guerrilla RF") product identified.

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