

SMD Power Inductor CDR74B



Description

- Ferrite drum core construction.
- Magnetically shielded.
- L × W × H: 8.15 × 7.35 × 4.9 mm Max.
- Product weight: 75mg(Ref.)
- Moisture Sensitivity Level: 1
- RoHS compliance.

Environmental Data

- Operating temperature range: -25°C~+80°C (including coil's self temperature rise)
- Storage temperature range: -25°C~+80°C
- Solder reflow temperature: 260 °C peak.

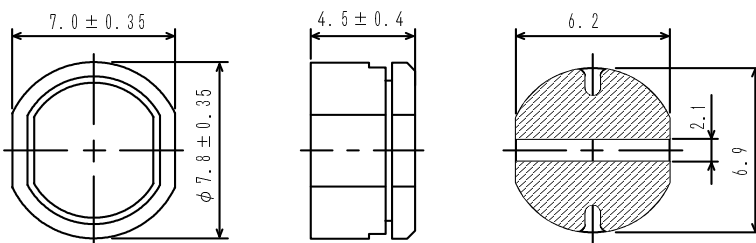
Packaging

- Carrier tape and reel packaging
- 12.9" diameter reel
- 1000pcs per reel

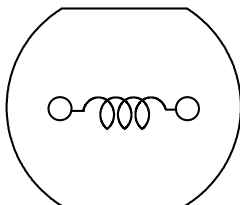
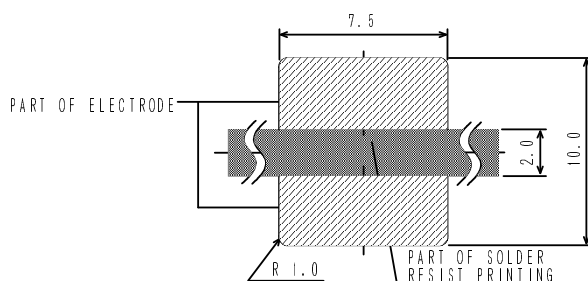
Applications

- Ideally used in Notebook PC ,LCD TV ,Game machine ,HDD ,DSC/DVC , etc as DC-DC converter inductors.

Dimension - [mm]



Land pattern and Schematics - [mm]



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Electrical Characteristics

Part name	Stamp	Inductance (μ H) [within] ※1	D.C.R.(Ω) MAX. (at 20°C)	Rated Current (A) ※2
CDR74BNP-100MC	100	10 $\pm$ 20%	0.07	1.65
CDR74BNP-120MC	120	12 $\pm$ 20%	0.07	1.57
CDR74BNP-150MC	150	15 $\pm$ 20%	0.08	1.39
CDR74BNP-180MC	180	18 $\pm$ 20%	0.10	1.29
CDR74BNP-220MC	220	22 $\pm$ 20%	0.13	1.12
CDR74BNP-270MC	270	27 $\pm$ 20%	0.16	1.06
CDR74BNP-330LC	330	33 $\pm$ 15%	0.18	0.97
CDR74BNP-390LC	390	39 $\pm$ 15%	0.18	0.91
CDR74BNP-470LC	470	47 $\pm$ 15%	0.27	0.80
CDR74BNP-560LC	560	56 $\pm$ 15%	0.29	0.76
CDR74BNP-680LC	680	68 $\pm$ 15%	0.33	0.68
CDR74BNP-820LC	820	82 $\pm$ 15%	0.43	0.62
CDR74BNP-101KC	101	100 $\pm$ 10%	0.49	0.55
CDR74BNP-121KC	121	120 $\pm$ 10%	0.68	0.49
CDR74BNP-151KC	151	150 $\pm$ 10%	0.94	0.44
CDR74BNP-181KC	181	180 $\pm$ 10%	1.00	0.40
CDR74BNP-221KC	221	220 $\pm$ 10%	1.18	0.36
CDR74BNP-271KC	271	270 $\pm$ 10%	1.30	0.33

※1. Inductance measuring frequency: 10 μ H $\sim$ 82 μ H ; at 2.52 MHz
100 μ H $\sim$ 270 μ H ; at 1 kHz

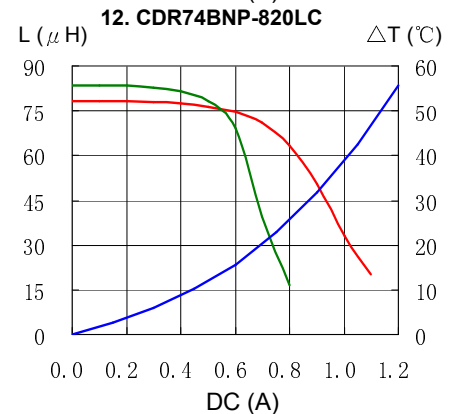
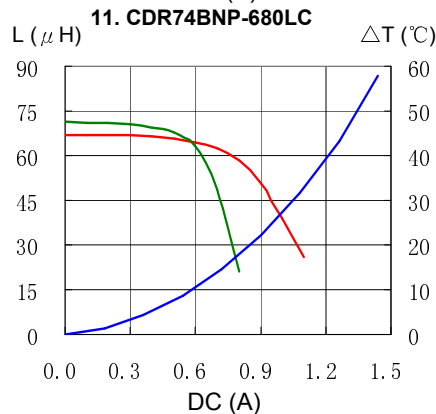
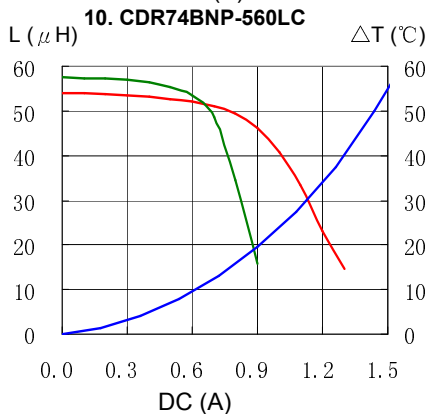
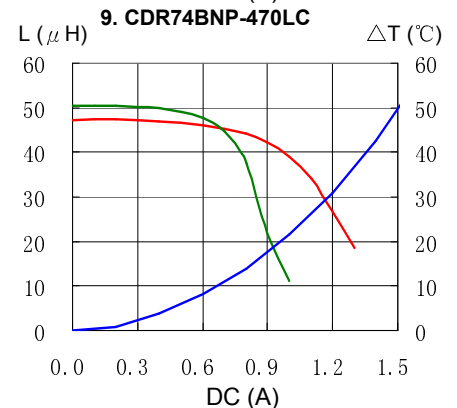
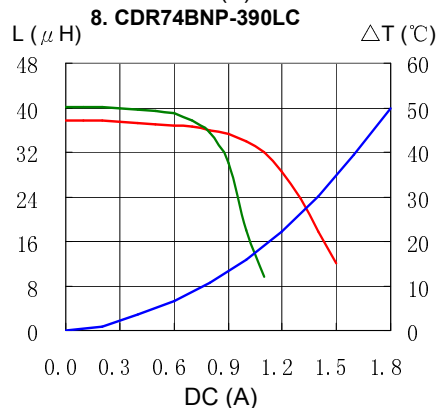
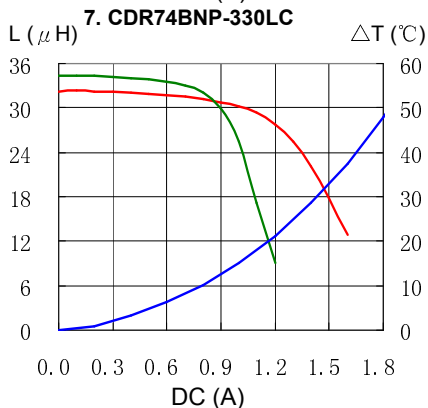
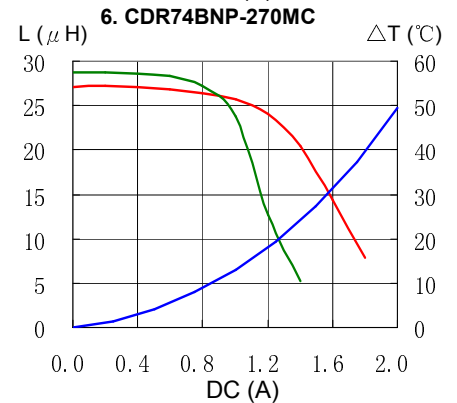
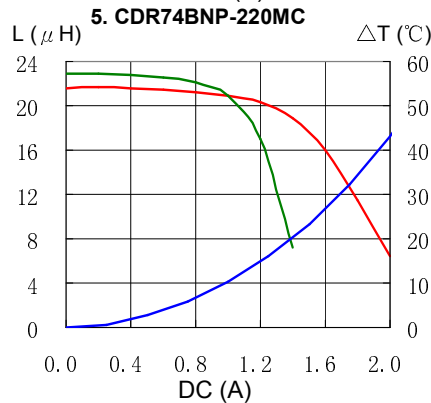
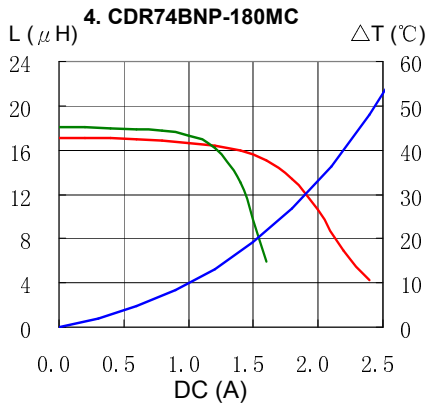
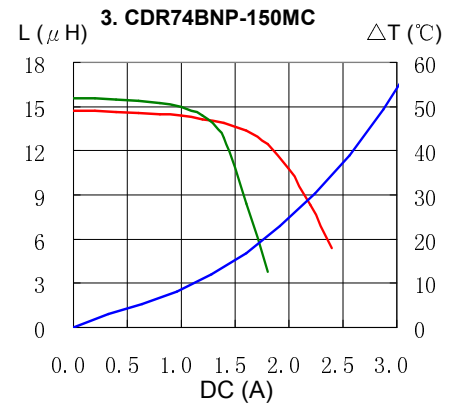
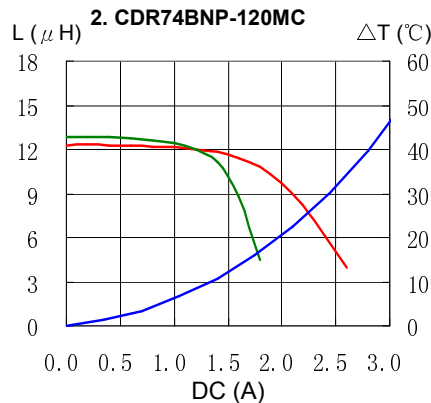
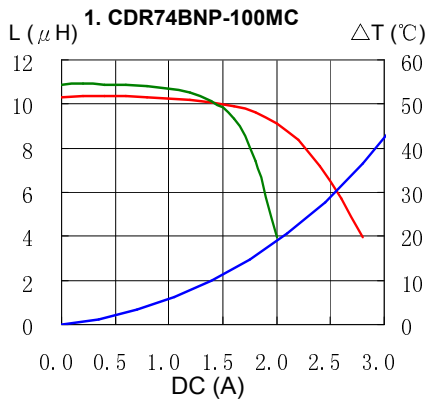
※2. Rated current: The D.C. current at which the inductance decreases to 90% of it's nominal value or when $\Delta t=40^{\circ}\text{C}$, whichever is lower (Ta=20°C).

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Saturation Current & Temperature Rise Graph

— L (20°C) — L (105°C) — ΔT

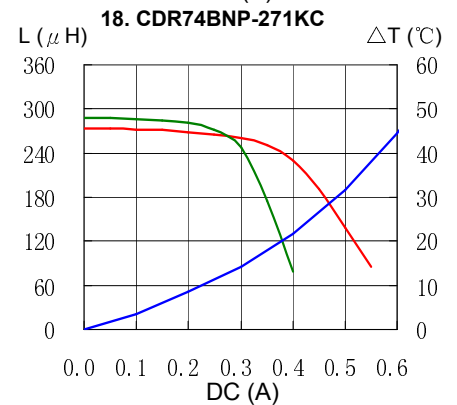
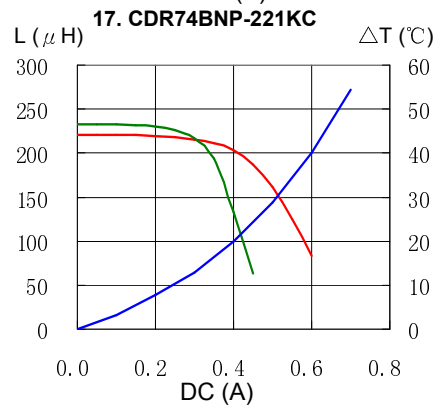
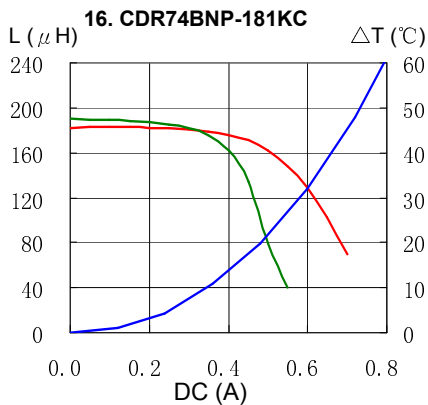
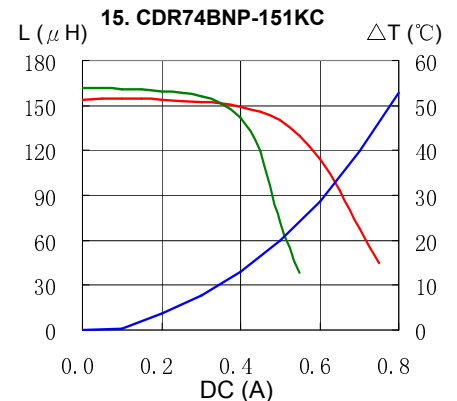
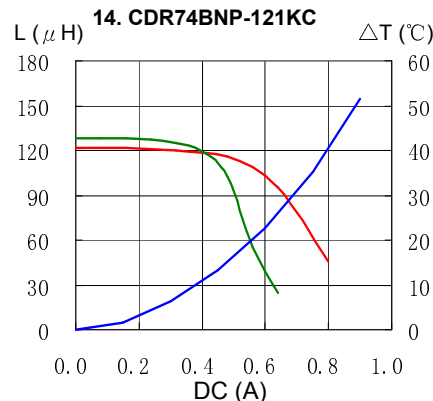
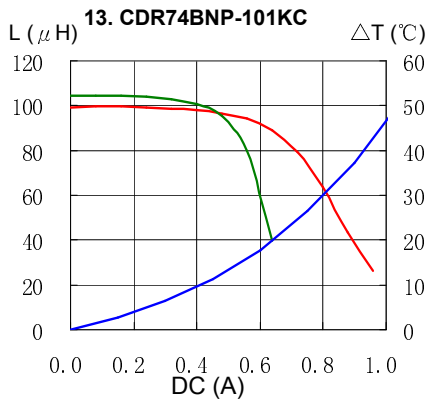


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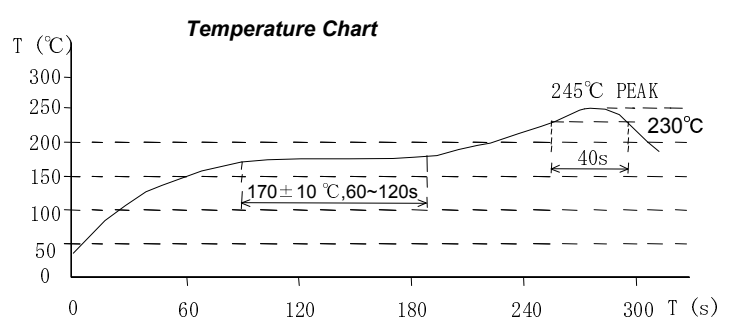
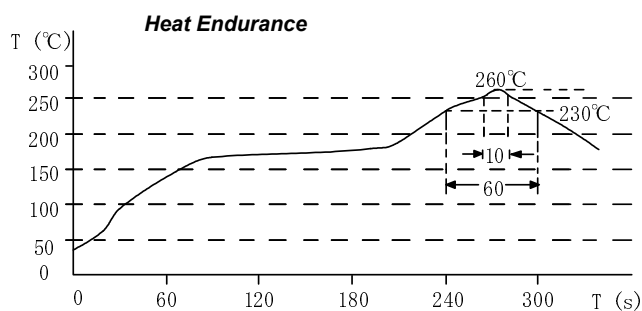


Saturation Current & Temperature Rise Graph

— L (20°C) — L (105°C) — ΔT



Solder Reflow Condition



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