

# Silicon Carbide (SiC) MOSFET - EliteSiC, 28 mohm, 1700 V, M1, TO-247-4L NTH4L028N170M1

## **Features**

- Typ.  $R_{DS(on)} = 28 \text{ m}\Omega$  @  $V_{GS} = 20 \text{ V}$
- Ultra Low Gate Charge  $(Q_{G(tot)} = 200 \text{ nC})$
- High Speed Switching with Low Capacitance (Coss = 200 pF)
- 100% Avalanche Tested
- These Devices are Pb-Free and are RoHS Compliant

## **Typical Applications**

- UPS
- DC-DC Converter
- Boost Converter

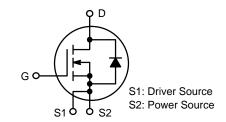
## **MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted)

Param	neter		Symbol	Value	Unit
Drain-to-Source Voltage		$V_{DSS}$	1700	V	
Gate-to-Source Voltage			$V_{GS}$	-15/+25	V
	ecommended Operation Values Gate–to–Source Voltage		$V_{GSop}$	-5/+20	V
Continuous Drain Current (Note 1)	Steady State	T <sub>C</sub> = 25°C	I <sub>D</sub>	81	Α
Power Dissipation (Note 1)			P <sub>D</sub>	535	W
Continuous Drain Current (Note 1)	Steady State	T <sub>C</sub> = 100°C	I <sub>D</sub>	57	Α
Power Dissipation (Note 1)			P <sub>D</sub>	267	W
Pulsed Drain Current (Note 2)	T <sub>C</sub> = 25°C		I <sub>DM</sub>	363	Α
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C	
Source Current (Body Diode)		I <sub>S</sub>	124	Α	
Single Pulse Drain-to-Source Avalanche Energy (I <sub>L(pk)</sub> = 30 A, L = 1 mH) (Note 3)		E <sub>AS</sub>	450	mJ	
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)		T <sub>L</sub>	300	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. EAS of 450 mJ is based on starting  $T_J = 25^{\circ}\dot{C}$ ; L = 1 mH,  $I_{AS} = 30$  A,  $V_{DD} = 120$  V,  $V_{GS} = 18$  V.

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub> MAX	I <sub>D</sub> MAX
1700 V	40 mΩ @ 20 V	81 A



**N-CHANNEL MOSFET** 



## **MARKING DIAGRAM**



H4L028N170M1 = Specific Device Code

A = Assembly Location

Y = Year WW = Work Week ZZ = Lot Traceability

## **ORDERING INFORMATION**

Device	Package	Shipping
NTH4L028N170M1	TO-247-4L	30 Units / Tube

## THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Note 1)	$R_{\theta JC}$	0.28	°C/W

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$		1700	_	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>	I <sub>D</sub> = 1 mA, referenced to 25°C		-	0.46	1	V/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{GS} = 0 \text{ V}, \qquad T_{J} = 25^{\circ}\text{C}$		-	-	100	μΑ
		V <sub>DS</sub> = 1700 V	T <sub>J</sub> = 175°C	_	-	1	mA
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{GS} = +25/-15 \text{ V},$	V <sub>DS</sub> = 0 V	-	-	±1	μΑ
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D =$	= 20 mA	1.8	2.75	4.3	٧
Recommended Gate Voltage	$V_{GOP}$			-5	-	+20	٧
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	$V_{GS} = 20 \text{ V}, I_D = 60 \text{ A}$	A, T <sub>J</sub> = 25°C	-	28	40	mΩ
		V <sub>GS</sub> = 20 V, I <sub>D</sub> = 60 A	A, T <sub>J</sub> = 175°C	-	57	-	
Forward Transconductance	9FS	$V_{DS} = 20 \text{ V}, I_{D}$	= 60 A	-	31	-	S
CHARGES, CAPACITANCES & GATE RES	ISTANCE						
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz, } V_{DS} = 800 \text{ V}$		-	4230	-	pF
Output Capacitance	C <sub>OSS</sub>			-	200	-	
Reverse Transfer Capacitance	C <sub>RSS</sub>			-	10	_	
Total Gate Charge	Q <sub>G(TOT)</sub>	$V_{GS} = -5/20 \text{ V}, V_{DS} = 800 \text{ V},$ $I_{D} = 60 \text{ A}$		-	200	-	nC
Gate-to-Source Charge	$Q_{GS}$			-	77	-	
Gate-to-Drain Charge	$Q_{GD}$			-	46	-	
Gate-Resistance	$R_{G}$	f = 1 MHz		-	5.8	-	Ω
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS} = -5/20$	0 V,	-	47	-	ns
Rise Time	t <sub>r</sub>	$V_{DS} = 1200 \text{ V},$ $I_{D} = 60 \text{ A},$		-	18	-	
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$R_G = 2 \Omega$ inductive lo	$R_G = 2 \Omega$		121	-	
Fall Time	t <sub>f</sub>	illuuctive loau		-	13	-	
Turn-On Switching Loss	E <sub>ON</sub>			-	1311	-	μJ
Turn-Off Switching Loss	E <sub>OFF</sub>			-	683	-	
Total Switching Loss	E <sub>tot</sub>			-	1994	-	
SOURCE-DRAIN DIODE CHARACTERIST	ICS						
Continuous Source-Drain Diode Forward Current	I <sub>SD</sub>	$V_{GS} = -5 \text{ V}, T_J = 25^{\circ}\text{C}$		-	-	124	Α
Pulsed Source–Drain Diode Forward Current (Note 2)	I <sub>SDM</sub>			-	-	363	
Forward Diode Voltage	$V_{SD}$	$V_{GS} = -5 \text{ V}, I_{SD} = 60$	A, T <sub>J</sub> = 25°C	-	4.3	-	V
Reverse Recovery Time	t <sub>RR</sub>	$V_{GS} = -5/20 \text{ V, } I_{SD} = 60 \text{ A,}$ $dI_{S}/dt = 1000 \text{ A/}\mu\text{s}$		-	34	-	ns
Reverse Recovery Charge	$Q_{RR}$			_	263	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

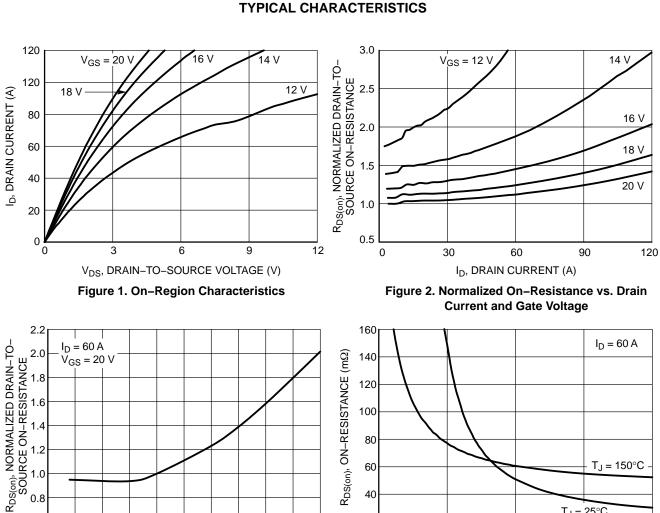


Figure 3. On-Resistance Variation with **Temperature** 

50 75

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

100 125 150 175

25

0.8

0.6 -75 -50

-25 0

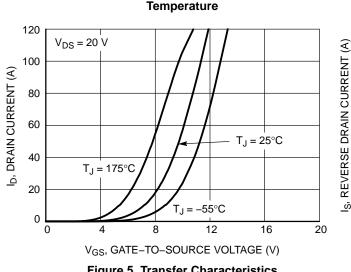


Figure 5. Transfer Characteristics

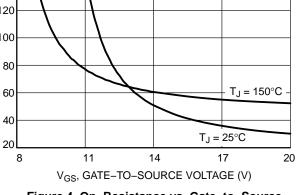
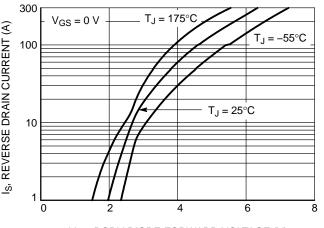


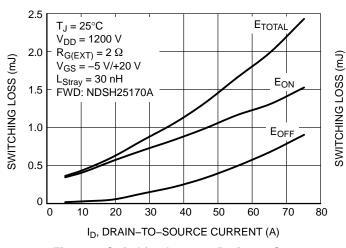
Figure 4. On-Resistance vs. Gate-to-Source Voltage



V<sub>SD</sub>, BODY DIODE FORWARD VOLTAGE (V)

Figure 6. Diode Forward Voltage vs. Current

## **TYPICAL CHARACTERISTICS**



3.0  $T_{.1} = 125^{\circ}C$ V<sub>DD</sub> = 1200 V E<sub>TOTAL</sub> 2.5  $R_{G(EXT)} = 2 \Omega$  $V_{GS} = -5 \text{ V/+20 V}$ 2.0 L<sub>Stray</sub> = 30 nH FWD: NDSH25170A  $E_{ON}$ 1.5 1.0 **E**OFF 0.5 0 40 ID, DRAIN-TO-SOURCE CURRENT (A)

Figure 7. Switching Loss vs. Drain-to-Source Current (25°C)

Figure 8. Switching Loss vs. Drain-to-Source Current (125°C)

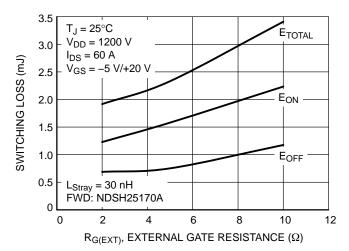
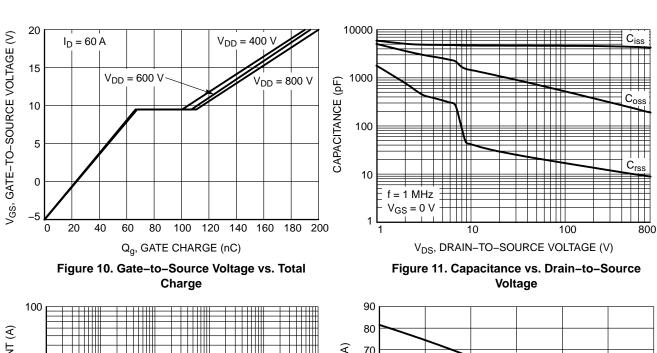
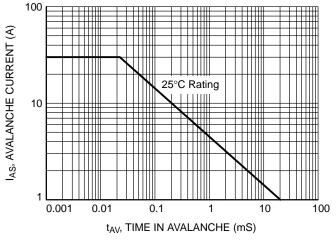
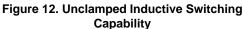


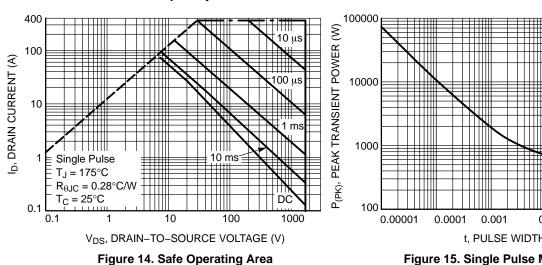
Figure 9. Switching Loss vs. External Gate Resistance

## **TYPICAL CHARACTERISTICS**









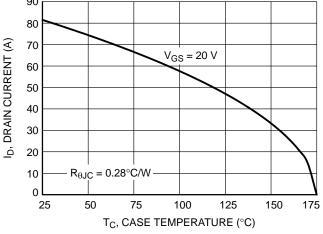
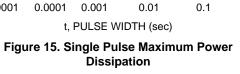


Figure 13. Maximum Continuous Drain Current vs. Case Temperature

Single Pulse

T<sub>C</sub> = 25°C

 $R_{\theta JC} = 0.28^{\circ}C/W$ 



# **TYPICAL CHARACTERISTICS**

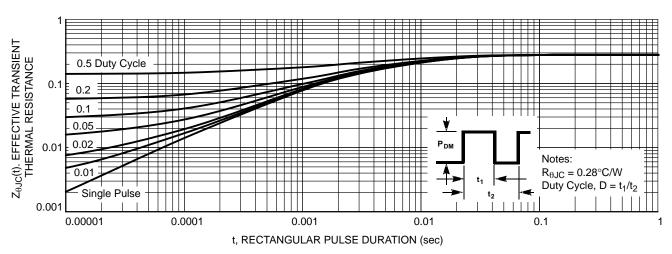


Figure 16. Junction-to-Case Thermal Response

 $\emptyset$ p1

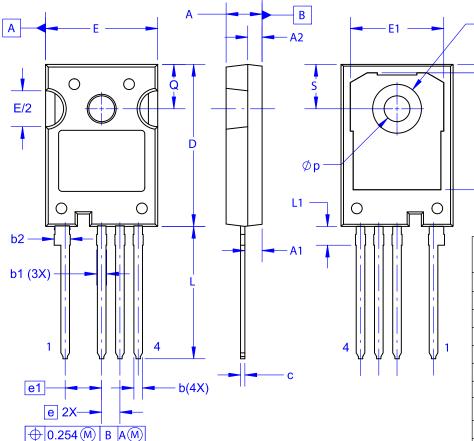
D1

D2



## TO-247-4LD CASE 340CJ **ISSUE A**

**DATE 16 SEP 2019** 



# NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
  B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
  FLASH, AND TIE BAR EXTRUSIONS.
  C. ALL DIMENSIONS ARE IN MILLIMETERS.
  D. DRAWING CONFORMS TO ASME Y14.5-2009.

DIM	MIL	LIMETER	S		
DIM	MIN	NOM	MAX		
Α	4.80	5.00	5.20		
A1	2.10	2.40	2.70		
A2	1.80	2.00	2.20		
b	1.07	1.20	1.33		
b1	1.20	1.40	1.60		
b2	2.02	2.22	2.42		
С	0.50	0.60	0.70		
D	22.34	22.54	22.74		
D1	16.00	16.25	16.50		
D2	0.97	1.17	1.37		
е	2.54 BSC				
e1	5	5.08 BSC			
E	15.40	15.60	15.80		
E1	12.80	13.00	13.20		
E/2	4.80	5.00	5.20		
L	18.22	18.42	18.62		
L1	2.42	2.62	2.82		
р	3.40	3.60	3.80		
p1	6.60	6.80	7.00		
Q	5.97	6.17	6.37		
S	5.97	6.17	6.37		

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