

Features

- High Density Cell Design For Low R_{DS(ON)}
- · Voltage Controlled Small Signal Switch
- · High Saturation Current Capability
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- ESD Protected up to 2KV (HBM)
- Halogen Free. "Green" Device (1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

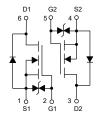
Maximum Ratings

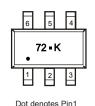
- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 416°C/W Junction to Ambient

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V _{DS}	60	V	
Gate-Source Voltage	V _{GS}	±20	V	
Drain Current	I _D	340	mA	
Total Power Dissipation	P _D	300	mW	

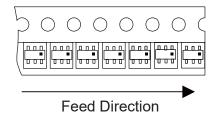
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Internal Structure and Marking Code



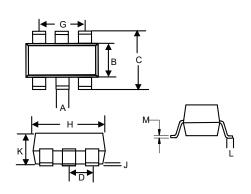


Special packing as below



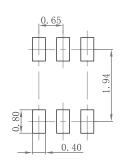
DUAL N-CHANNEL MOSFET





DIMENSIONS						
DIM	INCHES		MM		NOTE	
	MIN	MAX	MIN	MAX	NOTE	
Α	0.006	0.014	0.15	0.35		
В	0.045	0.053	1.15	1.35		
С	0.079	0.096	2.00	2.45		
D	0.026		0.65		TYP.	
G	0.047	0.055	1.20	1.40		
Н	0.071	0.087	1.80	2.20		
J		0.004		0.10		
K	0.031	0.043	0.80	1.10		
L	0.010	0.018	0.26	0.46		
M	0.003	0.006	0.08	0.15		

SUGGESTED SOLDER PAD LAYOUT





ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

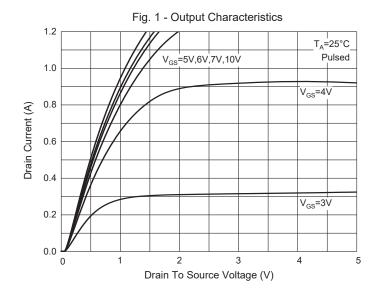
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V	
Gate-Threshold Voltage ⁽²⁾	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =1mA	1.0		2.5	V	
Gate-Body Leakage	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±10	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =48V, V _{GS} =0V			1	μA	
Drain-Source On-Resistance ⁽²⁾	В	V _{GS} =10V, I _D =500mA			3.0	Ω	
	R _{DS(on)}	V _{GS} =4.5V, I _D =200mA			4.0		
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =300mA			1.3	V	
Recovered Charge	Q _r	V_{GS} =0V, I_S =300mA, V_R =25V, dls/dt=-100A/ μ s		30		nC	
Dynami Characteristics ⁽³⁾							
Input Capacitance	C _{iss}				40	pF	
Output Capacitance	C _{oss}	V _{DS} =10V,V _{GS} =0V, f=1MHz			30		
Reverse Transfer Capacitance	C _{rss}				10		
Switching Characteristics ⁽³⁾							
Turn-On Delay Time	t _{d(on)}	V_{DD} =50V, V_{GS} =10V, R_{L} =250 Ω ,			20		
Turn-Off Delay Time	t _{d(off)}	R_{GS} =50 Ω , R_{GEN} =50 Ω			40	ns	
Reverse Recovery Time	t _{rr}	V_{GS} =0V, I_{S} =300mA, V_{R} =25V, dls/dt=-100A/ μ s		30			
Gate-Source Zener Diode							
Gate-Source Breakdown Voltage	BV_{GSO}	I _{gs} =±1mA(Oper Drain)	±21.5		±30	V	

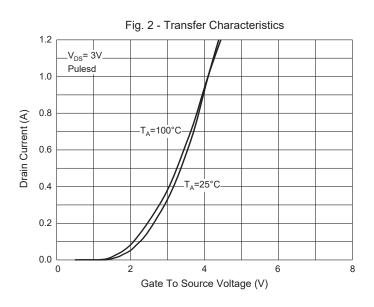
Note: 2. Pulse Test: Pulse Width ${\leqslant}300\mu\text{s},$ Duty Cycle ${\leqslant}2\%.$

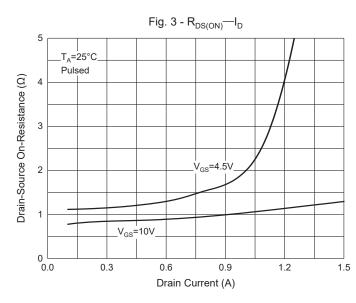
^{3.} These Parameters Have No Way to Verify.

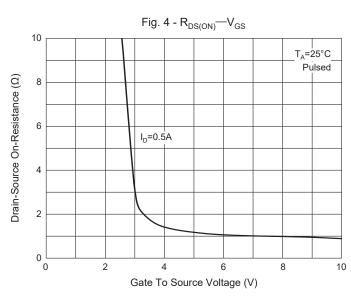


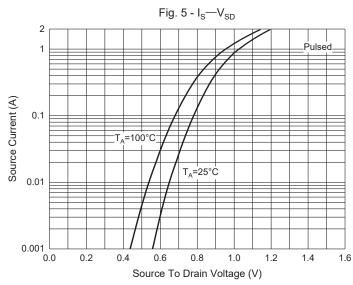
Curve Characteristics

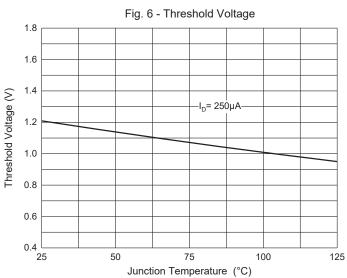














Ordering Information

Device	Packing	
Part Number-TPQ2	Tape&Reel:3Kpcs/Reel	

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