

EM6K31N-VB Datasheet Dual N-Channel 60 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)		
60	1.200 at V _{GS} = 10 V	0.3			
	1.300 at V _{GS} = 8 V	0.28	0.75		
	1.500 at V _{GS} = 4.5V	0.25	0.75		
	1.650 at V _{GS} = 2.5V	0.15			

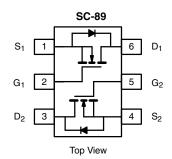
FEATURES

- TrenchFET[®] Power MOSFET
 100 % R_g Tested



APPLICATIONS

- Load/Power Switching for Portable Devices
- ٠ Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems •
- Power Supply Converter Circuits



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V _{GS}	± 12	v	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	0.30 ^{a, b}		
Continuous Drain Current $(T_J = 150 \text{ °C})^{-1}$	T _A = 70 °C		0.25 ^{a, b}	A	
Pulsed Drain Current		I _{DM}	2		
Continuous Source-Drain Diode Current	T _A = 25 °C	۱ _S	0.18 ^{a, b}	A	
	T _A = 25 °C	PD	0.22 ^{a, b}	w	
Maximum Power Dissipation ^a	T _A = 70 °C		0.14 ^{a, b}	vv	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient ^b	t ≤ 5 s	R _{thJA}	470	565	°C/W	
Maximum Junction-to-Ambient-	Steady State	' 'thJA	560	675	0/11	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. t = 5 s.

SPECIFICATIONS ($T_J = 25 \degree C$ Parameter	Symbol	Test Conditions	Min.	Typ	Max.	Unit		
	Symbol	Test Conditions	win.	Тур.	wax.	Unit		
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	60			V		
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		17		mV/°C		
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 1.8				
Gate-Source Threshold Voltage	V _{GS(th)}	$VDS = VGS$, $ID = 250 \ \mu A$		1.6		V		
Gate-Source Leakage	lass	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 30			
Calle Oburbe Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 4.5 V$			± 1	μΑ		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1			
Zero dale volage Diali ourient		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			3			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	2			А		
		V _{GS} = 10 V, I _D = 0.3A		1.200				
Drain-Source On-State Resistance ^a	Base	$V_{GS} = 8 V, I_{D} = 0.2 A$		1.300		Ω		
Drain Gource on Grate nesistance	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 0.2 \text{ A}$		1.500				
		V _{GS} = 2.5 V, I _D = 0.15A		1.650		1		
Forward Transconductance	9 _{fs}	$V_{\rm DS} = 10 \text{ V}, I_{\rm D} = 0.5 \text{ A}$		7.5		S		
Dynamic ^b								
Input Capacitance	C _{iss}			40		pF		
Output Capacitance	C _{oss}	V_{DS} = 10 V, V_{GS} = 0 V, f = 1 MHz		14				
Reverse Transfer Capacitance	C _{rss}			8				
Total Gate Charge	0	$V_{PQ} = 10 \text{ V} V_{QQ} = 8 \text{ V} \text{ I}_{P} = 0.6 \text{ A}$		1.3	2			
Iolai Gale Charge	Qg			0.75	1.2	nC		
Gate-Source Charge	Q _{gs}	V_{DS} = 10 V, V_{GS} = 4.5 V, I_{D} = 0.6 A		0.15		no		
Gate-Drain Charge	Q _{gd}			0.13				
Gate Resistance	R _g	f = 1 MHz	2.4	12.2	24.4	Ω		
Turn-On Delay Time	t _{d(on)}			11	20			
Rise Time	t _r	V_{DD} = 10 V, R_L = 20 Ω		16	24	ns		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 0.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		26	39			
Fall Time	t _f	-		11	20			
Drain-Source Body Diode Characteristics								
Pulse Diode Forward Currenta	I _{SM}				2	А		
Body Diode Voltage	V _{SD}	I _S = 0.5 A		0.8	1.2	V		
Body Diode Reverse Recovery Time	t _{rr}	-		10	15	ns		
Body Diode Reverse Recovery Charge	Q _{rr}			2	4	nC		
Reverse Recovery Fall Time	$l_{r} = 0.5 \text{ A } dl/dt = 10$			5				
Reverse Recovery Rise Time	t _b			5		ns		

Notes:

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

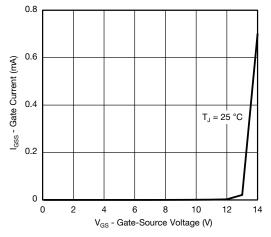
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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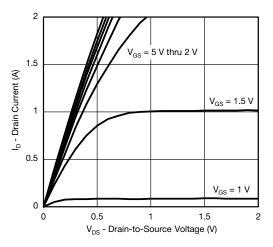
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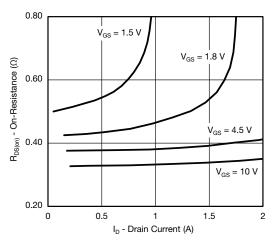
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



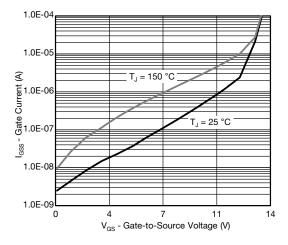




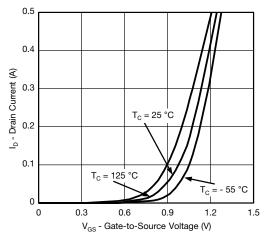




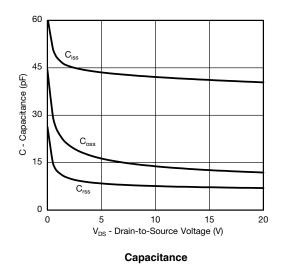
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage

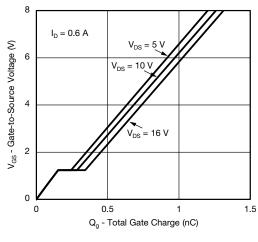


Transfer Characteristics

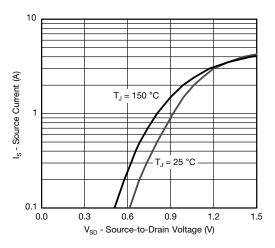




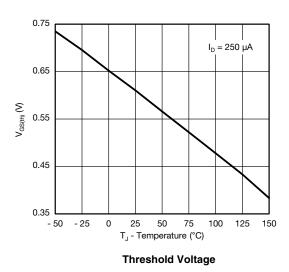
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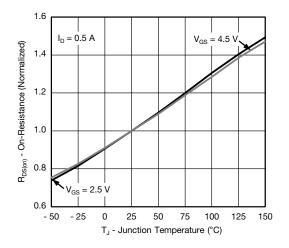


Gate Charge

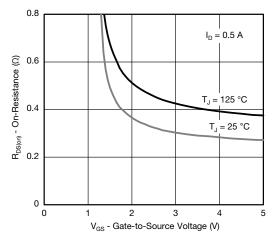


Soure-Drain Diode Forward Voltage

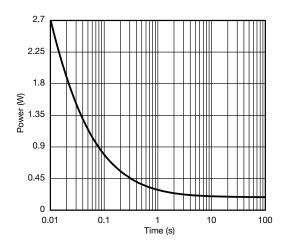




On-Resistance vs. Junction Temperature

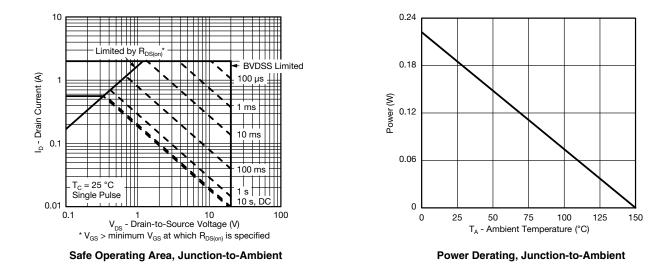


On-Resistance vs. Gate-to-Source Voltage



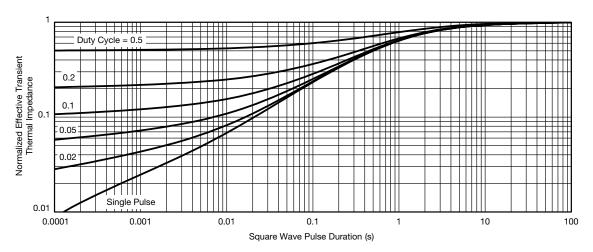
Single Pulse Power, Junction-to-Ambient





TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

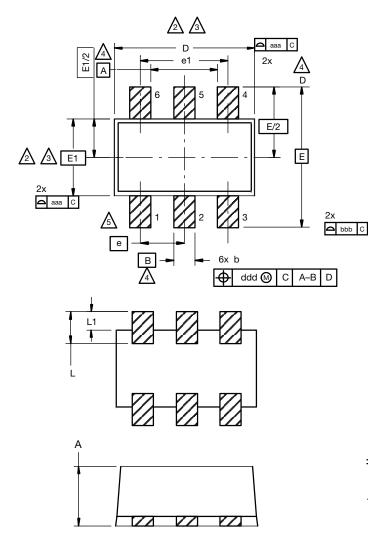
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Ambient

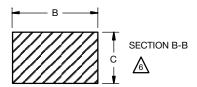


SC-89 6-Leads (SOT-563F)

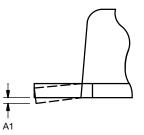


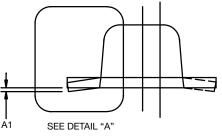
Notes

- 1. Dimensions in millimeters.
- Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.
- 3. Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.
- 4. Datums A, B and D to be determined 0.10 mm from the lead tip.
- 5. Terminal numbers are shown for reference only.
- 6. These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.





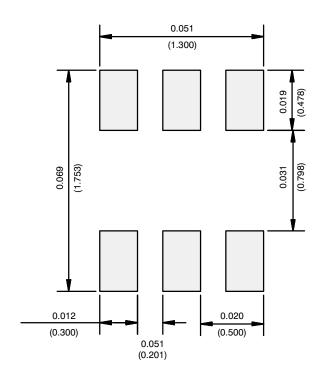




DIM.	MILLIMETERS				
	MIN.	NOM.	MAX.		
A	0.56	0.58	0.60		
A1	0	0.02	0.10		
b	0.15	0.22	0.30		
с	0.10	0.14	0.18		
D	1.50	1.60	1.70		
E	1.50	1.60	1.70		
E1	1.15	1.20	1.25		
е	0.45	0.50	0.55		
e1	0.95	1.00	1.05		
L	0.25	0.35	0.50		
L1	0.10	0.20	0.30		



RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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