

Specification for Approval

Date: 2021/08/16

Customer: 天诚科技

TAI-TECH P/N: HCB3216KF-600T40

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

REMARK:	
	Customer Approval Feedback
	西北臺慶科技股份有限公司
	TAI-TECH Advanced Electronics Co., Ltd

代理商:

■ 深圳市天诚科技有限公司 Shenzhen TsaSun Technology Co., Ltd. Room 209, 2/F, Block A, Tengfei Industrial Building, No.6, Taohua Road, Futian District, Shenzhen TEL: 0755-8335 8885 / 0755-8335 9885 E-mail: sales@tsasun.com www.tsacoil.com

APPROVED	CHECKED
夏暁曼	夏暁曼

□ 西北臺慶科技股份有限公司
TAI-TECH Advanced Electronics Co., Ltd
Headquarter:
NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI,
TAO-YUAN HSIEN, TAIWAN, R.O.C.
TEL: +886-3-4641148 FAX: +886-3-4643565
http://www.tai-tech.com.tw
E-mail: sales@tai-tech.com.tw

□ 臺慶精密電子(昆山)有限公司 TAI-TECH ADVANCED ELECTRONICS(KUNSHAN)CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA TEL: +86-512-57619396 FAX: +86-512-57619688 E-mail: hui@tai-tech.com.tw

R&D Center

Sales Dep.

APPROVED	CHECKED	DRAWN
羅宜春	梁周虎	卜文娟

TAI-TECH

High Current Ferrite Chip Bead(Lead Free)

HCB3216KF-600T40

1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. Available in various sizes.
- 6. Excellent solder ability and heat resistance.
- 7. High reliability.
- 8.100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.

Halogen-free Pb-free Certificate Green Partner

2.Dimensions



Chip Size						
A 3.20±0.20						
В	1.60±0.20					
С	1.10±0.20					
D	0.50±0.30					
Units: mm						

3.Part Numbering

HCB	<mark>3216</mark>	KF	-	<mark>600</mark>	Т	40
А	В	С		D	Е	F
A: Series B: Dimensi C: Material D: Impedar E: Packagi F: Rated C	nce ng	600=6	Free Marcon Ω Note the formula of Ω	iterial Reel, B=Bu	ılk(Bags)	



4.Specification

Tai-Tech	Impedance (Ω)	Test Frequency	DC Resistance	Rated Current
Part Number		(Hz)	(Ω) max.	(mA) max.
HCB3216KF-600T40	60±25%	60mV/100M	0.03	4000

• Rated current: based on temperature rise test



Impedance-Frequency Characteristics

5. Reliability and Test Condition

ltem	Performance							Те	st Con	dition	
Series No.	FCB FCM HCB	GHB FCA	FCI	FHI	FCH	HCI					
Operating Temperature	-40~+12 Including self-tem)		(Inc		+105℃ temperatur	e rise)					
Transportation Storage Temperature	-40~+12 (on boa				+105℃ board)			g storag ion Notic		ons, please	see the
Impedance (Z)			-				Agilent4	291			
Inductance (Ls)							Agilent I				
Q Factor	Refer to standard elec	trical character	istics list				Agilent4 Agilent1				
DC Resistance			131103 1131				Agilent 4				
Rated Current								er Supp Ited Curi		rements, the	ere will be
Temperature Rise Test	Rated Current < 1A $\Delta T 2$ Rated Current \geq 1A $\Delta T 4$	-					2. Temp			current. by digital s	urface
Resistance to Soldering Heat	Appearance : No dan Impedance : within±1 Inductance : within±1 Q : Shall not exceed RDC : within ±15% of	5% of initial valu 0% of initial valu the specification	ue value.	exceed the	specificatio	on value	Solder to Flux for Tempera rate: 25: Dip time Depth: c	Sn99.5% emperat lead free ature ran ±6 mm/s :: 10±1se complete	6-Cu0.5% ure: 260± e: Rosin. 9 mp/immer ec. ely cover t	5℃	
Solderability	More than 95% of the electrode should be with solder.		245°C	60	latural cooling 4±1 second		Solder to Flux for	Sn99.5% emperat lead free complete	%-Cu0.5% ure: 245± e: Rosin. 9 ely cover t	5℃	on.
Terminal strength	Appearance : No dan Impedance : within±1 Inductance : within±1 Q : Shall not exceed RDC : within ±15% of exceed the sp	5% of initial valu 0% of initial valu the specification	ue 1 value. d shall not	DUT , substrate	adus 0,5 mm press tool	wide thickness shear force	times.(I Reflow I Compor (>0805: device b for 60 applied	PC/JED Profiles) nent mou 1kg <=(peing tes +1 seco gradua	EC J-STE unted on 0805:0.5k ted. This t onds. Also	ough IR ref 0-020D Clas a PCB appi g)to the s force shall b o the force not to sh	sification y a force ide of a e applied shall be
Bending	Appearance : No dan Impedance : within±1 Inductance : within±1 Q : Shall not exceed RDC : within ±15% or	0% of initial valu 0% of initial valu the specification	ue value.	exceed the	specificatio	on value	following Bending	g dimens I depth:>	sions:>=0	8mm	x1.2mm
Vibration Test	Appearance : No dan Impedance : within±1 Inductance : within±1 Q : Shall not exceed RDC : within ±15% of	5% of initial valu 0% of initial valu the specification	ue value.	not exceed the specification value Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cy each of 3 orientations) •				sificatior			
Shock	Appearance : No dan Impedance : within±1 Inductance : within±1 Q : Shall not exceed RDC : within ±15% of	5% of initial valu 0% of initial valu the specification	ue value.	exceed the	specificatio	on value	Test co Type SMD Lead	Peak Value (g's) 1,500 100	Normal duration (D) (ms) 0.5 6	Wave form Half-sine Half-sine	Velocity change (Vi)ft/sec 15.4 12.3

Item	Performance	Test Condition		
Life test	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2℃ (bead), 85±2℃ (inductor) Applied current: rated current. Duration: 1000±12hrs. Measured at room temperature after placing for 24±2 hrs. Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles) Humidity: 85±2%R.H. Temperature: 85±2℃. Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±2 hrs.		
Thermal shock	Appearance: no damage. Impedance: within±15%of initial value. Inductance: within±10%of initial value. Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	$\begin{array}{l} \hline Preconditioning: Run through IR reflow for 2 \\ times.(IPC/JEDEC J-STD-020D Classification \\ Reflow Profiles) \\ \hline Condition for 1 cycle \\ Step 1: -40\pm2^{\circ} 30\pm5 \mbox{ min.} \\ Step 2: 25\pm2^{\circ} C \qquad \leqq 0.5 \mbox{min} \\ Step 3: +105\pm2^{\circ} C \qquad 30\pm5 \mbox{min.} \\ Number of cycles: 500 \\ Measured at room temperature after placing \\ for 24\pm2 \mbox{ hrs.} \\ \hline \end{array}$		
Insulation Resistance	IR>1GΩ	Chip Inductor Only Test Voltage:100±10%V for 30Sec.		

**Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85° C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



6.Soldering and Mounting

6-1. Recommended PC Board Pattern

			Pattern	••••				
Series	D(mm)	L(mm)	G(mm)	H(mm)				
	0603	0.6±0.03	0.30±0.03	0.30±0.03	0.15±0.05	0.80	0.30	0.30
FCB	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	1.50	0.40	0.55
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	2.60	0.60	0.80
HCB	0040	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30		1.00	1.00
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	3.00		
FCI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	4.40	2.20	1.40
FHI	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	4.40	2.20	3.40
FCH	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	5.70	2.70	1.40
HCI	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	5.90	2.57	4.22



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PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

TAI-TECH 6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools. Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2. • Preheat circuit and products to 150°C • Never contact the ceramic with the iron tip • Use a 20 watt soldering iron with tip diameter of 1.0mm

- Preheat circuit and products to 150°C
 Never contact the ceramic
 350°C tip temperature (max)
 Nomm tip diameter (max)
- vitn the iron tip Use a
 - Limit soldering time to 4~5sec.









6-2.3 Solder Volume:

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height

7.Packaging Information

7-1. Reel Dimension



7-2.1 Tape Dimension / 8mm

Material of taping is paper







Upper limit

Recommendable

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
060303	0.68±0.05	0.38±0.05	0.50max	2.0±0.05	0.50max
100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03

Ī	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
	160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
-	201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05

■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321611	3.35±0.10	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	1.0±0.10
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

7-2.2 Tape Dimension / 12mm



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.1	0.24±0.05	1.5±0.1
453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.1	0.24±0.05	1.5±0.1

7-3. Packaging Quantity

Chip Size	453215	451616	322513	321611	321609	201212	201209	160808	100505	060303
Chip / Reel	1000	2000	2500	3000	3000	2000	4000	4000	10000	15000
Inner box	4000	8000	12500	15000	15000	10000	20000	20000	50000	75000
Middle box	20000	40000	62500	75000	75000	50000	100000	100000	250000	375000
Carton	40000	80000	125000	150000	150000	100000	200000	200000	500000	750000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed		
(°C)	(%)	(hPa)	mm/min		
5~35	45~85	860~1060	300		

Application Notice

- Storage Conditions(component level)
 - To maintain the solder ability of terminal electrodes:
 - 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
 - 2. Temperature and humidity conditions: Less than 40 $^\circ\!\mathrm{C}$ and 60% RH.
 - 3. Recommended products should be used within 12 months from the time of delivery.
 - 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation

- 1.Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.