

## HM120N04D-VB Datasheet

### N-Channel 40 V (D-S) MOSFET

#### PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)	$Q_g$ (Typ.)
40	0.0010 at $V_{GS} = 10$ V	280	240 nC
	0.0012 at $V_{GS} = 4.5$ V	250	

#### FEATURES

- Trench Power MOSFET
- 100 %  $R_g$  and UIS Tested

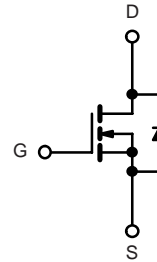
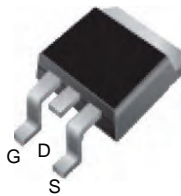
#### APPLICATIONS

- Synchronous Rectification
- Power Supplies



**RoHS**  
COMPLIANT

D<sup>2</sup>PAK (TO-263)



N-Channel MOSFET

#### ABSOLUTE MAXIMUM RATINGS $T_A = 25^\circ\text{C}$ , unless otherwise noted

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		$V_{DS}$	40	V
Gate-Source Voltage		$V_{GS}$	$\pm 25$	
Continuous Drain Current ( $T_J = 175^\circ\text{C}$ )	$T_C = 25^\circ\text{C}$	$I_D$	280	A
	$T_C = 70^\circ\text{C}$		220	
	$T_A = 25^\circ\text{C}$		229 <sup>b</sup>	
	$T_A = 70^\circ\text{C}$		223 <sup>b</sup>	
Pulsed Drain Current		$I_{DM}$	750	
Avalanche Current Pulse		$I_{AS}$	80	
Single Pulse Avalanche Energy		$E_{AS}$	320	V
Continuous Source-Drain Diode Current	$T_C = 25^\circ\text{C}$	$I_S$	110 <sup>a, c</sup>	A
	$T_A = 25^\circ\text{C}$		2.6 <sup>b</sup>	
Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	$P_D$	312 <sup>a</sup>	W
	$T_C = 70^\circ\text{C}$		200	
	$T_A = 25^\circ\text{C}$		3.13 <sup>b</sup>	
	$T_A = 70^\circ\text{C}$		2.0 <sup>b</sup>	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	- 55 to 150	$^\circ\text{C}$

#### THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	$R_{thJA}$	32	40	$^\circ\text{C/W}$
	Steady State	$R_{thJC}$	0.33	0.4	

Notes:

a. Based on  $T_C = 25^\circ\text{C}$ .

b. Surface Mounted on 1" x 1" FR4 board.

c. Calculated based on maximum junction temperature. Package limitation current is 110 A.

SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	45			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA		41		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			- 8		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.2		2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	120			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A		0.0010		Ω
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		0.0012		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A		180		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, f = 1 MHz		9335		pF
Output Capacitance	C <sub>oss</sub>			1150		
Reverse Transfer Capacitance	C <sub>rss</sub>			850		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		160	260	nC
Gate-Source Charge	Q <sub>gs</sub>			40		
Gate-Drain Charge	Q <sub>gd</sub>			22		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		0.85	1.3	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 20 V, R <sub>L</sub> = 1.0 Ω I <sub>D</sub> ≅ 20 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω		20	30	ns
Rise Time	t <sub>r</sub>			11	17	
Turn-Off Delay Time	t <sub>d(off)</sub>			77	115	
Fall Time	t <sub>f</sub>			10	15	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 20 V, R <sub>L</sub> = 1.0 Ω I <sub>D</sub> ≅ 20 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω		102	155	
Rise Time	t <sub>r</sub>			62	95	
Turn-Off Delay Time	t <sub>d(off)</sub>			180	270	
Fall Time	t <sub>f</sub>			60	90	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			110	A
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				200	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 20 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		50	75	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			70	105	nC
Reverse Recovery Fall Time	t <sub>a</sub>			30		ns
Reverse Recovery Rise Time	t <sub>b</sub>			20		

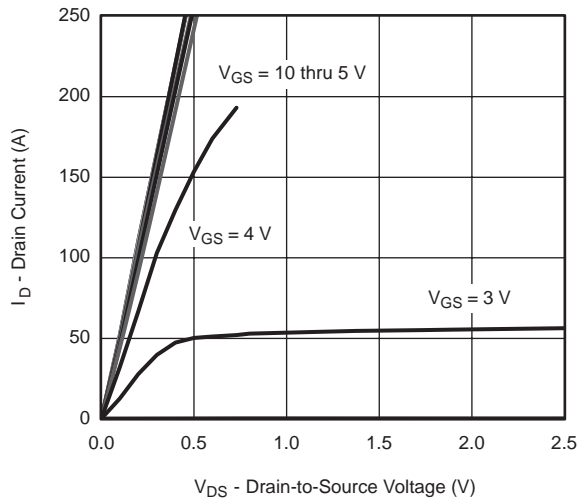
Notes:

a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

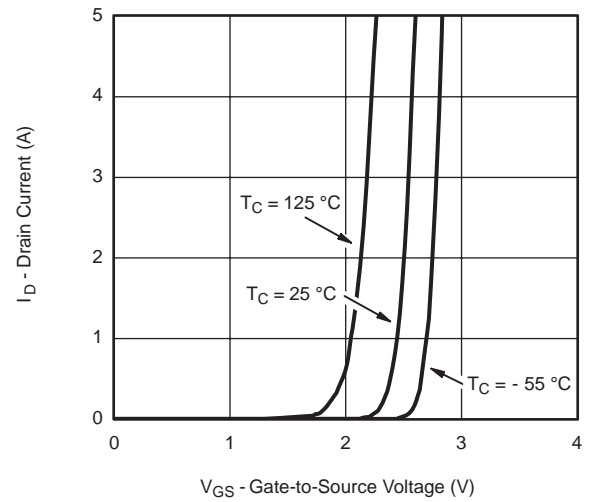
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

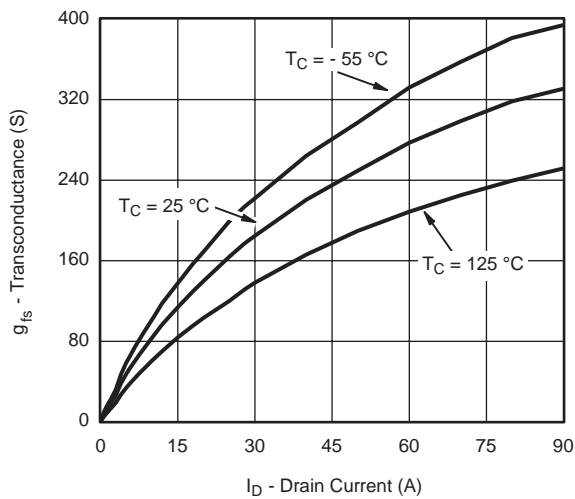
**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted



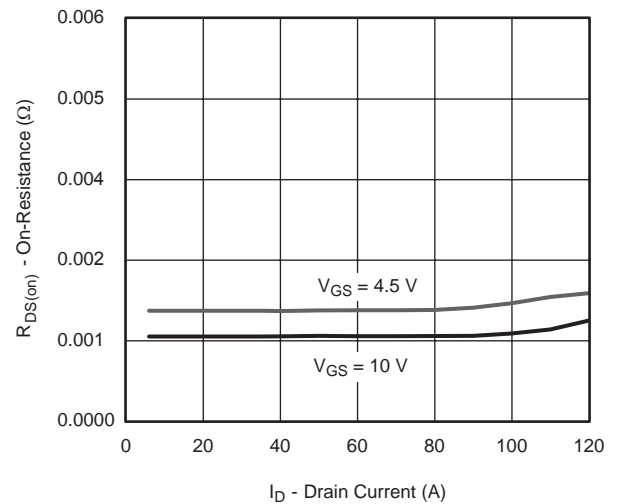
**Output Characteristics**



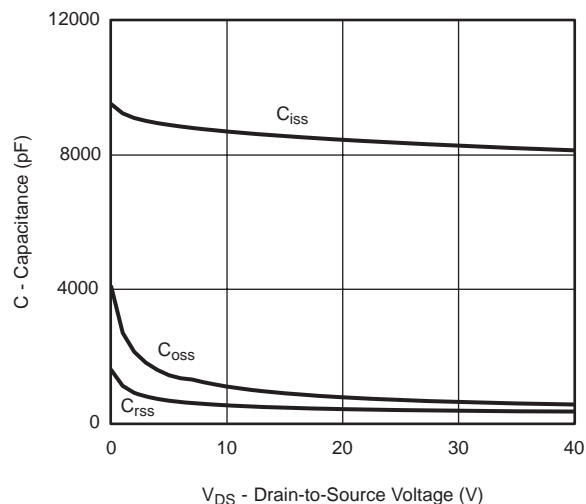
**Transfer Characteristics**



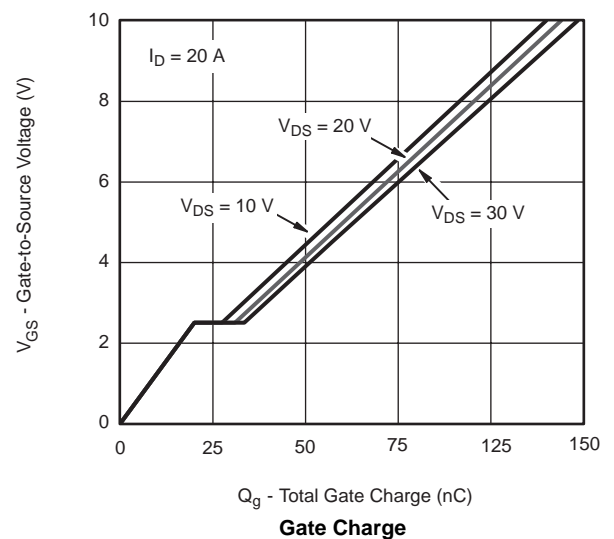
**Transconductance**



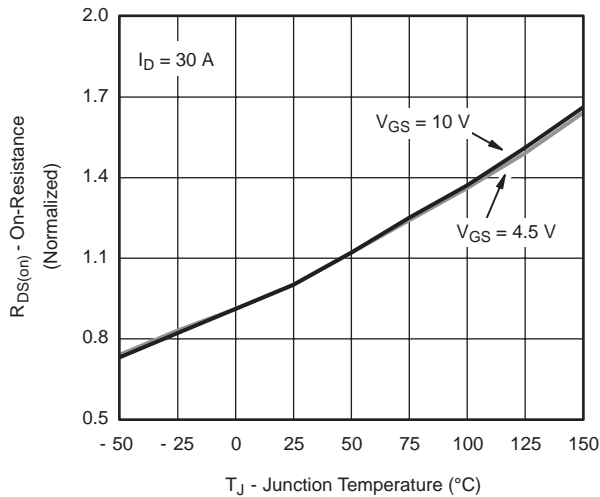
**On-Resistance vs. Drain Current**



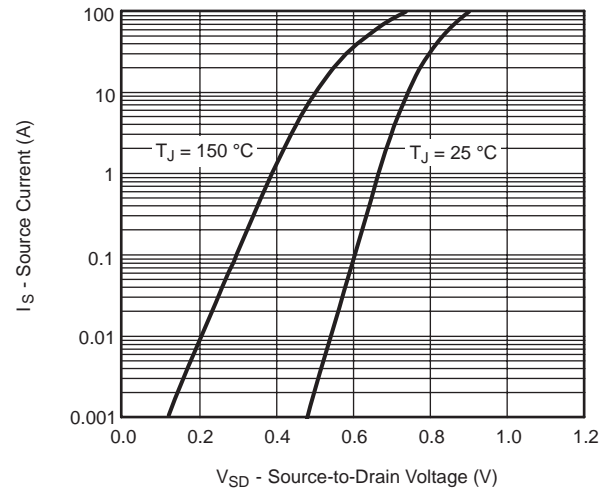
**Capacitance**



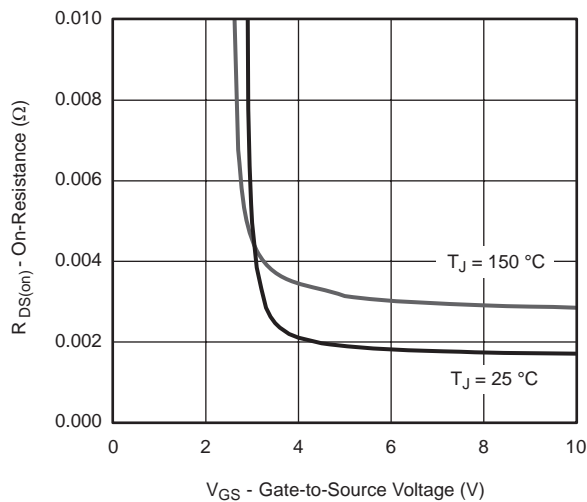
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



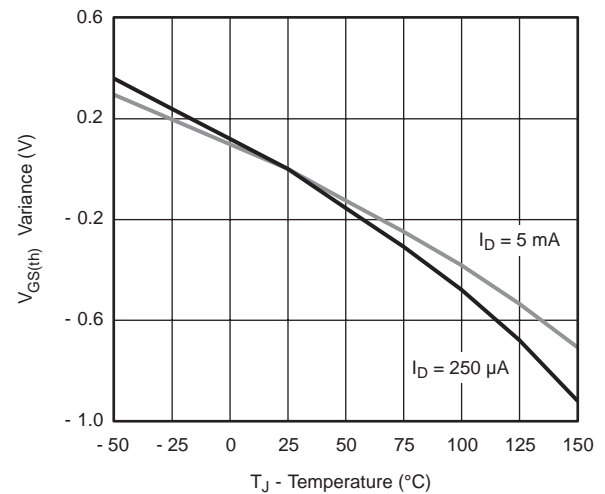
On-Resistance vs. Junction Temperature



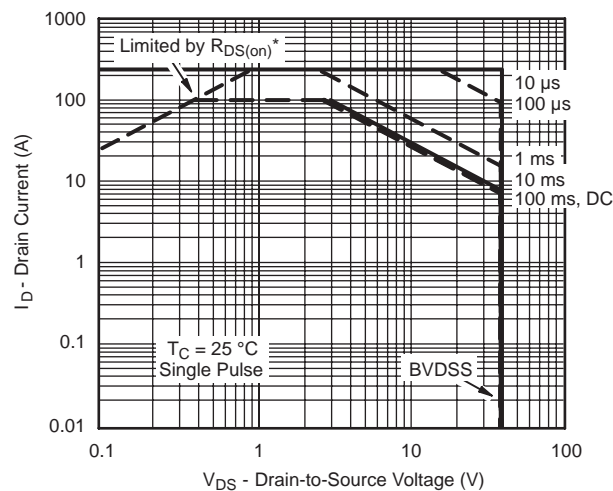
Forward Diode Voltage vs. Temperature



On-Resistance vs. Gate-to-Source Voltage



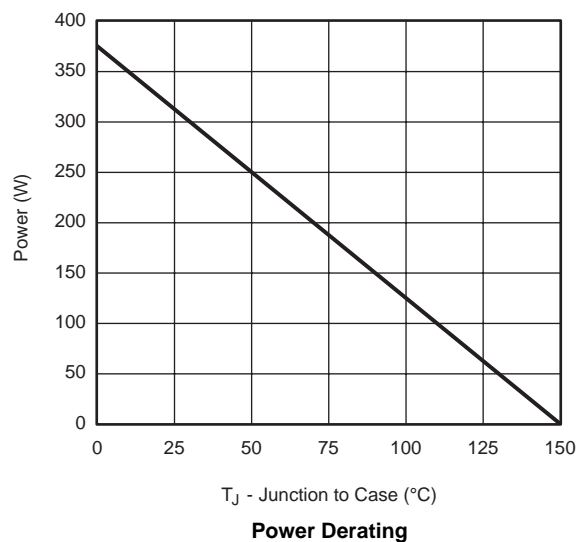
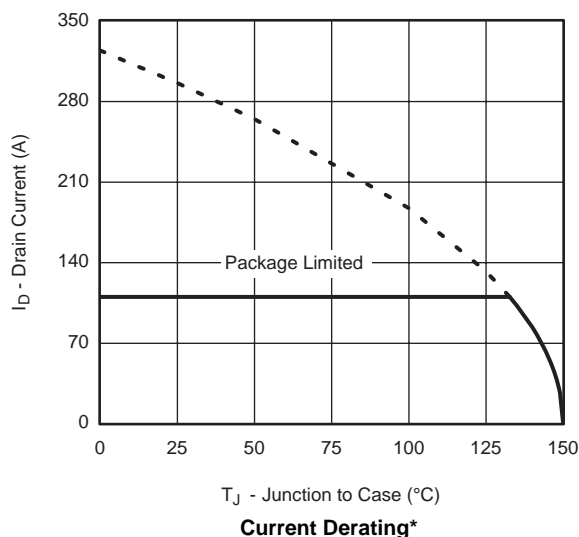
Threshold Voltage



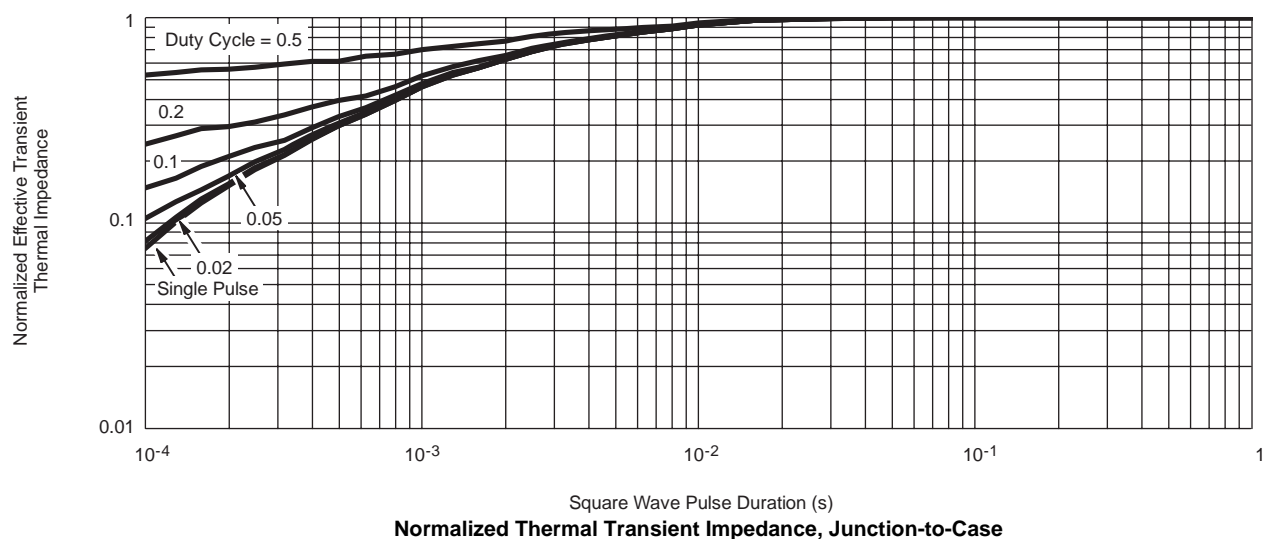
\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area, Junction-to-Ambient

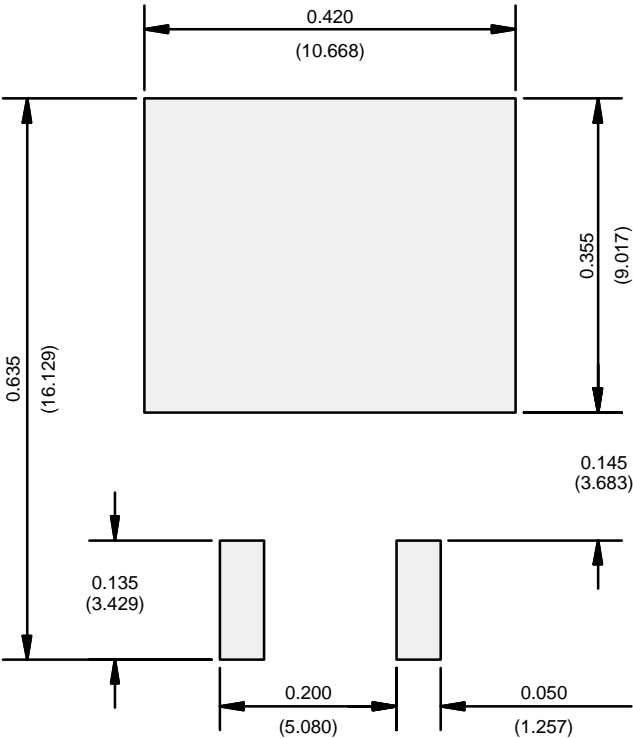
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150\text{ °C}$ , using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



**RECOMMENDED MINIMUM PADS FOR D<sup>2</sup>PAK: 3-Lead**



Recommended Minimum Pads  
Dimensions in Inches/(mm)

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