TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

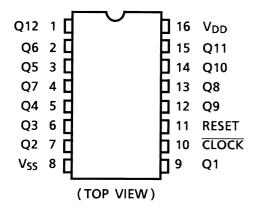
# TC4040BP, TC4040BF

### TC4040B 12 Stage Ripple-Carry Binary Counter/Dividers

TC4040B is 12 stage ripple carry binary counter having asynchronous clear function. This counter advances its counting stage by falling edge of  $\overline{CLOCK}$  input. When RESET input is placed "H", all the circuits are reset regardless of  $\overline{CLOCK}$  input making all the outputs (Q1 through Q12) to be "L".

This is most suitable for frequency dividers, control circuits and timing circuits.

### **Pin Assignment**

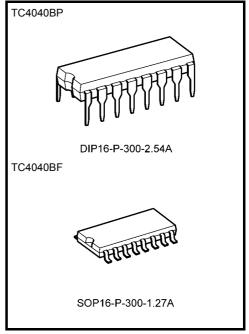


#### **Truth Table**

$\overline{CLOCK}\Delta$	RESET	Output State
*	Н	All Outputs = "L"
	L	No Change
¬_ L		Advance to Next State

Δ: Level change

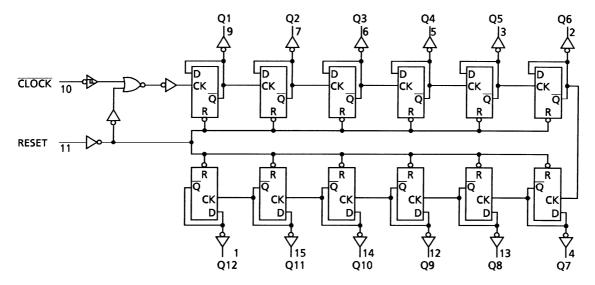
\*: Don't care



Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

### **Logic Diagram**



### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	$V_{DD}$	$V_{SS}$ – 0.5 to $V_{SS}$ + 20	V
Input voltage	V <sub>IN</sub>	V <sub>SS</sub> – 0.5 to V <sub>DD</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Storage temperature range	T <sub>stg</sub>	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### Operating ranges $(V_{SS} = 0 V)$ (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	$V_{DD}$	_	3	_	18	V
Input voltage	$V_{IN}$		0	_	$V_{DD}$	V

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Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

# Static Electrical Characteristics ( $V_{SS} = 0 V$ )

		Sym-	Test Condition		-40°C		25°C			85°C		
		bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage			$ I_{OUT}  < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	4.95	_	4.95	5.00	_	4.95	_	
		V <sub>OH</sub>		10	9.95	_	9.95	10.00	_	9.95	_	V
			VIN - VSS, VDD	15	14.95	_	14.95	15.00	_	14.95	_	
			I <sub>OUT</sub>   < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level voltage	output	V <sub>OL</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V
			VIIV - VSS, VDD	15	_	0.05	_	0.00	0.05	_	0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V <sub>OH</sub> = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA
Output hig	gh current	IOH	V <sub>OH</sub> = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	
			V <sub>OH</sub> = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		l <sub>OL</sub>	V <sub>OL</sub> = 0.4 V	5	0.61	_	0.51	1.5	_	0.42	_	mA
Output lov	v current		V <sub>OL</sub> = 0.5 V	10	1.50	_	1.30	3.2	_	1.10	_	
Output 10V	Vourient		V <sub>OL</sub> = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		V <sub>IH</sub>	V <sub>OUT</sub> = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.5	_	V
Input high	voltage		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_	
input mgm	voltage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	v
			I <sub>OUT</sub>   < 1 μA									
			V <sub>OUT</sub> = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
Input low voltage		V <sub>IL</sub>	V <sub>OUT</sub> = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0	V
			V <sub>OUT</sub> = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	
			$ I_{OUT}  < 1 \mu A$									
Input	"H" level	I <sub>IH</sub>	V <sub>IH</sub> = 18 V	18		0.1	_	10 <sup>-5</sup>	0.1	_	1.0	μΑ
current	"L" level	I <sub>I</sub> L	V <sub>IL</sub> = 0 V	18	_	-0.1	_	-10 <sup>-5</sup>	-0.1	_	-1.0	μΑ
			, , , , ,	5		5	_	0.005	5	_	150	
Quiescent supply current		I <sub>DD</sub>	$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	10	_	0.010	10	_	300	μΑ
			(510)	15		20	_	0.015	20	_	600	

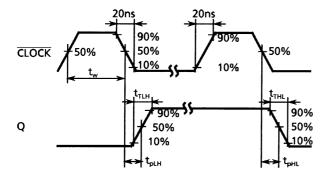
Note: All valid input combinations.

# Dynamic Electrical Characteristics (Ta = 25°C, $V_{SS}$ = 0 V, $C_L$ = 50 pF)

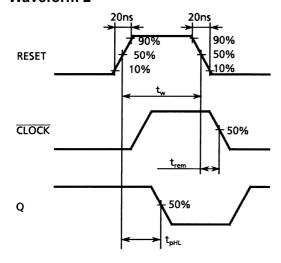
Observe et aviation	O make al	Test Condition	N 41	T	N4	Lloit	
Characteristics	Symbol		V <sub>DD</sub> (V)	Min	Тур.	Max	Unit
Output transition time			5	_	70	200	
Output transition time	t <sub>TLH</sub>	_	10	_	35	100	ns
(low to high)			15	_	30	80	
Output transition time			5	_	70	200	
Output transition time	t <sub>THL</sub>	_	10	_	35	100	ns
(high to low)			15	_	30	80	
Propagation delay time			5	_	160	360	
(CLOCK -Q1)	t <sub>pLH</sub>	_	10	_	80	160	ns
(CLOCK -QT)			15	_	65	130	
Draw a setion delevations			5	_	160	360	
Propagation delay time (CLOCK -Q1)	t <sub>pHL</sub>	_	10	_	80	160	ns
(CLOCK -QT)			15	_	65	130	
Draw a setion delevitime			5	_	900	1800	
Propagation delay time ( CLOCK -Q12)	t <sub>pLH</sub>	_	10	_	450	900	ns
(CLOCK -Q12)			15	_	360	720	
Description delections			5	_	900	1800	
Propagation delay time ( CLOCK -Q12)	t <sub>pHL</sub>	_	10	_	450	900	ns
(CLOCK -Q12)			15	_	360	720	
Draw a setion delevitime			5	_	150	280	
Propagation delay time	t <sub>pHL</sub>	_	10	_	70	120	ns
(RESET-Q)			15	_	50	100	
			5	3.5	10	_	
Max clock frequency	f <sub>CL</sub>	_	10	8.0	20	_	MHz
			15	12.0	25	_	
			5	_	50	140	
Min clock pulse width	t <sub>W</sub>	_	10	_	20	60	ns
			15	_	15	40	
Min pulso width			5	_	100	200	
Min pulse width	t <sub>W</sub>	_	10	_	40	80	ns
(RESET)			15	_	30	60	
Min remarkal times			5	_	_	350	
Min removal time	t <sub>rem</sub>	_	10	_	_	150	ns
(RESET- CLOCK )			15	_	_	100	
May alogh input star for-			5			•	
Max clock input rise time	t <sub>rCL</sub>	_	10		No limit		μS
Max clock input fall time	t <sub>fCL</sub>		15				
Input capacitance	C <sub>IN</sub>	_		_	5	7.5	pF

## **Waveforms for Measurement of Dynamic Characteristics**

### Waveform 1



### Waveform 2

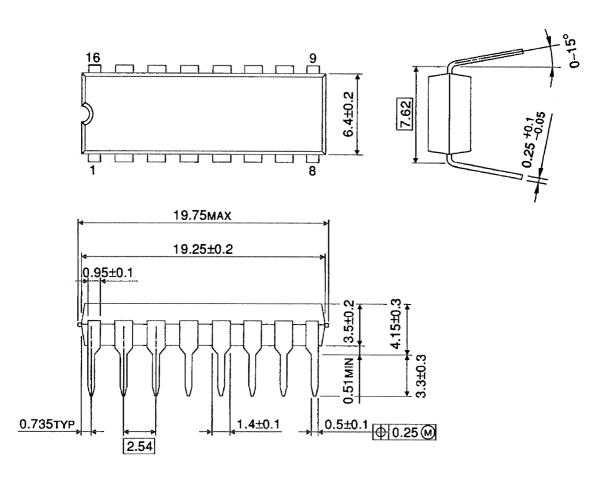


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## **Package Dimensions**

DIP16-P-300-2.54A Unit: mm

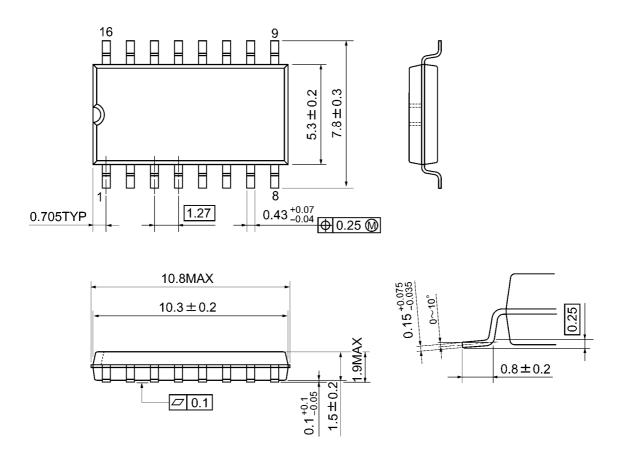


Weight: 1.00 g (typ.)



### **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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