TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4021BP, TC4021BF

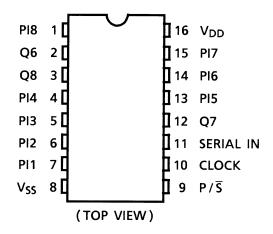
### TC4021B 8-Stage Static Shift Register

(asynchronous parallel input or synchronous serial input/serial output)

TC4021B is 8 stage parallel in/serial out shift register, which can be used also for serial in/serial out operations. In the case of parallel operation, the data of PARALLEL IN is input to each F/F asynchronously with CLOCK and the output is obtained. In the case of serial operations, each F/F is triggered by rising edge of CLOCK. (asynchronous parallel or synchronous serial input)

Switching of PARALLEL operation and SERIAL operation is achieved by P/ $\bar{S}$  CONTROL input. When P/ $\bar{S}$  CONTROL input is "H", PARALLEL operation is designated and when it is "L", SERIAL operation is designated.

### **Pin Assignment**



### **Truth Table**

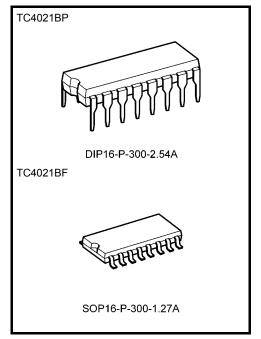
Inputs						Outputs∆		
CLOCKAA	P/S	PI1	Pln	SI	Q1	Qn		
	L	*	*	L	L	Qn – 1		
	L	*	*	Н	Н	Qn – 1		
$\neg$	L	*	*	*	No Change			
*	Н	L	L	*	L	L		
*	Н	L	Н	*	L	Н		
*	Н	Н	L	*	Н	L		
*	Н	Н	Н	*	Н	Н		

n: 2 to 8

Δ: Q1 to Q5 internal

ΔΔ: Level change

\*: Don't care



Weight

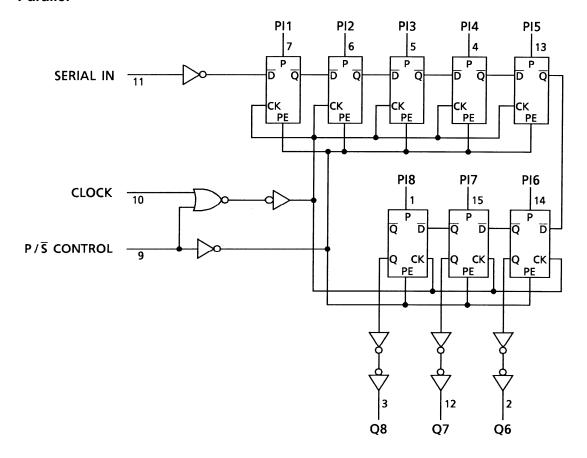
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

Start of commercial production 1978-04

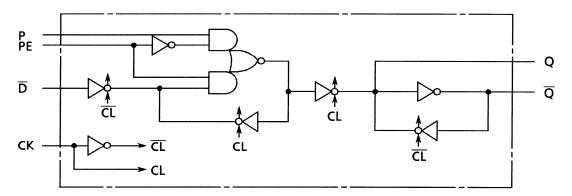
## **Logic Diagram**

**TOSHIBA** 

## Parallel



## Internal Flip Flop



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### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	$V_{DD}$	$V_{SS}$ – 0.5 to $V_{SS}$ + 20	V
Input voltage	V <sub>IN</sub>	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
Output voltage	V <sub>OUT</sub>	$V_{SS} - 0.5$ to $V_{DD} + 0.5$	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOP)	mW
Operating temperature range	T <sub>opr</sub>	-40 to 85	°C
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### Operating Ranges (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	$V_{DD}$	_	3	_	18	V
Input voltage	V <sub>IN</sub>	_	0	_	V <sub>DD</sub>	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

## Static Electrical Characteristics ( $V_{SS} = 0 V$ )

		Sym-	Test Condition		-40°C		25°C			85°C			
Charac	teristics	bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
High-level voltage	output	V <sub>OH</sub>	$\begin{split} & \left  \left  I_{OUT} \right  < 1 \; \mu A \\ & V_{IN} = V_{SS},  V_{DD} \end{split} \label{eq:interpolation}$	5 10 15	4.95 9.95 14.95		4.95 9.95 14.95	5.00 10.00 15.00		4.95 9.95 14.95		٧	
					14.95								
Low-level voltage	Low-level output		I <sub>OUT</sub>   < 1 μA	5 10		0.05 0.05	_	0.00	0.05 0.05	_	0.05 0.05	V	
. o.u.go			$V_{IN} = V_{SS}, V_{DD}$	15	_	0.05	_	0.00	0.05	_	0.05		
			V <sub>OH</sub> = 4.6 V	5	-0.61		-0.51	-1.0	_	-0.42			
			V <sub>OH</sub> = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_		
Output hig	h current	loh	V <sub>OH</sub> = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_	mA	
			V <sub>OH</sub> = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_		
			$V_{IN} = V_{SS}, V_{DD}$										
		l <sub>OL</sub>	V <sub>OL</sub> = 0.4 V	5	0.61	_	0.51	1.5	_	0.42	_	mA	
Output lov	/ current		V <sub>OL</sub> = 0.5 V	10	1.50	_	1.30	3.8	_	1.10	_		
Output low	Current		V <sub>OL</sub> = 1.5 V	15	4.00	_	3.40	15.0	_	2.80	_		
			$V_{IN} = V_{SS}, V_{DD}$										
		V <sub>IH</sub>	V <sub>OUT</sub> = 0.5 V, 4.5 V	5	3.5		3.5	2.75	_	3.5	_	V	
Input high	voltago		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	_		
input nign	voltage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_		
			$ I_{OUT}  < 1 \mu A$										
		VIL	V <sub>OUT</sub> = 0.5 V, 4.5 V	5		1.5	_	2.25	1.5	_	1.5	V	
Input Iow y	voltage		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	_	3.0	_	4.50	3.0	_	3.0		
Input low v	Input low voltage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	V	
			$ I_{OUT}  < 1 \mu A$										
Input	"H" level	lін	V <sub>IH</sub> = 18 V	18		0.1		10 <sup>-5</sup>	0.1		1.0	μА	
current	"L" level	I <sub>IL</sub>	V <sub>IL</sub> = 0 V	18	_	-0.1	_	$-10^{-5}$	-0.1	_	-1.0	μΛ	
				5	_	5	_	0.005	5	_	150		
Quiescent current	Quiescent supply current		$V_{IN} = V_{SS}, V_{DD}$ (Note)	10	_	10	_	0.010	10	_	300	μА	
			(.1310)	15	_	20		0.020	20	_	600		

Note: All valid input combinations.

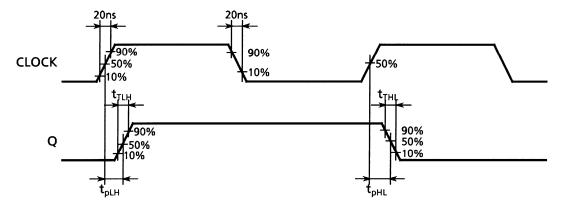
## Dynamic Electrical Characteristics (Ta = 25°C, $V_{SS}$ = 0 V, $C_L$ = 50 pF)

21		Test Condition	N disa	_		l lait	
Characteristics	Symbol		V <sub>DD</sub> (V)	Min	Тур.	Max	Unit
Outrout transition times			5	_	80	200	
Output transition time	t <sub>TLH</sub>	_	10	_	50	100	ns
(low to high)			15	_	40	80	
Output transition time			5	_	80	200	
(high to low)	t <sub>THL</sub>	_	10	_	50	100	ns
(flight to low)			15	—	40	80	
Propagation delay time	t		5	_	150	320	
(CLOCK-Q)	t <sub>pLH</sub>	_	10	_	65	160	ns
(02001-4)	t <sub>pHL</sub>		15	_	45	120	
Propagation delay time	t		5	_	230	460	
(P/S -Q)	t <sub>pLH</sub>	_	10	_	90	180	ns
(175-4)	t <sub>pHL</sub>		15	_	60	120	
	fcL		5	3.0	6.5	_	
Max clock frequency		_	10	6.0	18.0	_	MHz
			15	8.5	24.0	_	
	tw	_	5	_	80	180	
Min clock pulse width			10	_	30	80	ns
			15	_	20	50	
Max clock rise time	t <sub>rCL</sub>		5	20.0	_	_	
Max clock fall time	t <sub>fCL</sub>	_	10	2.5	_	_	μS
Wax Gook fall time	чСL		15	1.0	_	_	
Min set-up time			5	_	40	120	
(SI-CLOCK)	tsu	_	10	_	20	80	ns
(OF-OLOGIN)			15	_	15	320 160 120 460 180 120 — — 180 80 50 — — — —	
Min set-up time			5	_	25	50	
(PI-P/S)	tsu	_	10	_	15	30	ns
(1170)			15	_	10	20	
Min hold time			5	_	35	70	
(SI-CLOCK), (PI-P/S)	t <sub>H</sub>	_	10	_	20	40	ns
(3. 32331), (1.1.10)			15	_	15	30	
Min pulse width			5	_	90	180	
(P/S -CONTROL)	t <sub>WH</sub>	_	10	_	30	80	ns
(			15	_	10	50	
Min removal time			5	_	45	280	
(P/S -CLOCK)	t <sub>rem</sub>	_	10	_	20	140	ns
5255117			15	_	15	100	
Input capacitance	C <sub>IN</sub>	_		_	5	7.5	pF

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## **Waveforms for Measurement of Dynamic Characteristics**

### Waveform 1



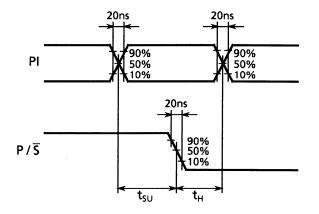
### Waveform 2

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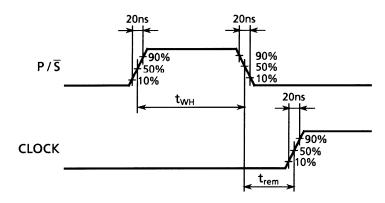
t<sub>SU</sub>

t<sub>H</sub>

### Waveform 3



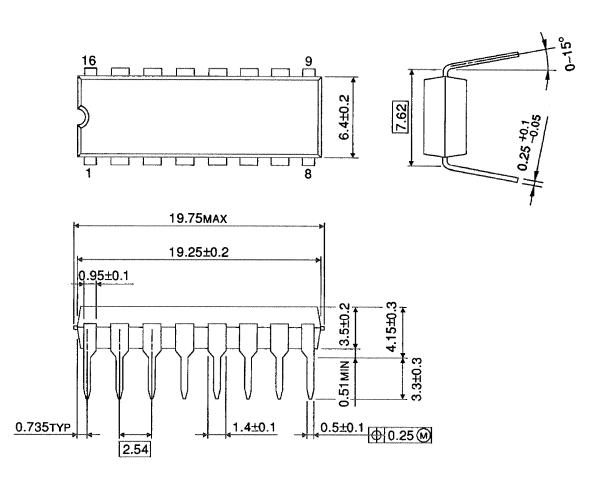
### Waveform 4





## **Package Dimensions**

DIP16-P-300-2.54A Unit: mm

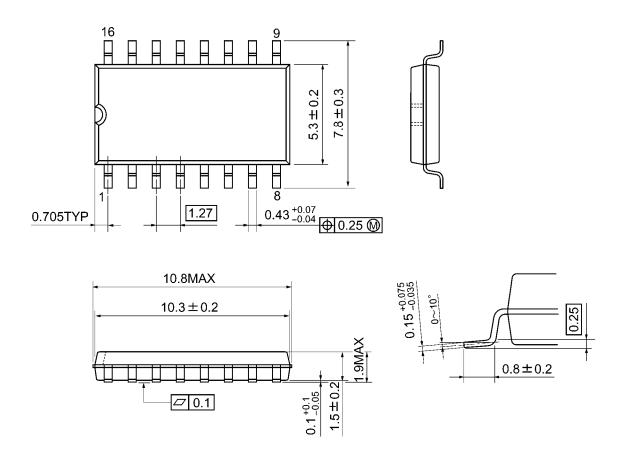


Weight: 1.00g (typ.)



## **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

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