V10PN50-M3

Vishay General Semiconductor

High Current Density Surface Mount TMBS[®] (Trench MOS Barrier Schottky) Rectifier

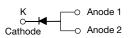
Ultra Low $V_F = 0.30$ V at $I_F = 5$ A

eSMP[®] Series

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SMPC (TO-277A)



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	10 A			
V _{RRM}	50 V			
I _{FSM}	180 A			
V_F at $I_F = 10 A$	0.40 V			
T _J max.	150 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.1 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\text{C}$
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V10PN50	UNIT	
Device marking code		10N5		
Maximum repetitive peak reverse voltage	V _{RRM}	50	V	
Maximum average forward rectified current (fig. 1)	I _F ⁽¹⁾	10	Α	
Maximum average forward rectified current (lig. 1)	I _F ⁽²⁾	5.3		
Maximum DC reverse voltage	V _{DC}	35	V	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	180	A	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

⁽¹⁾ Mounted on 30 mm x 30 mm 2 oz. pad PCB

⁽²⁾ Free air, mounted on recommended copper pad area

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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 5 A	T _A = 25 °C		0.40	-	V
	I _F = 10 A			0.47	0.55	
	I _F = 5 A	T _A = 125 °C		0.30	-	
	I _F = 10 A			0.40	0.49	
Reverse current	V _B = 50 V	T _A = 25 °C	I _R ⁽²⁾	50	1500	μA
	$V_R = 50 V$ $T_A = 7$	T _A = 125 °C	IR (=/	32	85	mA

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V10PN50	UNIT	
Typical thermal resistance	R _{0JA} ^{(1) (2)}	70	°C/W	
	R _{0JM} ⁽³⁾	4		

Notes

⁽¹⁾ Free air, mounted on recommended copper pad area; thermal resistance R_{0JA} - junction-to-ambient

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

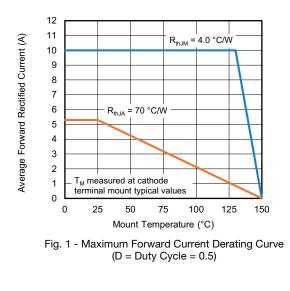
⁽³⁾ Mounted on 30 mm x 30 mm 2 oz. pad PCB; thermal resistance R_{0JM} - junction-to-mount measured at cathode side

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V10PN50-M3/86A	0.10	86A	1500	7" diameter plastic tape and reel		
V10PN50-M3/87A	0.10	87A	6500	13" diameter plastic tape and reel		



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



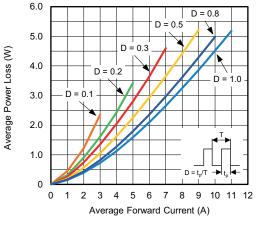
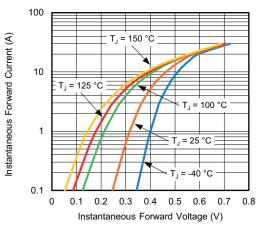
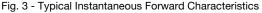


Fig. 2 - Forward Power Loss Characteristics





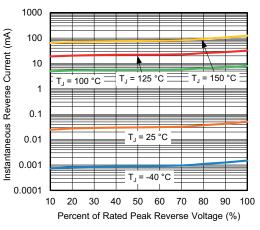


Fig. 4 - Typical Reverse Leakage Characteristics

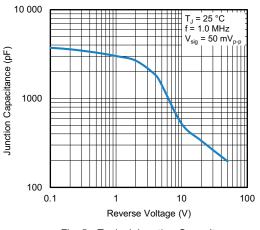
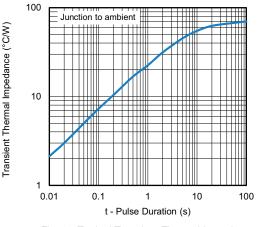


Fig. 5 - Typical Junction Capacitance





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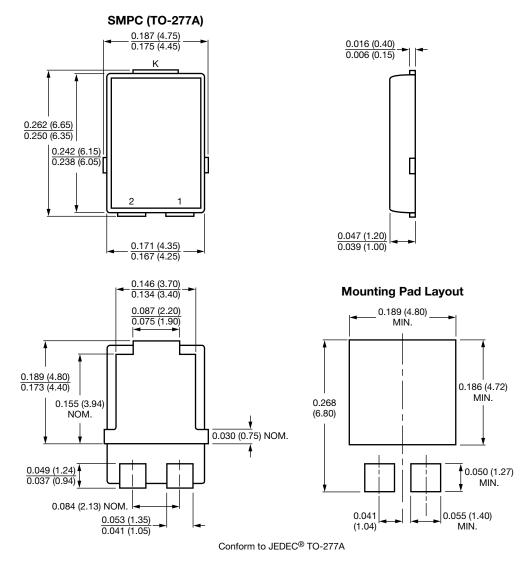
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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