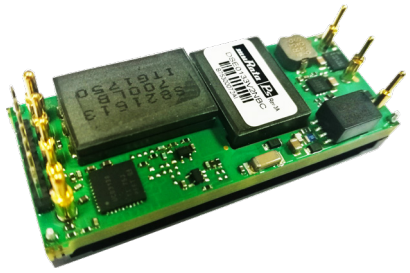
Murata Power Solutions

DSE/DAE/DCE Series

400W Eighth Brick
DOSA Digital PMBus Interface



Output Voltage (V)	Output Current (A)	Input Voltage (V)
12	33	36-75

FEATURES

- DOSA Compliant Digital Eighth-Brick with PMBus interface
- 36-75Vin Range
- 95.5% Typical Efficiency
- Delivers up to 33A (400W)
- Low Output Ripple & Noise
- Wide Operating Temperature Range -40°C to +85°C
- Optional Droop Load Sharing of two or more modules
- Baseplate included for improved thermal performance
- Output Over Current/Voltage Protection
- Over Temperature Protection
- Negative & Positive Logic (Negative Logic standard configuration)
- Basic insulation, 2250Vdc I/O Isolation compliant with IEEE802.3 PoE Standards
- Optional Reflow processable
- Three pin/function configurations available:
  [1] Full PMBus with Sense & Trim Pins
  [2] No PMBus with Sense & Trim Pins
  [3] 5 Pin Bus converter, No Sense & Trim Pins
- Certified to UL/EN/IEC 60950-1, CAN/CSA-C22.2 No. 60950-1, 2nd Edition, safety approvals and EN55022/CISPR22 standards

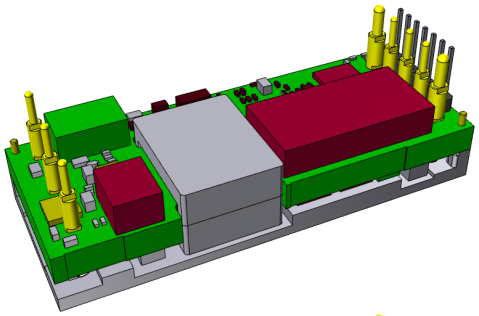
Applications

- Distributed Power Architectures
- Intermediate Bus Voltage Applications
- Networking Equipment including POE applications
- Servers & Storage Applications
- Fan Tray assemblies along with other applications requiring a regulated Voltage source

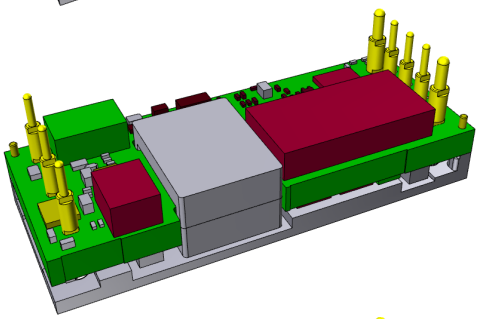
PRODUCT OVERVIEW

Murata Power Solutions is introducing the first in a series of DOSA compliant, digitally controlled DC-DC converters that are based on a 32-bit ARM processor. The DSE series provides a fully regulated, digitally controlled DC output in a 1/8-brick format that will support the DOSA industry standard footprint for isolated board mounted power modules. The DSE series supports advances in power conversion technology including a digital interface supporting the PMBus protocol for communications to power modules. The DSE0133V2 is an isolated, regulated, 396W-12Vout eighth brick that supports the TNV input voltage range of 36V–75V with a typical efficiency of 95.5%. The DSE series also incorporates a “droop” load sharing option that allows connecting two or more units together in parallel for demanding power-hungry applications or to provide redundancy in high reliability applications. The converter also offers high input to output isolation of 2250 VDC as required for Power over Ethernet (PoE) applications. The DSE series is suitable for applications covering MicroTCA, servers and storage applications, networking equipment, Telecommunications equipment, Power over Ethernet (PoE), fan trays, wireless networks, wireless pre-amplifiers, and industrial and test equipment, along with other applications requiring a regulated 12V.

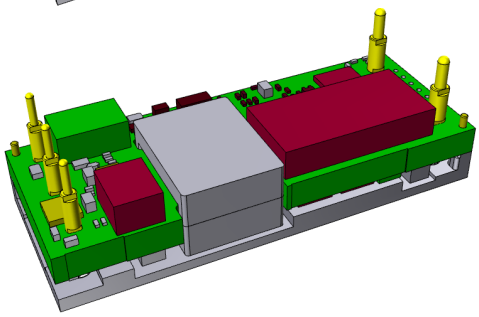
DSE



DAE



DCE



RoHS COMPLIANT

For full details go to www.murata-ps.com/rohs

PERFORMANCE SPECIFICATIONS SUMMARY AND ORDERING GUIDE [1]

	Output						Input			Efficiency
					Regulation (Typ.)					
Root Model	V_{OUT} (V)	I_{OUT} (A, max.)	Total Power (W)	Ripple & Noise (mVp-p)	Line (mV)	Load (mV)	V_{IN} (V, Nom.)	Range (V)	I_{IN}, full load (A)	Typ.
DSE0133V2	12	33	396	80	36	36	48	36-75	11.7	95.5%
DAE0133V2	12	33	396	80	36	36	48	36-75	11.7	95.5%
DCE0133V2	12	33	396	80	36	36	48	36-75	11.7	95.5%

Notes:

[1] Typical at TA = +25°C under nominal line voltage and full-load conditions. All models are specified with an external 1µF multi-layer ceramic and 10µF capacitors across their output pins.

Part Number Structurer																
Product Family[1]	D	S														DS = DOSA Standard Digital Eighth Brick W/Sense & Trim, W/PMBus DA = DOSA Analog Eighth Brick W/Sense & Trim, No PMBus DC = DOSA Analog Eighth Brick (5pin IBC)
Form Factor			E													E = Eighth Brick
Vout				01												01 = 12Vout, 02 = 5Vout, 03 = 3.3 Vout
Output Current					33											Max Iout in Amps
Vin Range						V2										V2 = 36-75V
Logic							N									N=Negative Logic, P=Positive Logic
Pin Length[2]								X								1 = 0.110"(2.79 mm), 2 = 0.145"(3.68 mm), 3 = 0.220"(5.58mm), Omit for standard pin length 0.180"(4.57mm) (shown in the mechanical drawings)
Mechanical Configuration									B							B = Baseplate
Load Sharing										S						S = Load Sharing Feature, Omit for Standard
Reflow compliant[2]											R					R = MSL-3 compliant Packaging, Omit for Standard through hole processing
Specific Customer Configuration												X	X	X		Customer Code, Omit for Standard
RoHS															C	RoHS 6/6 Compliant

Example Part Number DSE0133V2N2BSRC DOSA Digital Eighth Brick, 12Vout@33A, Negative Logic, 0.145" Pin length, Baseplate, Load sharing, Reflow MSL-3 compliant, RoHS 6/6.

[1] Load Sharing on DSE (with PMBus) will not include Sense & Trim Pins. Load sharing is NOT available on DAE modules.

[2] Minimum order quantity is required. Samples available with standard pin length only.

FUNCTIONAL SPECIFICATIONS

ABSOLUTE MAXIMUM RATINGS	Conditions [1]	Minimum	Typical/Nominal	Maximum	Units
Input Voltage, Continuous		0		75	Vdc
Input Voltage, Transient	100 mS max. duration			100	Vdc
Isolation Voltage	Input to output			2250	Vdc
On/Off Remote Control	Power on, referred to -Vin	0		13.5	Vdc
Output Power		0		396	W
Output Current	Current-limited, no damage, short-circuit protected	0		33	A
Storage Temperature Range	Vin = Zero (no power)	-55		125	°C
Absolute maximums are stress ratings. Exposure of devices to greater than any of these conditions may adversely affect long-term reliability. Proper operation under conditions other than those listed in the Performance/Functional Specifications Table is not implied nor recommended.					
General Conditions for Device under Test unless otherwise specified: Typical at TA = +25°C under nominal line voltage and nominal load conditions. All models are specified with an external 220µF input capacitor and 1µF & 10µf capacitors across their output pins.					
INPUT					
Operating voltage range (V2)		36	48	75	Vdc
Start-up threshold	(Default, configurable via PMBus)	32	34	36	Vdc
Undervoltage shutdown	(Default, configurable via PMBus)	28	31	34	Vdc
Internal Filter Type			Pi		
External Input fuse			20		A
Input current					
Full Load Conditions	Vin = nominal		8.80	9.30	A
Low Line input current	Vin = minimum		11.70	12.20	A
Inrush Transient	Vin = 48V.		0.7	1	A^2-Sec.
Short Circuit input current				0.2	A
No Load input current	Vin = 48V, Iout = 0, unit=ON		80	150	mA
Shut-Down input currrent (Off, UV, OT)				35	mA
Back Ripple Current	No filtering		1.5		Ap-p
GENERAL and SAFETY					
Efficiency	Vin = 48V, full load	94.5	95.5		%
Isolation Voltage	Input to output			2250	Vdc
	Input to Baseplate			1500	Vdc
	Output to Baseplate			1500	Vdc
Insulation Safety Rating			Basic		
Isolation Resistance			10		MΩ
Isolation Capacitance			1500		pF
Safety	Certified to UL-60950-1, CSA-C22.2 No.60950-1, IEC/EN60950-1, 2nd edition		Yes		
Calculated MTBF	Per Telcordia SR-332, Issue 3, Method 1, Class 1, Ground Fixed, Tcase=+25°C		4900		Hours x 10^3
DYNAMIC CHARACTERISTICS					
Switching Frequency (Configurable via PMBus)					
Fixed Frequency Control			200		KHz
Variable Frequency Control (Default)			N/A		KHz
Turn On Time (Configurable via PMBus)					
Vin On to Vout Regulated			40	50	mS
Remote On to Vout Regulated				8	mS
Vout Rise Time (Default, Configurable via PMBus)					
From 10%~90%				30	mS
Vout Fall Time of Regulated Off (Default, Configurable via PMBus)					
From 90%~10%			N/A		mS
Dynamic Load Response	50-75-50%, 0.1A/us,within 1% of Vout (Vin=Vinnom, tested with a 1.0 µF ceramic, 10 µF tantalum and 330µF low ESR polymer capacitor across the load.)		200	300	µSec
Dynamic Load Peak Deviation			±250	±350	mV
Dynamic Load Response	50-75-50%, 1A/us,within 1% of Vout (Vin=Vinnom, tested with a 1.0 µF ceramic, 10 µF tantalum and 330µF low ESR polymer capacitor across the load.)		120	200	µSec
Dynamic Load Peak Deviation			±500	±750	mV
FEATURES and OPTIONS					
Remote On/Off Control					
Primary On/Off control (designed to be driving with an open collector logic, Voltages referenced to -Vin)					
"P" Suffix:					
Positive Logic, ON State	ON = pin open or external voltage	3.5		13.5	Vdc
Positive Logic, OFF State	OFF = ground pin or external voltage	0		0.8	Vdc
Control Current	Open collector / drain		0.1	0.2	mA
"N" Suffix:					
Negative Logic, ON state	ON = ground pin or external voltage	-0.1		0.8	Vdc
Negative Logic, OFF state	OFF = pin open or external voltage	3.5		13.5	Vdc
Control Current	Open collector / drain		0.1	0.2	mA
Remote Sense Compliance	Sense pins connected externally to respective Vout pins			10	%

OUTPUT					
Total Output Power		0	396	396	W
Voltage					
Initial Output Voltage	Vin = 48V, Iout = 0A, temp=25°C, with/without "S" suffix	11.97		12.03	Vdc
Output Voltage	VOUT_DROOP = 0 mΩ, All conditions	11.82	12	12.18	Vdc
Output Adjust Range	Hardware TRIM	9.6		13.2	Vdc
Overvoltage Protection	Configurable via PMBus	13.8	14.4	15.6	Vdc
Voltage Droop	Default, configurable via PMBus		0		mΩ
Voltage Droop, for "S" suffix	Default, configurable via PMBus		7		mΩ
Current					
Output Current Range		0		33	A
Minimum Load			No minimum load		
Current Limit Inception [2]	90% of Vnom., after warmup, Configurable via PMBus (Need check the OCP Inception of Vout is whether reasonable)	37	41	45	A
Short Circuit					
Short Circuit Current	Hiccup technique, autorecovery within 1% of Vout		0.2		A
Short Circuit Duration (remove short for recovery)	Output shorted to ground, no damage		Continuous		
Short circuit protection method	Hiccup current limiting		Non-latching		
Regulation [3]					
Line Regulation	Vin = 36-75, Vout = nom., full load			36	mV
Load Regulation	Iout = min. to max., Vin = nom. |Vout @ min_load - Vout @ max_load|			36	mV
Ripple and Noise	5Hz-20MHz BW, Cout = 1µF Vin=nom and Io=min to max, tested with a 1.0µF ceramic, 10 µF tantalum and 330µF low ESR polymer capacitors across the load			300	mV pk-pk
Temperature Coefficient	At all outputs		0.01	0.02	% of Vnom./°C
Maximum Output Capacitance	Low ESR, 50% ceramic, and 50% OSCON or POSCAP	47		10,000	µF
PMBus Monitoring Accurracy					
VIN_READ		-7		7	%
VOUT_READ		-2		2	%
IOUT_READ		-4		4	A
TEMP_READ		-5		5	°C
MECHANICAL					
Outline Dimensions (with baseplate)	L x W x H		2.3 x 0.9 x 0.55	2.32 x 0.92 x 0.57	Inches
			58.42 x 22.86 x 14	58.93 x 23.37 x 14.5	mm
Weight (with baseplate)			1.94		Ounces
			55.0		Grams
Through Hole Pin Diameter			0.04 & 0.062		Inches
			1.016 & 1.575		mm
Digital Interface Pin Diameter			0.02		
			0.5		
Through Hole Pin Material			Copper alloy		
TH Pin Plating Metal and Thickness	Nickel subplate		98.4-299		µ-inches
	Gold overplate		4.7-19.6		µ-inches
ENVIRONMENTAL					
Operating Ambient Temperature Range	With derating	-40		85	°C
Operating Baseplate Temperature		-40		110	°C
Storage Temperature	Vin = Zero (no power)	-55		125	°C
Thermal Protection/Shutdown (with "B" Suffix)	Baseplate temperature measured in the center		130		°C
Electromagnetic Interference Conducted, EN55022/CISPR22	External filter required; see emissions performance test.		B		Class
RoHS Rating			RoHS-6		

Notes:

[1] Typical at TA=+25°C under nominal line voltage and full-load conditions. All models are specified with an external 1µF multi-layer ceramic and 10µF capacitors across their output pins.

[2] Over-current protection is non-latching with auto recovery (Hiccup).

[3] Regulation specifi cations describe the output voltage changes as the line voltage or load current is varied from its nominal or midpoint value to either extreme.

CMD	Command Name	SMBus Transaction Type: Writing Data	SMBus Transaction Type: Reading Data	Number Of Data Bytes	Default Value
01h	OPERATION[2]	Write Byte	Read Byte	1	0x80
02h	ON_OFF_CONFIG[3]	Write Byte	Read Byte	1	0x19
03h	CLEAR_FAULTS	Send byte	N/A	0	N/A
10h	WRITE_PROTECT	Write Byte	Read Byte	1	0x00
11h	STORE_DEFAULT_ALL[4]	Send byte	N/A	0	N/A
12h	RESTORE_DEFAULT_ALL[4]	Send byte	N/A	0	N/A
15h	STORE_USER_ALL[4]	Send byte	N/A	0	N/A
16h	RESTORE_USER_ALL[4]	Send byte	N/A	0	N/A
19h	CAPABILITY	N/A	Read Byte	1	0xB0
20h	VOUT_MODE	N/A	Read Byte	1	0x17
21h	VOUT_COMMAND	Write Word	Read Word	2	12.000
22h	VOUT_TRIM	Write Word	Read Word	2	0
28h	VOUT_DROOP	Write Word[12]	Read Word	2	0/7[13]
40h	VOUT_OV_FAULT_LIMIT	Write Word	Read Word	2	14
41h	VOUT_OV_FAULT_RESPONSE[5]	Write Byte	Read Byte	1	0xB8
42h	VOUT_OV_WARN_LIMIT	Write Word	Read Word	2	13.500
46h	IOUT_OC_FAULT_LIMIT	Write Word	Read Word	2	40.00
47h	IOUT_OC_FAULT_RESPONSE[6]	Write Byte	Read Byte	1	0xB8
4Ah	IOUT_OC_WARN_LIMIT	Write Word	Read Word	2	38.00
4Fh	OT_FAULT_LIMIT	Write Word	Read Word	2	125
50h	OT_FAULT_RESPONSE[5]	Write Byte	Read Byte	1	0xB8
51h	OT_WARN_LIMIT	Write Word	Read Word	2	115
55h	VIN_OV_FAULT_LIMIT	Write Word	Read Word	2	110.00
56h	VIN_OV_FAULT_RESPONSE[7]	Write Byte	Read Byte	1	0xF8
57h	VIN_OV_WARN_LIMIT	Write Word	Read Word	2	100.00
58h	VIN_UV_WARN_LIMIT	Write Word	Read Word	2	32.00
59h	VIN_UV_FAULT_LIMIT	Write Word	Read Word	2	30.50
5Ah	VIN_UV_FAULT_RESPONSE[7]	Write Byte	Read Byte	1	0xF8
5Eh	POWER_GOOD_ON	Write Word	Read Word	2	10.199
5Fh	POWER_GOOD_OFF	Write Word	Read Word	2	8.400
60h	TON_DELAY	Write Word[12]	Read Word	2	0
61h	TON_RISE[16]	Write Word[12]	Read Word	2	60
64h	TOFF_DELAY	Write Word[12]	Read Word	2	0
65h	TOFF_FALL[16]	Write Word[12]	Read Word	2	0
78h	STATUS_BYTE	Write Byte	Read Byte	1	N/A
79h	STATUS_WORD	Write Word	Read Word	2	N/A
7Ah	STATUS_VOUT	Write Byte	Read Byte	1	N/A
7Bh	STATUS_IOUT	Write Byte	Read Byte	1	N/A
7Ch	STATUS_INPUT	Write Byte	Read Byte	1	N/A
7Dh	STATUS_TEMPERATURE	Write Byte	Read Byte	1	N/A
7Eh	STATUS_CML	Write Byte	Read Byte	1	N/A
88h	READ_VIN	N/A	Read Word	2	N/A
8Bh	READ_VOUT	N/A	Read Word	2	N/A
8Ch	READ_IOUT	N/A	Read Word	2	N/A
8Dh	READ_TEMPERATURE_1[8]	N/A	Read Word	2	N/A
8Eh	READ_TEMPERATURE_2[9]	N/A	Read Word*	2	N/A
94h	READ_DUTY_CYCLE	N/A	Read Word	2	N/A
95h	READ_FREQUENCY	N/A	Read Word	2	N/A
96h	READ_POUT	N/A	Read Word	2	N/A
98h	PMBUS_REVISION	N/A	Read Byte	1	0x42
99h	MFR_ID	N/A	Block Read	22	"Murata Power Solutions"
9Ah	MFR_MODEL[10]	Block Write*	Block Read	<=20	N/A
9Bh	MFR_REVISION[10]	Block Write*	Block Read	<=10	N/A
9Dh	MFR_DATE[10]	Block Write*	Block Read	<=10	N/A
9Eh	MFR_SERIAL[10]	Block Write*	Block Read	<=20	N/A
A0h	MFR_VIN_MIN	N/A	Read Word	2	36.00
A1h	MFR_VIN_MAX	N/A	Read Word	2	75.00
A2h	MFR_IIN_MAX	N/A	Read Word	2	15
A3h	MFR_PIN_MAX	N/A	Read Word	2	400
A4h	MFR_VOUT_MIN	N/A	Read Word	2	9.600
A5h	MFR_VOUT_MAX	N/A	Read Word	2	13.199
A6h	MFR_IOUT_MAX	N/A	Read Word	2	33.00
A7h	MFR_POUT_MAX	N/A	Read Word	2	400
A8h	MFR_TAMBIENT_MAX	N/A	Read Word	2	85
A9h	MFR_TAMBIENT_MIN	N/A	Read Word	2	-40
B0h	USER_DATA_00	Block Write	Block Read	<=20	"---"

B1h	USER_DATA_01	Block Write	Block Read	<=20	"---"
C0h	MFR_MAX_TEMP_1	N/A	Read Word	2	130
DBh	MFR_CURRENT_SHARE_CONFIG	Write Byte*	Read Byte	1	0x00/0x01[11]
DDh	MFR_PRIMARY_ON_OFF_CONFIG	Write Byte	Read Byte	1	0x04/0x06[14]
DEh	MFR_PGOOD_POLARITY	Write Byte	Read Byte	1	0x00
E8h	MFR_VIN_OV_FAULT_HYS	Write Word	Read Word	2	2.50
E9h	MFR_VIN_UV_FAULT_HYS	Write Word	Read Word	2	2.50
EAh	MFR_OT_FAULT_HYS	Write Word	Read Word	2	10
F6h	MFR_CALIBRATION_STATUS	N/A	Read Byte*	1	0xC7
F9h	MFR_VIN_SENSE_CALIBRATION	Write byte*	N/A	1	N/A
FAh	MFR_IOUT_SENSE_CALIBRATION	Write Word*	N/A	2	N/A
FBh	MFR_VOUT_SET_POINT_CALIBRATION	Write Word*	N/A	2	N/A
FCh	MFR_SUPERVISOR_PASSWORD	Block Write	N/A	N/A	N/A

Notes:

Only available in supervisor mode (default state is user mode, send password to comand 0xFC to change to supervisor mode).

[1] a) Unit restores the entire contents of the non-volatile User Store memory when power up.

b) PEC is supported.

c) Max bus speed: 400kHZ.

d) SMBALERT# is supported.

e) Linear data format used.

f) addressing: If the calculated PMBus address is 0d, 11d or 12d, SA0 or SA1 lefts open, default PMBus address 119d is assigned instead.

SA0/SA1 Index	R_{SA0}/R_{SA1} [kΩ]
0	10
1	22
2	33
3	47
4	68
5	100
6	150
7	220
The SA0 and SA1 pins can be configured with a resistor to GND according to the following equation. PMBus Address = 8x(SA0 value)+(SA1 value)	

[2] Not supported items:

100101XXb Margin Low (Ignore Fault).

101001XXb On Margin High (Ignore Fault).

[3] Restart delay of turned off by OPEATION or CONTROL or primary on/off is 200ms.

[4] Unit will shutdown 1s for protection , then recover automaticly.

[5] Restart delay unit: 500ms, lower limit: 500ms.

Turn off delay unit: 0ms, lower limit: 0ms, if bits 7:6=11b, restart delay is 500ms.

[6] Restart delay unit and Turn off delay unit are same as note 5.

Bits 7:6: 00b, 01b, 10b are not supported.

[7] Restart delay unit: 200ms, lower limit: 200ms.

Turn off delay unit: 0ms, lower limit: 0ms if bits 7:6=11b, restart delay is 200ms.

[8] Temperature of baseplate side.

[9] Temperature of control unit.

[10] Unit's actual inforamtion.

[11] Default value of DROOP CURRENT SHARE ENABLED mode: 0x01.

Default value of DROOP CURRENT SHARE DISABLED mode: 0x00.

[12] Available in supervisor mode when droop current share on, available in both mode when droop current share off.

[13] Locked to 7mΩ in DROOP CURRENT SHARE mode; VOUT_DROOP is not used in CURRENT SHARE DISABLED mode.

[14] Default value of negative logic: 0x04.

Default value of positive logic: 0x06.

[15] VOUT_TRIM + VOUT COMMAND is limited to 9.6~13.2V, if calculated Vout exceeds limit, then show invalid data.

[16] Value of 0 is acceptable, which is the same as lower limit to unit.

[17] Default value of without "B" suffix: 122C.

Default value of with "B" suffix: 128C.

PERFORMANCE DATA

Figure 1. Efficiency vs. Load Current

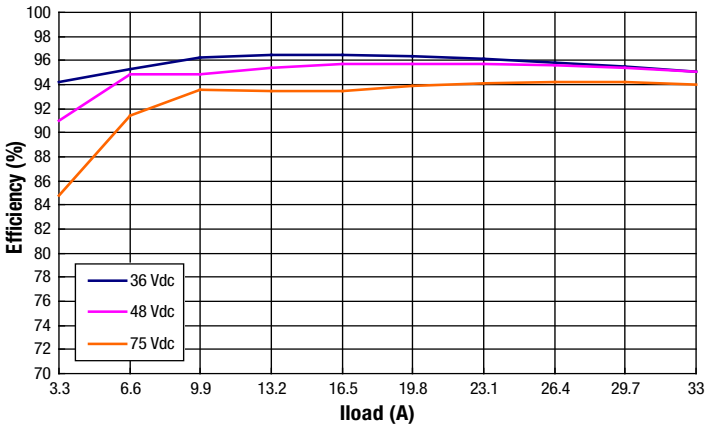


Figure 2. Baseplate Current Derating Max Baseplate Temperature (Vin=48V, tested on 10x10 inch PCB)

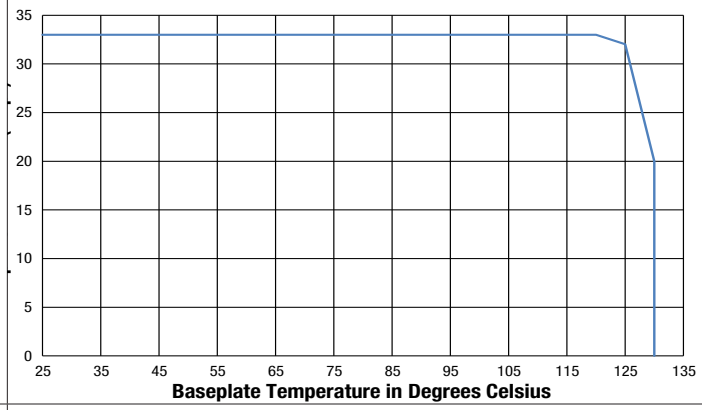


Figure 3. Maximum Current Temperature Derating with Baseplate (Vin=48V, airflow is from Vin to Vout on 10x10 inch PCB)

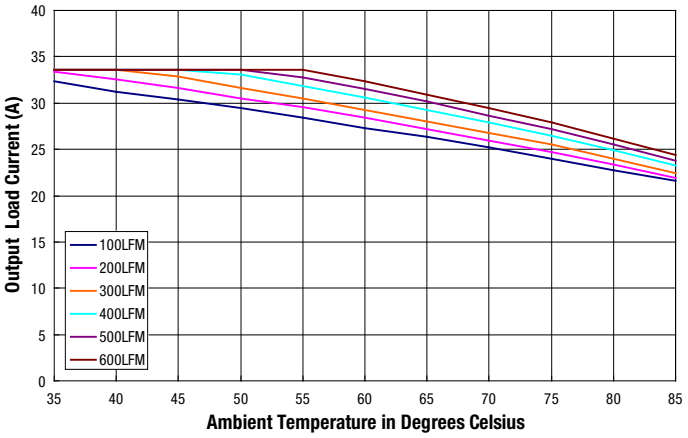


Figure 4. Maximum Power Temperature Derating with Baseplate (Vin=48V, airflow is from Vin to Vout on 10x10 inch PCB)

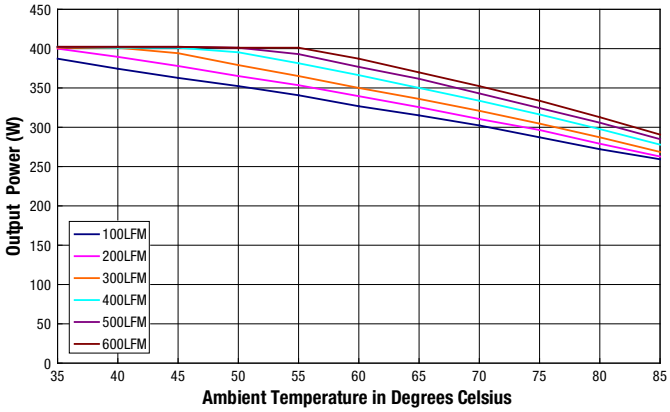


Figure 5. Maximum Current Temperature Derating with Baseplate (Vin=48V, airflow is from Vin- to Vin+ on 10x10 inch PCB)

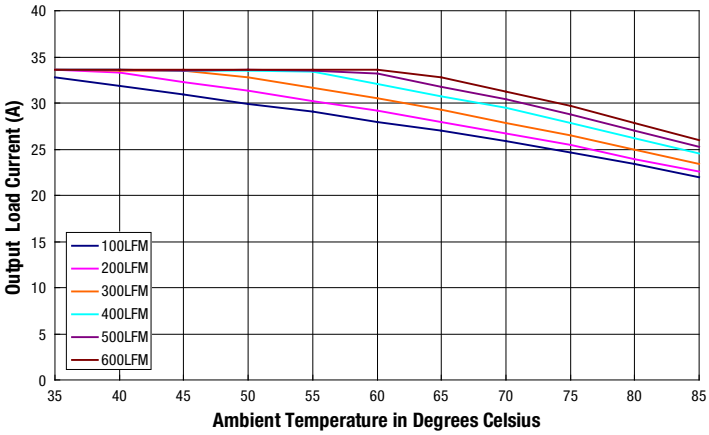
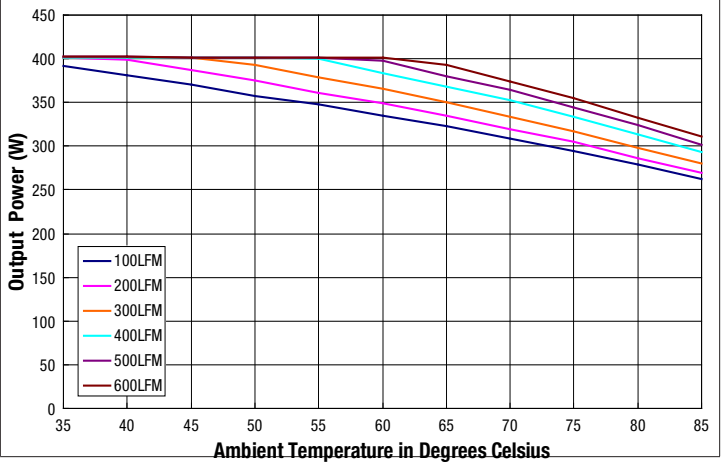


Figure 6. Maximum Power Temperature Derating with Baseplate (Vin=48V, airflow is from Vin- to Vin+ on 10x10 inch PCB)



PERFORMANCE DATA

Figure 7. Ripple/Noise @+25°C
(Vin=48V, Iout=0A, Cload=330µF, ScopeBW=20MHz, 2µS/div, 20mV/div)

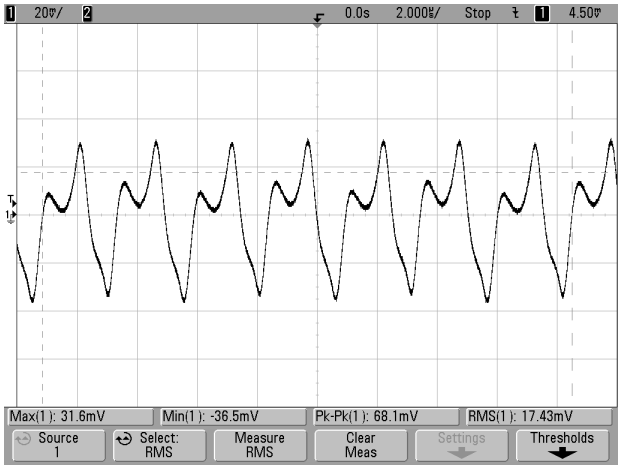


Figure 8. Ripple/Noise @+25°C
(Vin=48V, Iout=33A, Cload=330µF, ScopeBW=20MHz, 2µS/div, 20mV/div)

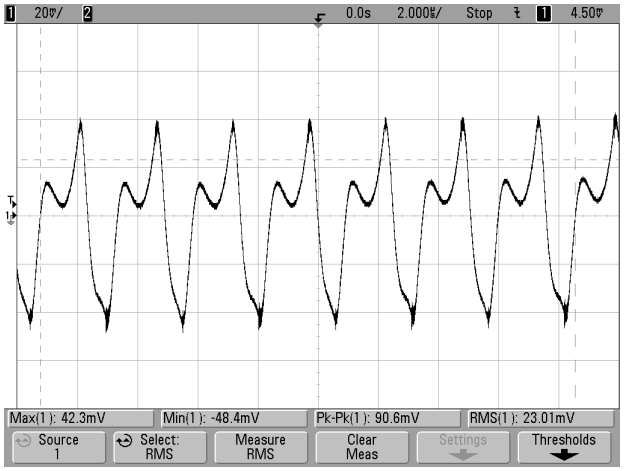


Figure 9. Enable Start-up Delay (CH2: Vout, CH4: On/Off)
(Vin=48V, Iload=0A, Cload=5500µF, Ta=+25°C, 10mS/div)

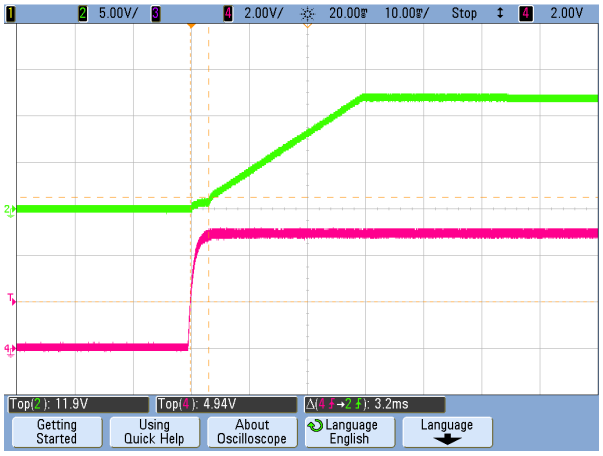


Figure 10. Enable Start-up Delay (CH2: Vout, CH4: On/Off)
(Vin=48V, Iload=33A, Cload=5500µF, Ta=+25°C, 10mS/div)

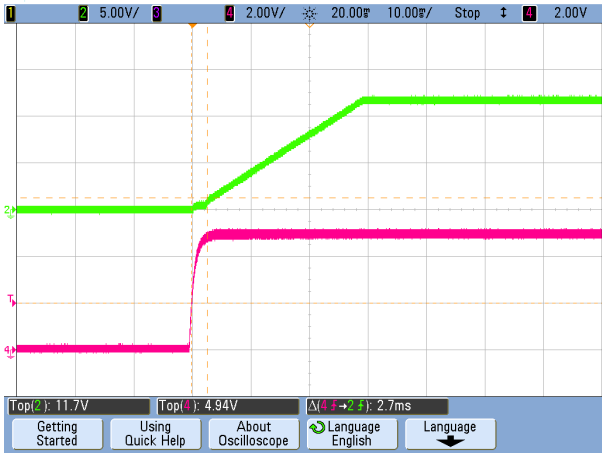


Figure 11. Vin Start-up Delay (CH2: Vout, CH1: Vin)
(Vin=48V, Iload=0A, Cload=5500µF, Ta=+25°C, 20mS/div)

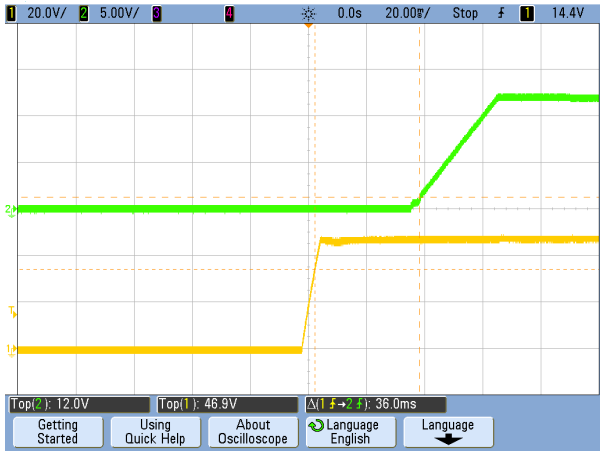


Figure 12. Vin Start-up Delay (CH2: Vout, CH1: Vin)
(Vin=48V, Iload=33A, Cload=5500µF, Ta=+25°C, 20mS/div)

PERFORMANCE DATA

Figure 13. Output Pre-bias start-up
Vin=48V, Iout = 0A, Cload = 47µF, Ta = 25°C, Pre-bias Voltage = 6V

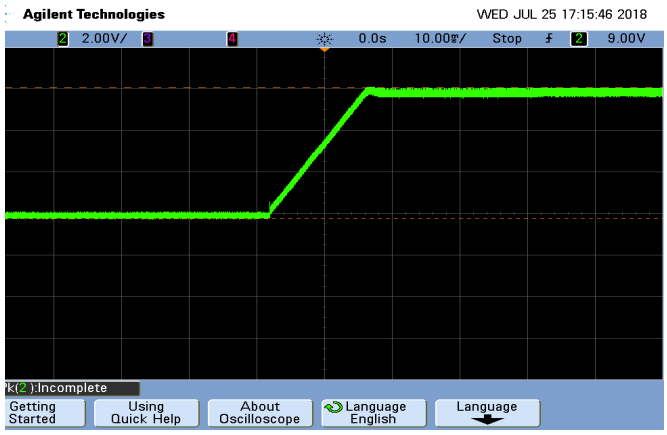


Figure 14. Output Pre-bias start-up
Vin=48V, Iout = 0A, Cload = 10000µF, Ta = 25°C, Pre-bias Voltage = 6V

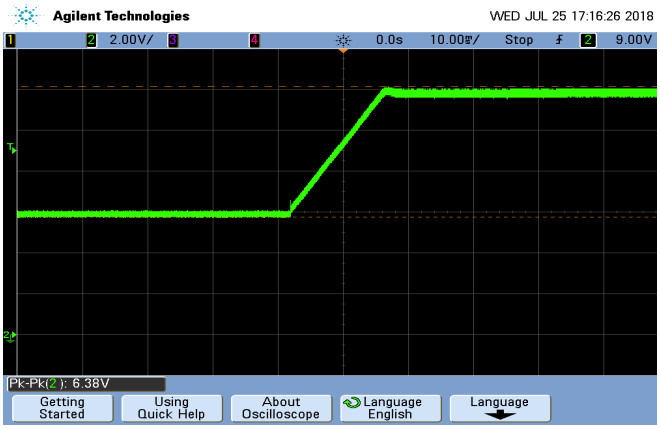


Figure 15. Output Pre-bias start-up
Vin=48V, Iout = 0A, Cload = 47µF, Ta = 25°C, Pre-bias Voltage = 9.6V

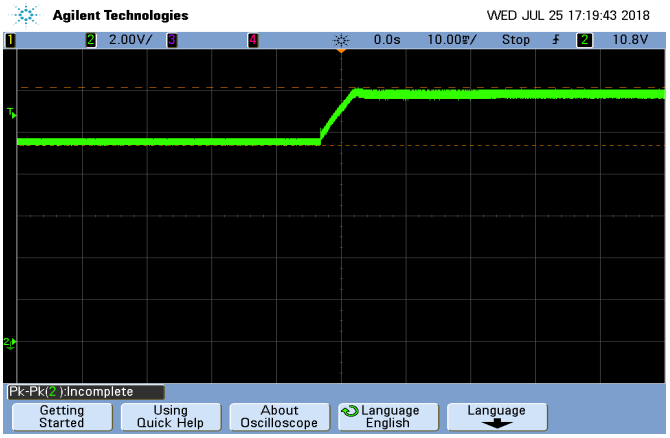


Figure 16. Output Pre-bias start-up
Vin=48V, Iout = 0A, Cload = 10000µF, Ta = 25°C, Pre-bias Voltage = 9.6V

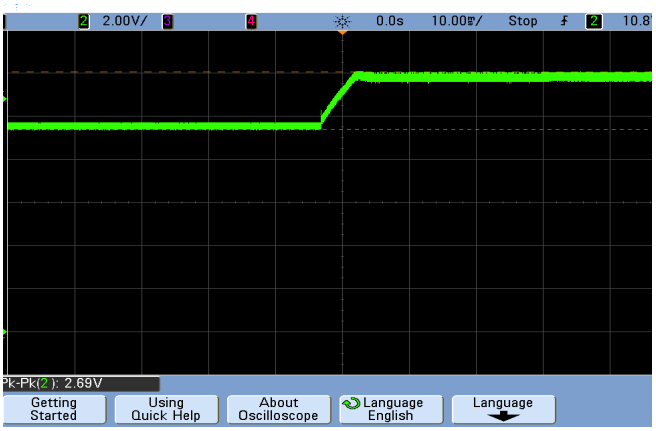
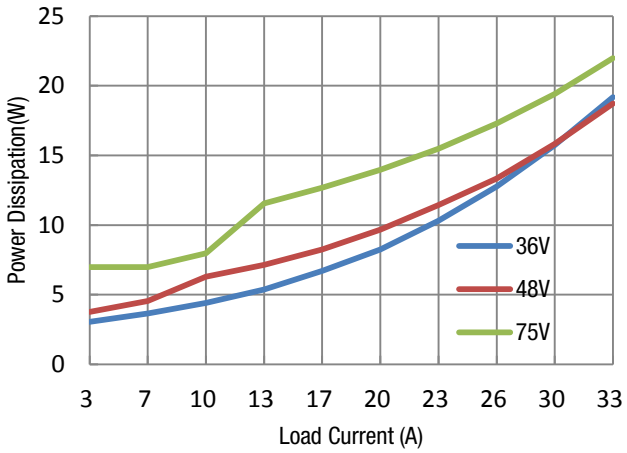
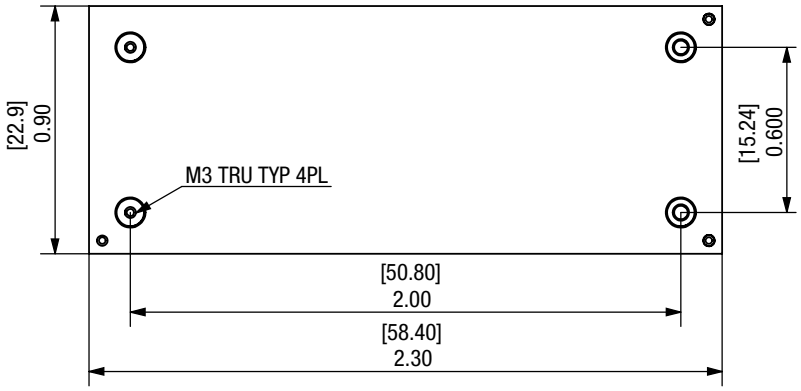


Figure 16. Power dissipation Vs Current Load

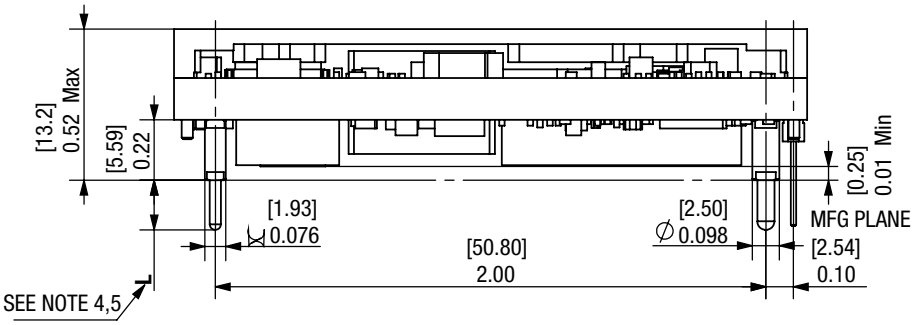


MECHANICAL SPECIFICATIONS

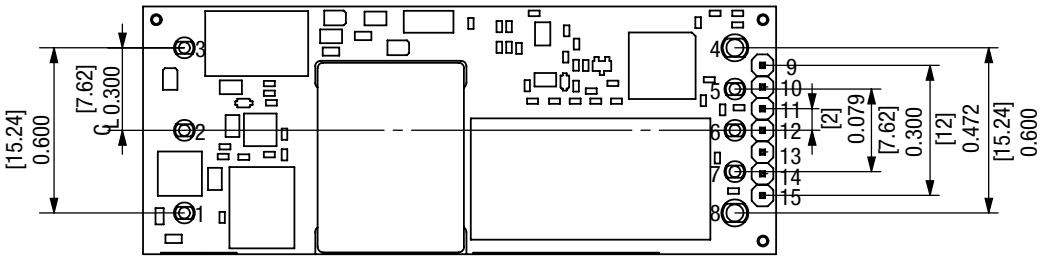
TOP VIEW



SIDE VIEW



BOTTOM VIEW

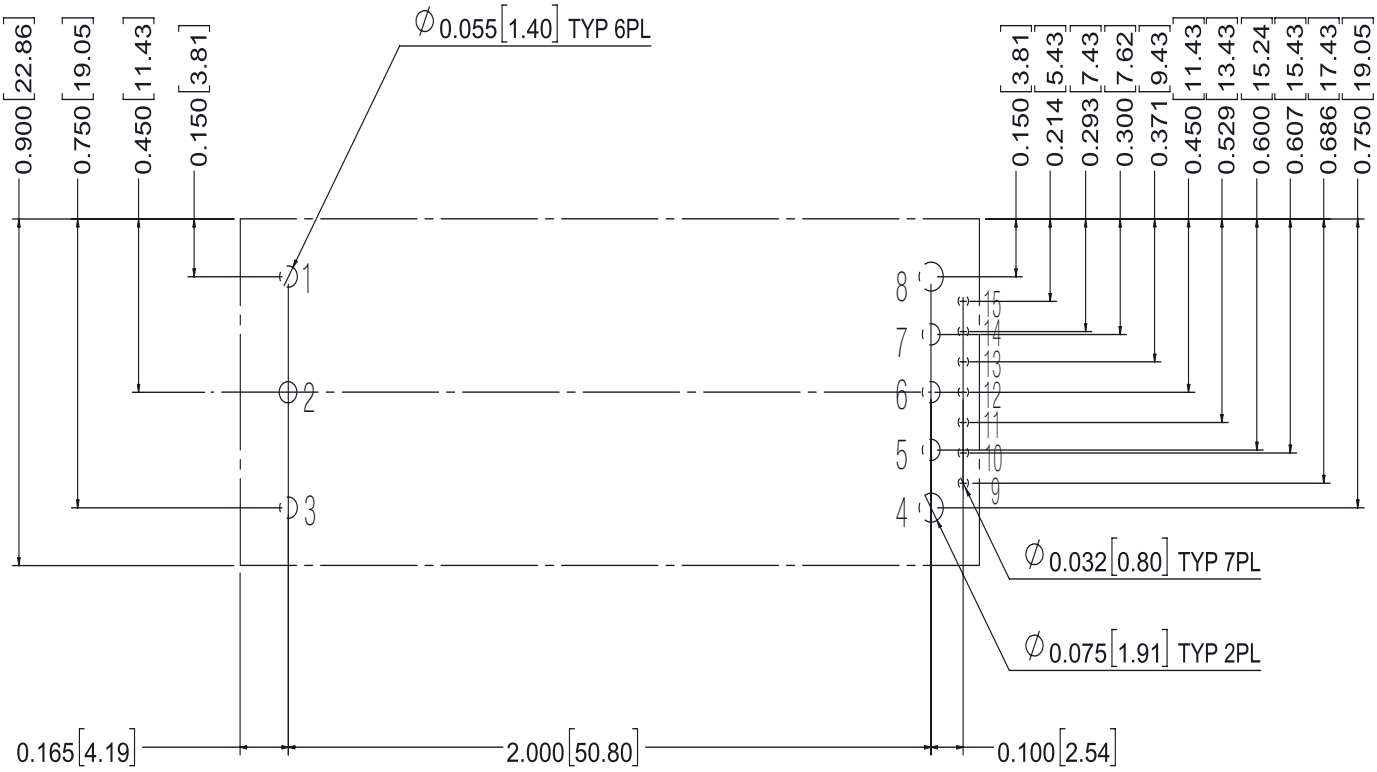


Pin Material
Pin No.1-3:5-7 Dia 0.04",Copper Alloy
Pin No. 4,8: Dia 0.06", Copper Alloy
Pin No. 9-15: Squar 0.02" x 0.02", Copper Alloy
Finish: (All Pins)
Gold (5u"Min) OverNickel (100u"Min)

NOTES:
UNLESS OTHERWISE SPECIFIED
[1] M3 SCREW USED TO BOLT UNIT'S BASEPLATE TO OTHER SURFACES (SUCH AS HEATSINK) MUST NOT EXCEED 0.110" (2.8mm) DEPTH BELOW THE SURFACE OF BASEPLATE.
[2] APPLIED TORQUE PER SCREW SHOULD NOT EXCEED 5.3In-lb (0.6Nm).
[3] ALL DIMENSION ARE IN INCHES (MILIMETER).
[4] STANDARD PIN LENGTH: 0.180Inch (4.57mm).
[5] OTHER PIN LENGTH OPTIONS: 1 = 0.110"(2.79mm), 2 = 0.145"(3.68mm), 3 = 0.220"(5.58mm).
[6] ALL TOLERANCES: x.xxin, ±0.02in (x.xmm,±0.5mm)
x.xxxin, ±0.01in (x.xxmm, ±0.25mm).
[7] COMPONENTS WILL VARY BETWEEN MODELS.

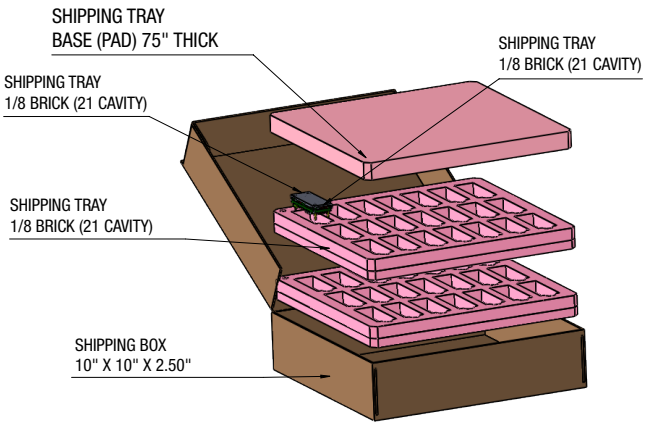
INPUT/OUTPUT CONNECTIONS				
PIN	FUNCTION	DSE	DAE	DCE
1	Vin+	•	•	•
2	On/Off	•	•	•
3	Vin-	•	•	•
4	Vout-	•	•	•
5	Sense-	•	•	
6	Trim/C1	•	•	
7	Sense+	•	•	
8	Vout+	•	•	•
9	C2	•		
10	Sig_Gnd	•		
11	Data	•		
12	SMBAlert	•		
13	Clock	•		
14	Addr1	•		
15	Addr0	•		

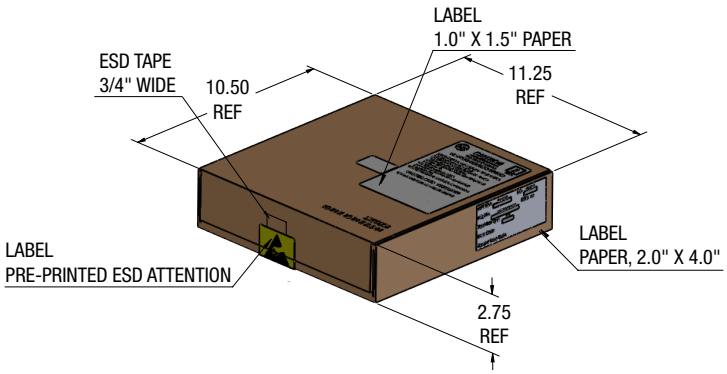
Please refer to the part number structure for alternate pin lengths.



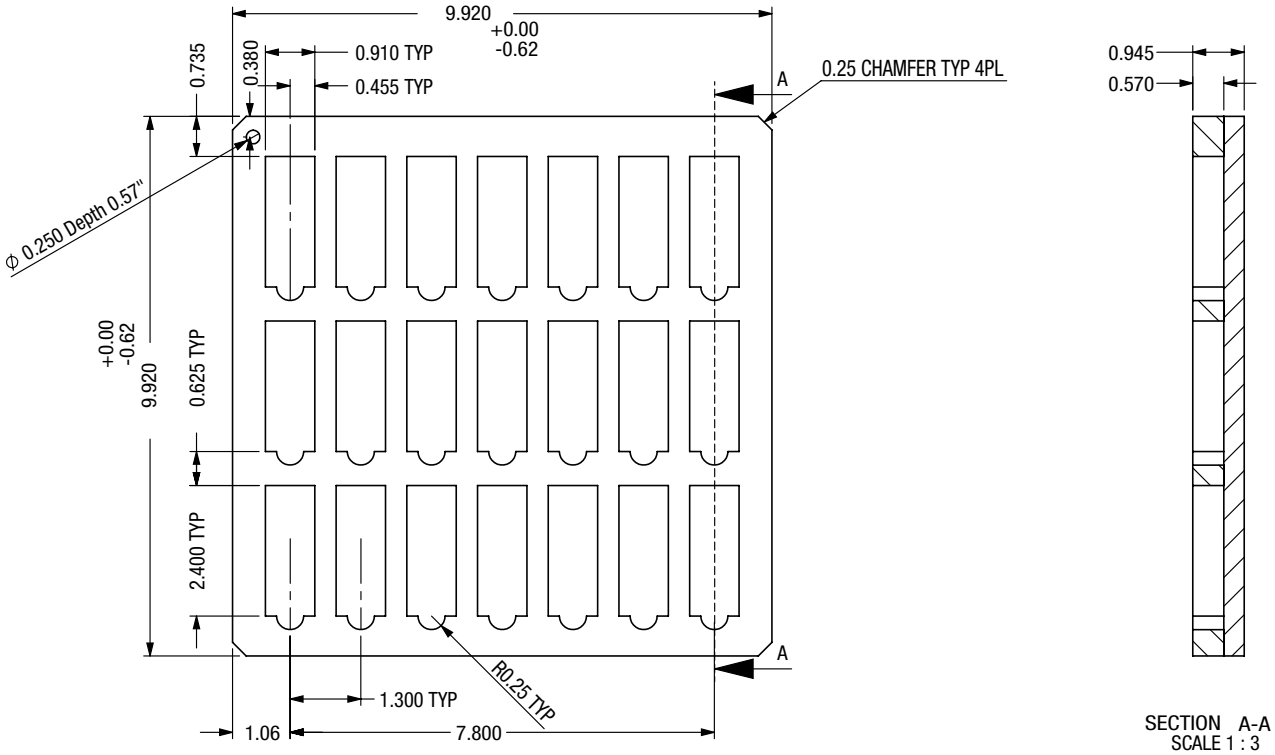
INPUT/OUTPUT CONNECTIONS				
PIN	FUNCTION	DSE	DAE	DCE
1	Vin+	•	•	•
2	On/Off	•	•	•
3	Vin-	•	•	•
4	Vout-	•	•	•
5	Sense-	•	•	
6	Trim/C1	•	•	
7	Sense+	•	•	
8	Vout+	•	•	•
9	C2	•		
10	Sig_Gnd	•		
11	Data	•		
12	SMBAlert	•		
13	Clock	•		
14	Addr1	•		
15	Addr0	•		

SHIPPING TRAYS AND BOXES, THROUGH-HOLE MOUNT





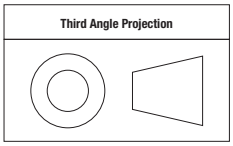
SHIPPING TRAY DIMENSIONS



Notes:

[1] THIS DOCUMENT DEFINES THE GENERAL PACKING RULES FOR APPLICABLE SHIPPING KIT. INFORMATION FOR SEALING AND MARKING IS NOT PART OF THIS DOCUMENT.
[2] REFER TO SHIPPING KIT BOM DETAILS.
[3] INSERT UNITS INTO FOAM POCKETS IN TRAYS APPROX AS SHOWN
[4] EACH FOAM TRAY CONTAINS 21 UNITS. IN FULL MPQ QUANTITIES, TWO TRAYS EQUAL A TOTAL OF 42 (2x21) UNITS PER BOX.
[5] FRONT FLAP SHALL BE SEALED WITH ESD TAPE SPECIFIED OR EQUIVALENT AFTER THE BOX IS CLOSED.
[6] MANUFACTURER TO APPLY LABEL ON 'SHORT' SIDE PANEL TOWARDS THE BACK AS SHOWN.
[7] APPLY ESD LABEL OVER TAPE USED TO SEAL BOX AND APPLY IDENTIFICATION LABEL APPROX AS SHOWN.
[8] PAD MAY, AT MFR'S OPTION, BE EXCHANGED FOR THINNER PAD IF FOAM STACKUP EXCEEDS CARTON HEIGHT BY >1/8" OR ADDITIONAL PAD MAY BE ADDED IF STACKUP IS BELOW INSIDE CARTON HEIGHT BY >1/8"

Dimensions are in inch.



Tolerances (unless otherwise specified):
.XX ± 0.032 (0.5)
.XXX ± 0.015 (0.25)
Angles ± 1˚

Components are shown for reference only.

TECHNICAL NOTES

Power Management Overview

The module includes a wide range of readable and configurable power management features that are easy to implement with a minimum of external components. Furthermore, the module includes protection features that continuously protect the load from damage due to unexpected system faults. The SMBALERT pin alerts the host if there is a fault in the module. The following product parameters can continuously be monitored by a host: Vout, Iout, Vin, Temperature, and Power Good. The module is distributed with a default configuration suitable for a wide range operation in terms of Vin, Vout, and load. All power management functions can be reconfigured using the PMBus interface. The product provides a PMBus digital interface that enables the user to configure many aspects of the device operation as well as monitor the input and output parameters. Please contact our FAE for special configurations.

Soft-start Power Up

The default rise time of the ramp up is 20 ms. When starting by applying input voltage the control circuit boot-up time adds an additional 10 ms delay. The soft-start power up of the module can be reconfigured using the PMBus interface.

Over Voltage Protection (OVP)

The module includes over voltage limiting circuitry for protection of the load. The default OVP limit is 20% above the nominal output voltage. If the output voltage surpasses the OVP limit, the module can respond in different ways. The default response from an over voltage fault is to immediately shut down. The device will continuously check for the presence of the fault condition, and when the fault condition no longer exists the device will be re-enabled. The OVP fault level and fault response can be reconfigured using the PMBus interface.

Over Current Protection (OCP, Current limit)

The module includes current limiting circuitry for protection at continuous over load. The default setting for the product is hiccup mode. The current limit could be configured by simply setting the IOUT_OC_FAULT_LIMIT to be greater than the IOUT_OC_WARN_LIMIT. The maximum value that the current limit could be set is 50A.

Power Good

The module provides Power Good (PG) flag in the Status Word register that indicates the output voltage is within a specified tolerance of its target level and no fault condition exists. The Power Good pin default logic is negative and it can be configured by MFR_PGOOD_POLARITY.

PMBus Interface

This module offers a PMBus digital interface that enables the user to configure many characteristics of the device operation as well as to monitor the input and output voltages, output current and device temperature. The module can be used with any standard two-wire I2C or SMBus host device. In addition, the module is compatible with PMBus version 1.2 and includes an SMBALERT line to help alleviate bandwidth limitations related to continuous fault monitoring. The module supports 100 kHz and 400 kHz bus clock frequency only.

Monitoring via PMBus

A system controller (host device) can monitor a wide variety of parameters through the PMBus interface. The controller can monitor fault conditions by monitoring the SMBALERT pin, which will be asserted when any number of pre-configured fault or warning conditions occurs. The system controller can also continuously monitor any number of power conversion parameters including but not limited to the following:

[1] Input voltage

[2] Output voltage

[3] Output current

[4] Module temperature

Software Tools for Design and Production

For these modules, Murata-PS provides software for configuring and monitoring via the PMBus interface. For more information please contact your local Murata-PS representative.

PMBus Addressing

Figure and the accompanying table display the recommended resistor values for hard-wiring PMBus addresses (1% tolerance resistors recommended): The address is set in the form of two octal (0 to 7) digits, with each pin setting one digit. The resistor value for each digit is shown below.

The SA0 and SA1 pins can be configured with a resistor to GND according to the following equation.

PMBus Address = 8 x (SA0value) + (SA1 value)

If the calculated PMBus address is 0d, 11d or 12d, PMBus address 119d is assigned instead. From a system point of view, the user shall also be aware of further limitations of the addresses as stated in the PMBus Specification. It is not recommended to keep the SA0 and SA1 pins left open.

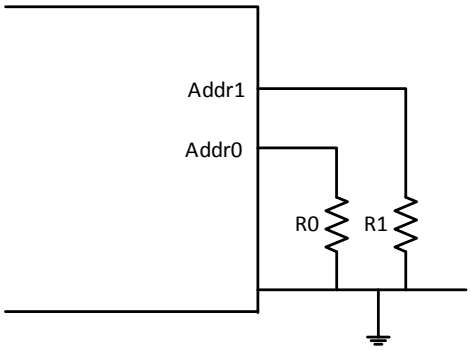


Figure 13. Schematic of Connection of Address Resistors

Digit (SA0, SA1 index)	Resistor Value [kΩ]
0	10
1	22
2	33
3	47
4	68
5	100
6	150
7	220

PMBus Commands

The products are designed to be PMBus compliant. The following tables list the implemented PMBus read commands. For more detailed information see **"PMBus Power System MAnagement Protocol Specification, Part I – General Requirements, Transport and Electrical Interface" and "PMBus Power System Management Protocol, Part II – Command Language."**

Parallel Load Sharing (S Option, Droop Load Sharing)

Two or more converters may be connected in parallel at both the input and output terminals to support higher output current or to improve reliability due to the reduced stress that result when the modules are operating below their rated limits. For applications requiring current share, followed the guidelines below. The products have a pre-configured voltage droop. The stated output voltage set point is at no load. The output voltage will decrease when the load current is increased. The voltage will drop 0.35V while load reaches max load. Our goal is to have each converter contribute nearly identical current into the output load under all input, environmental and load conditions.

CAUTION: This converter is not internally fused. To avoid danger to persons or equipment and to retain safety certification, the user must connect an external fast-blow input fuse as listed in the specifications. Be sure that the PC board pad area and etch size are adequate to provide enough current so that the fuse will blow with an overload.

Using Parallel Connections – Redundancy (N+1)

The redundancy connections require external user supplied "OR"ing diodes or "OR"ing MOSFETs for reliability purposes. The diodes allow for an uninterruptable power system operation in case of a catastrophic failure (shorted output) by one of the converters.

The diodes should be identical part numbers to enhance balance between the converters. The default factory nominal voltage should be sufficiently matched between converters. The OR'ing diode system is the responsibility of the user. Be aware of the power levels applied to the diodes and possible heat sink requirements.

Schottky power diodes with approximately 0.3V drops or "OR"ing MOSFETs may be suitable in the loop whereas 0.7 V silicon power diodes may not be advisable. In the event of an internal device fault or failure of the mains power modules on the primary side, the other devices automatically take over the entire supply of the loads. In the basic N+1 power system, the "N" equals the number of modules required to fully power the system and "+1" equals one back-up module that will take over for a failed module. If the system consists of two power modules, each providing 50% of the total load power under normal operation and one module fails, another one delivers full power to the load. This means you can use smaller and less expensive power converters as the redundant elements, while achieving the goal of increased availability.

Thermal Shutdown

Extended operation at excessive temperature will initiate over-temperature shutdown triggered by a temperature sensor outside the PWM controller. This operates similarly to overcurrent and short circuit mode. The inception point of the over-temperature condition depends on the average power delivered, the ambient temperature and the extent of forced cooling airflow. Thermal shutdown uses only the hiccup mode (auto restart) and PMBus configurable hysteresis.

Start Up Considerations

When power is first applied to the DC-DC converter, there is some risk of startup difficulties if you do not have both low AC and DC impedance and adequate regulation of the input source. Make sure that your source supply does not allow the instantaneous input voltage to go below the minimum voltage at all times. Use a moderate size capacitor very close to the input terminals. You may need two or more parallel capacitors. A larger electrolytic or ceramic cap supplies the surge current and a smaller parallel low-ESR ceramic cap gives low AC impedance.

Remember that the input current is carried both by the wiring and the ground plane return. Make sure the ground plane uses adequate thickness copper. Run additional bus wire if necessary.

Input Fusing

Certain applications and/or safety agencies may require fuses at the inputs of power conversion components. Fuses should also be used when there is the possibility of sustained input voltage reversal which is not current-limited. For greatest safety, we recommend a fast blow fuse installed in the ungrounded input supply line.

Input Under-Voltage Shutdown and Start-Up Threshold

Converters will not begin to regulate properly until the rising input voltage exceeds and remains at the Start-Up Threshold Voltage (see Specifications). Once operating, converters will not turn off until the input voltage drops below the Under-Voltage Shutdown Limit. Subsequent restart will not occur until the input voltage rises again above the Start-Up Threshold. This built-in hysteresis prevents any unstable on/off operation at a single input voltage. The over/under-voltage fault level and fault response and hysteresis can be configured via the PMBus interface.

Start-Up Time

Start-Up Time (see Specifications) is the time interval between the point when the rising input voltage crosses the Start-Up Threshold and the output voltage enters and remains within its specified accuracy band.

These converters include a soft start circuit to control Vout ramp time, thereby limiting the input inrush current.

The On/Off Remote Control interval from On command to Vout (final ±5%) assumes that the converter already has its input voltage stabilized above the Start-Up Threshold before the On command. The interval is measured from the

On command until the output enters and remains within its specified accuracy band.

Recommended Input Filtering

The user must assure that the input source has low AC impedance to provide dynamic stability and that the input supply has little or no inductive content, including long distributed wiring to a remote power supply. The converter will operate with no additional external capacitance if these conditions are met.

For best performance, we recommend installing a low-ESR capacitor immediately adjacent to the converter's input terminals. The capacitor should be a ceramic type such as the Murata GRM32 series or a polymer type. More input bulk capacitance may be added in parallel (either electrolytic or tantalum) if needed.

Recommended Output Filtering

The converter will achieve its rated output ripple and noise with no additional external capacitor. However, the user may install more external output capacitance to reduce the ripple even further or for improved dynamic response. Again, use low-ESR ceramic (Murata GRM32 series) or polymer capacitors. Mount these close to the converter. Measure the output ripple under your load conditions.

Use only as much capacitance as required to achieve your ripple and noise objectives. Excessive capacitance can make step load recovery sluggish or possibly introduce instability. Do not exceed the maximum rated output capacitance listed in the specifications.

Input Ripple Current and Output Noise

All models in this converter series are tested and specified for input reflected ripple current and output noise using designated external input/output components, circuits and layout as shown in the figures below. The Cbus and Lbus components simulate a typical DC voltage bus.

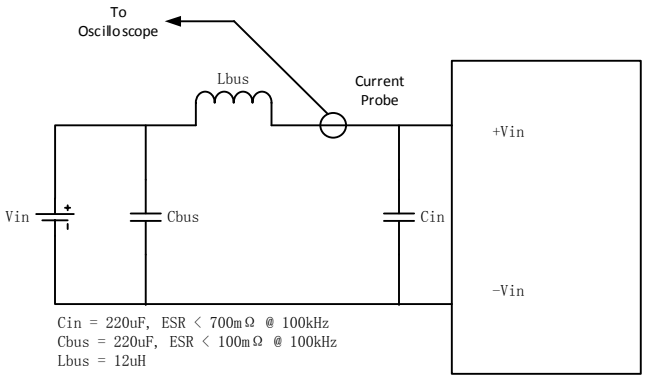


Figure 14. Measuring Input Ripple Current

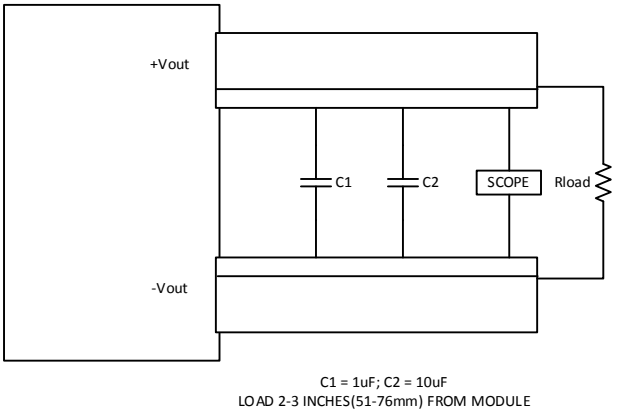


Figure 15. Measuring Output Ripple and Noise (PARD)

Minimum Output Loading Requirements

All models regulate within specification and are stable under no load to full load conditions.

Thermal Shutdown (OTP, UTP)

To prevent many over temperature problems and damage, these converters include thermal shutdown circuitry. If environmental conditions cause the temperature of the DC-DCs to rise above the Operating Temperature Range up to the shutdown temperature, an on-board electronic temperature sensor will power down the unit. When the temperature decreases below the turn-on threshold set in the command recover temp is (OT_FAULT_LIMIT-MFR_OT_FAULT_HYS), the hysteresis is defined in general electrical specification section. The OTP and hysteresis of the module can be reconfigured using the PMBus. The OTP and UTP fault limit and fault response can be configured via the PMBus.

CAUTION: If you operate too close to the thermal limits, the converter may shut down suddenly without warning. Be sure to thoroughly test your application to avoid unplanned thermal shutdown.

Temperature Derating Curves

The graphs in this data sheet illustrate typical operation under a variety of conditions. The Derating curves show the maximum continuous ambient air temperature and decreasing maximum output current which is acceptable under increasing forced airflow measured in Linear Feet per Minute ("LFM"). Note that these are AVERAGE measurements. The converter will accept brief increases in current or reduced airflow as long as the average is not exceeded.

Note that the temperatures are of the ambient airflow, not the converter itself which is obviously running at higher temperature than the outside air. Also note that "natural convection" is defined as very fl ow rates which are not using fan-forced airflow. Depending on the application, "natural convection" is usually about 30-65 LFM but is not equal to still air (0 LFM).

Murata Power Solutions makes Characterization measurements in a closed cycle wind tunnel with calibrated airflow. We use both thermocouples and an infrared camera system to observe thermal performance. As a practical matter, it is quite difficult to insert an anemometer to precisely measure airflow in most applications. Sometimes it is possible to estimate the effective airflow if

you thoroughly understand the enclosure geometry, entry/exit orifice areas and the fan flowrate specifications.

CAUTION: If you exceed these Derating guidelines, the converter may have an unplanned Over Temperature shut down. Also, these graphs are all collected near Sea Level altitude. Be sure to reduce the derating for higher altitude.

Output Short Circuit Condition

The short circuit condition is an extension of the "Current Limiting" condition. When the monitored peak current signal reaches a certain range, the PWM controller's outputs are shut off thereby turning the converter "off." This is followed by an extended time out period. This period can vary depending on other conditions such as the input voltage level. Following this time out period, the PWM controller will attempt to re-start the converter by initiating a "normal start cycle" which includes softstart. If the "fault condition" persists, another "hiccup" cycle is initiated. This "cycle" can and will continue indefinitely until such time as the "fault condition" is removed, at which time the converter will resume "normal operation." Operating in the "hiccup" mode during a fault condition is advantageous in that average input and output power levels are held low preventing excessive internal increases in temperature.

Remote On/Off Control

The DSE series modules are equipped with both primary (On/Off 1, enabled, pull up internal) and secondary (On/Off 2, disabled, pull up internal) control pins for increased system flexibility. Both are configurable via PMBus. The On/Off pins are TTL open-collector and/or CMOS open-drain compatible. (See general specifications for threshold voltage levels. See also MFR_PRIMARY_ON_OFF_CONFIG section.)

Negative-logic models are on (enabled) when the On/Off is grounded or brought to within a low voltage (see specifications) with respect to –Vin. The device is off (disabled) when the On/Off is left open or is pulled high to +13.5Vdc with respect to –Vin. The On/Off function allows the module to be turned on/off by an external device switch.

Positive-logic models are enabled when the On/Off pin is left open or is pulled high to +13.5V with respect to –Vin. Positive-logic devices are disabled when the On/Off is grounded or brought to within a low voltage (see specifications) with respect to –Vin. For voltage levels for On/Off 2 signal see functional specifications.

The restart delay for this module to turn On/Off by the On/Off control pin is 200ms.

On/Off 1 can be configured by PMBus command MFR_PRIMARY_ON_OFF_CONFIG (DDh); default configuration is not ignored; required On/Off 1 control pin to be asserted to start the unit.

On/Off 2 can be configured by PMBUS command ON_OFF_CONFIG (02h); default configuration is ignored; treat it as always ON.

DSE's On/Off status is dependent on On/Off 1 control, On/Off 2 control, and OPERATION (PMBus command) status; all three must be ON to turn DSE on; if one of them is OFF, unit will be turned off.

Output Capacitive Load

These converters do not require external capacitance added to achieve rated specifications. Users should only consider adding capacitance to reduce switching noise and/or to handle spike current load steps. Install only enough capacitance to achieve noise objectives. Excess external capacitance may cause degraded transient response and possible oscillation or instability.

Remote Sense Input

Use the Sense inputs with caution. Sense is normally connected at the load. Sense inputs compensate for output voltage inaccuracy delivered at the load. This is done by correcting IR voltage drops along the output wiring and the current carrying capacity of PC board etches. This output drop (the difference between Sense and Vout when measured at the converter) should not exceed 0.5V. Consider using heavier wire if this drop is excessive. Sense inputs also improve the stability of the converter and load system by optimizing the control loop phase margin.

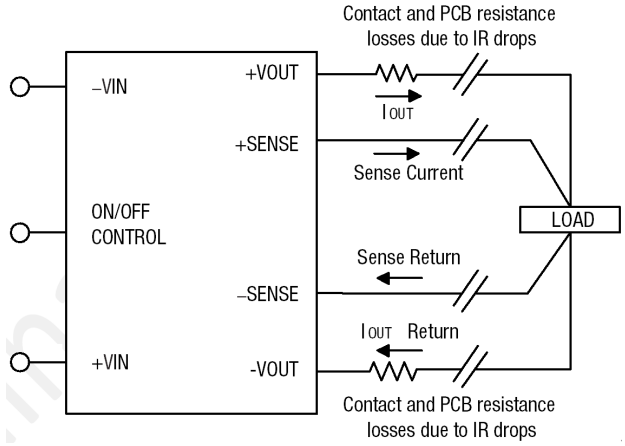


Figure 16. Remote Sense Circuit Configuration

Note: The Sense input and power Vout lines are internally connected through low value resistors to their respective polarities so that the converter can operate without external connection to the Sense. Nevertheless, if the Sense function is not used for remote regulation, the user should connect +Sense to +Vout and –Sense to –Vout at the converter pins.

The remote Sense lines carry very little current. They are also capacitively coupled to the output lines and therefore are in the feedback control loop to regulate and stabilize the output. As such, they are not low impedance inputs and must be treated with care in PC board layouts. Sense lines on the PCB should run adjacent to DC signals, preferably Ground. In cables and discrete wiring, use twisted pair, shielded tubing or similar techniques.

Any long, distributed wiring and/or significant inductance introduced into the Sense control loop can adversely affect overall system stability. If in doubt, test your applications by observing the converter's output transient response during step loads. There should not be any appreciable ringing or oscillation. You may also adjust the output trim slightly to compensate for voltage loss in any external filter elements. Do not exceed maximum power ratings.

Please observe Sense inputs tolerance to avoid improper operation:

[Vout(+) –Vout(-)] – [Sense(+) –Sense(-)] ≤ 5% of Vout

Output overvoltage protection is monitored at the output voltage pin, not the Sense pin. Therefore excessive voltage differences between Vout and Sense together with trim adjustment of the output can cause the overvoltage protection circuit to activate and shut down the output.

Power derating of the converter is based on the combination of maximum output current and the highest output voltage. Therefore the designer must ensure:

(Vout at pins) x (Iout) ≤ (Max. rated output power)

Soldering Guidelines

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Be cautious when there is high atmospheric humidity. We strongly recommend a mild pre-bake (100° C. for 30 minutes). Your production environment may differ; therefore please thoroughly review these guidelines with your process engineers.

Wave Solder Operation for Through-Hole Mounted Products (THMT)	
For Sn/Ag/Cu based solders:	
Maximum Preheat Temperature	115
Maximum Pot Temperature	270
Maximum Solder Dwell Time	7 seconds
For Sn/Pb based solders:	
Maximum Preheat Temperature	105
Maximum Pot Temperature	250
Maximum Solder Dwell Time	6 seconds

PIH Soldering Profile

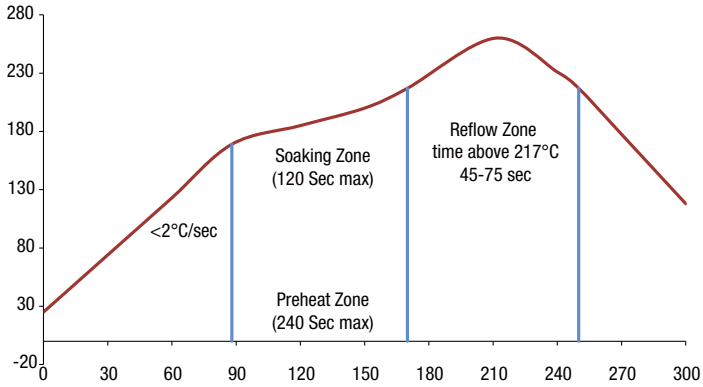
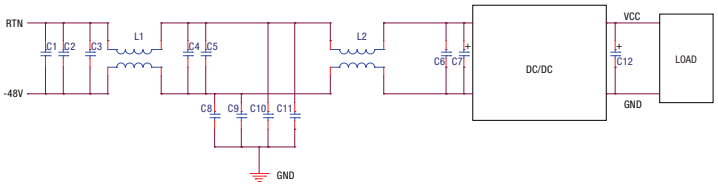


Figure 18. PIH Soldering Profile

Emissions Performance

Murata Power Solutions measures its products for conducted emissions against the EN 55022 and CISPR 22 standards. Passive resistance loads are employed and the output is set to the maximum voltage. If you set up your own emissions testing, make sure the output load is rated at continuous power while doing the tests.

The recommended external input and output capacitors (if required) are included. Please refer to the fundamental switching frequency. All of this information is listed in the Product Specifications. An external discrete filter is installed and the circuit diagram is shown below.



[1] Conducted Emissions Parts List

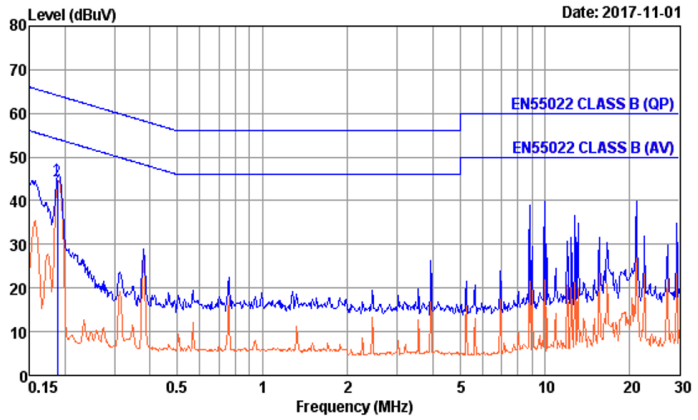
Reference	Part Number	Description	Vendor
C1, C2, C3, C4, C5	GRM32ER72A105KA01L	SMD CERAMIC-100V-1000nF-X7R-1210	Murata
C6	GRM319R72A104KA01D	SMD CERAMIC100V-100nF-±10%-X7R-1206	Murata
L1, L2	PG0060T	COMMON MODE-473uH-±25%-14A	Pulse
C8, C9, C10, C11	GRM55DR72J224KW01L	SMD CERAMIC630V-0.22uF-±10%-X7R-2220	Murata
C7	UHE2A221MHD	Aluminum100V-220Uf-±10%-long lead	Nichicon
C12	NA		

[2] Conducted Emissions Test Equipment Used

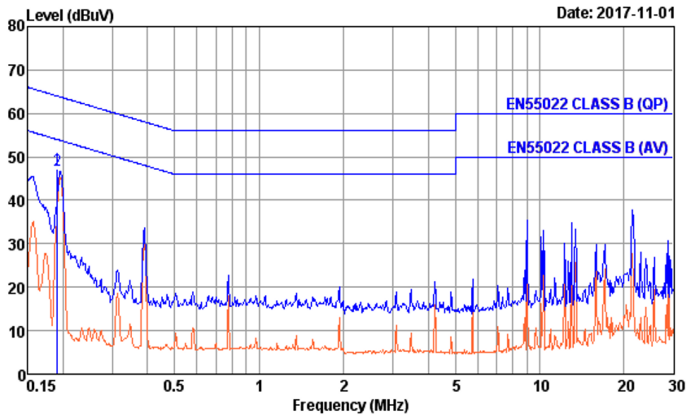
Hewlett Packard HP8594L Spectrum Analyzer – S/N 3827A00153

2Line V-networks LS1-15V 50Ω/50Uh Line Impedance Stabilization Network

[3] Conducted Emissions Test Results



Graph 1. Conducted emissions performance, Positive Line, CISPR 22, Class B, full load



Graph 2. Conducted emissions performance, Negative Line, CISPR 22, Class B, full load

[4] Layout Recommendations

Most applications can use the filtering which is already installed inside the converter or with the addition of the recommended external capacitors. For greater emissions suppression, consider additional filter components and/or shielding. Emissions performance will depend on the user's PC board layout, the chassis shielding environment and choice of external components. Please refer to Application Note GEAN-02 for further discussion.

Since many factors affect both the amplitude and spectra of emissions, we recommend using an engineer who is experienced at emissions suppression.

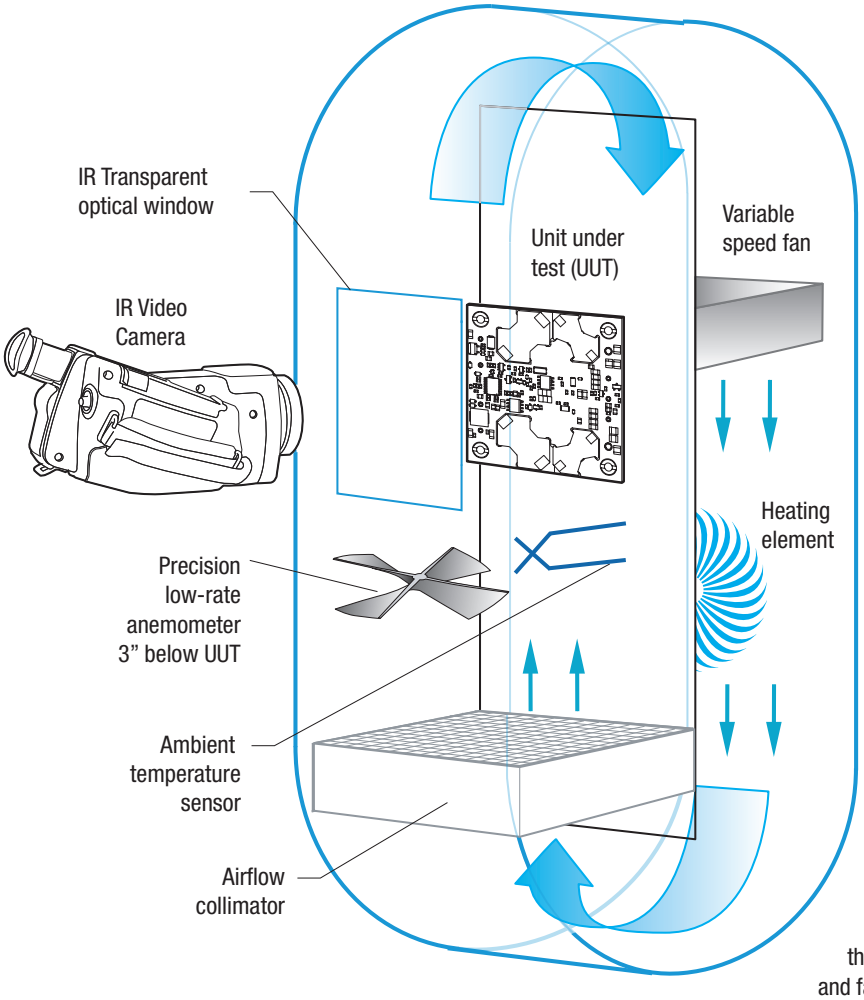


Figure 16. Vertical Wind Tunnel

Vertical Wind Tunnel

Murata Power Solutions employs a computer controlled custom-designed closed loop vertical wind tunnel, infrared video camera system, and test instrumentation for accurate airflow and heat dissipation analysis of power products. The system includes a precision low flow-rate anemometer, variable speed fan, power supply input and load controls, temperature gauges, and adjustable heating element.

The IR camera monitors the thermal performance of the Unit Under Test (UUT) under static steady-state conditions. A special optical port is used which is transparent to infrared wavelengths.

Both through-hole and surface mount converters are soldered down to a 10" x 10" host carrier board for realistic heat absorption and spreading. Both longitudinal and transverse airflow studies are possible by rotation of this carrier board since there are often significant differences in the heat dissipation in the two airflow directions. The combination of adjustable airflow, adjustable ambient heat, and adjustable Input/Output currents and voltages mean that a very wide range of measurement conditions can be studied.

The collimator reduces the amount of turbulence adjacent to the UUT by minimizing airflow turbulence. Such turbulence influences the effective heat transfer characteristics and gives false readings. Excess turbulence removes more heat from some surfaces and less heat from others, possibly causing uneven overheating.

Both sides of the UUT are studied since there are different thermal gradients on each side. The adjustable heating element and fan, built-in temperature gauges, and no-contact IR camera mean that power supplies are tested in real-world conditions.

Soldering Guidelines

Murata Power Solutions recommends the specifications below when installing these converters. These specifications vary depending on the solder type. Exceeding these specifications may cause damage to the product. Your production environment may differ; therefore please thoroughly review these guidelines with your process engineers.

Wave Solder Operations for through-hole mounted products (THMT)			
For Sn/Ag/Cu based solders:		**For Sn/Pb based solders:**	
Maximum Preheat Temperature	115° C.	Maximum Preheat Temperature	105° C.
Maximum Pot Temperature	270° C.	Maximum Pot Temperature	250° C.
Maximum Solder Dwell Time	7 seconds	Maximum Solder Dwell Time	6 seconds

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