

**ABSTRACT**

The LM5157EVM-FLY evaluation module showcases the features and performance of the LM5157 as wide input non-synchronous flyback controller to produce multiple output voltage rails for typical applications of the 3-phase inverter gate driver bias supplies. The standard configuration is designed to provide a regulated output of 10 V at 250 mA from an input of 8 V to 16 V, and three isolated 20-V rails at 75 mA, 75 mA, and 150 mA, respectively. The switching frequency is 250 kHz. This evaluation module was designed for ease of configuration, enabling the user to evaluate different applications on the same module. The PCB has two copper layers.

Table of Contents

1 Introduction.....	2
2 Electrical Parameters.....	3
3 Application Schematic.....	4
4 EVM Picture.....	5
5 Test Setup and Procedure.....	6
5.1 EVM Test Setup Schematic.....	6
5.2 Test Equipment	6
6 Test Data and Performance Curves	7
6.1 Efficiency.....	7
6.2 Output Regulation and Cross-Regulations.....	7
6.3 Steady-State Waveforms.....	8
6.4 Start-Up Waveforms.....	10
6.5 Dynamic Responses.....	10
6.6 Short-Circuit Protection.....	11
6.7 Bode Plots.....	11
6.8 Thermal Image.....	12
7 Schematic.....	13
8 Bill of Materials.....	14
9 EVM Layout.....	16

List of Figures

Figure 3-1. Application Circuit.....	4
Figure 4-1. EVM Picture.....	5
Figure 5-1. Test Setup.....	6
Figure 6-1. Efficiency Under Different Input Voltages and Load Levels.....	7
Figure 6-2. Main Output Voltage Regulation vs Main Output Load ($I_{O2}, I_{O3}, I_{O4} = 100\%$).....	7
Figure 6-3. Cross Regulation of Isolated Outputs vs Main Output Load ($I_{O2}, I_{O3}, I_{O4} = 10\%$).....	7
Figure 6-4. Main Output Voltage Regulation vs Input Voltage ($I_{O2}, I_{O3}, I_{O4} = 100\%$).....	7
Figure 6-5. Voltage Regulation of Isolated Outputs vs Input Voltage at 10% load.....	7
Figure 6-6. Voltage Regulation of Isolated Outputs vs Input Voltage at 50% load.....	8
Figure 6-7. Voltage Regulation of Isolated Outputs vs Input Voltage at 100% load.....	8
Figure 6-8. SW Waveform at 100% Load, Under 16-V Maximum V_{IN}	8
Figure 6-9. Zoomed SW Waveform at 100% Load, Under 16-V Maximum V_{IN}	8
Figure 6-10. SW Waveform at 10% Load, Under 16-V Maximum V_{IN}	8
Figure 6-11. PIV of D7 (main Output Rectifier) Under 100% Load and 16-V Maximum V_{IN}	8
Figure 6-12. PIV of D1 (Vo2 Rectifier) Under 100% Load and 16-V Maximum V_{IN}	9
Figure 6-13. PIV of D4 (Vo3 Rectifier) Under 100% Load and 16-V Maximum V_{IN}	9
Figure 6-14. PIV of D5 (Vo4 Rectifier) Under 100% Load and 16-V Maximum V_{IN}	9
Figure 6-15. Main Output Vo1 Ripple Voltage Under 250-mA Load.....	9
Figure 6-16. Isolated Output Vo4 Ripple Voltage Under 150-mA Load.....	9

Figure 6-17. Start-Up at 10% Load on Each Output Rail.....	10
Figure 6-18. Start-Up at 100% Load on Each Output Rail.....	10
Figure 6-19. Vo1 Response Under Step Load Between 125 mA and 250 mA, $V_{IN} = 12$ V, $I_{O2-4} = 10\%$	10
Figure 6-20. Vo1 Response Under Step Load Between 1 mA to 250 mA, $V_{IN} = 12$ V, $I_{O2-4} = 10\%$	10
Figure 6-21. Vo4 Response Under Step Load Between 100 mA and 150 mA, $V_{IN} = 12$ V, $I_{O1-3} = 10\%$	10
Figure 6-22. Vo1 Response Under Input Change From 8 V to 16 V, 10% Load.....	10
Figure 6-23. Vo1 Response Under Input Change From 16 V to 8 V, 10% Load.....	11
Figure 6-24. Short-Circuit Protection With Hiccup Mode Disabled, $V_{IN} = 12$ V ($R_{13} = 0 \Omega$).....	11
Figure 6-25. Short-Circuit Protection With Hiccup Mode Enabled, $V_{IN} = 12$ V ($R_{13} = 62 \text{ k}\Omega$, $C_{29} = 47 \text{ nF}$).....	11
Figure 6-26. Bode Plot ($V_{IN} = 12$ V).....	11
Figure 6-27. Bode Plot (Vo1-4: 100% Load).....	11
Figure 6-28. EVM Thermal Image at 12 V_{IN} and 100% Load.....	12
Figure 9-1. Top Silkscreen.....	16
Figure 9-2. Top Layer.....	16
Figure 9-3. Bottom Layer Viewed From Bottom.....	17
Figure 9-4. Bottom Silkscreen Viewed From Bottom.....	17

List of Tables

Table 2-1. Electrical Performance Standard Configuration.....	3
Table 8-1. LM5157EVM-FLY Bill of Materials.....	14

Trademarks

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1 Introduction

The LM5157EVM-FLY supports the following features and performance capabilities:

- Tightly-regulated main output voltage $Vo_1 = 10 \text{ V} \pm 1\%$
- Three cross-regulated, isolated output voltages:
 - $Vo_2 = 20 \text{ V}$
 - $Vo_3 = 20 \text{ V}$
 - $Vo_4 = 20 \text{ V}$
- Total rated load 8.5 W:
 - 250 mA at Vo_1
 - 75 mA at Vo_2
 - 75 mA at Vo_3
 - 150 mA at Vo_4
- Peak efficiency > 90%
- Optional hiccup mode overloading protection
- 250-kHz switching frequency
- 2-layer PCB, $3.5 \times 2.3 \text{ in}$

2 Electrical Parameters

Table 2-1 details the electrical performance standard configuration.

Table 2-1. Electrical Performance Standard Configuration

Parameter	Test Conditions	MIN	TYP	MAX	Unit
Input Characteristics					
Input voltage range V_{IN}	Operation	8	12	16	V
Abs max input voltage V_{INMAX}	Operation without damage			18	V
Input voltage turn-on $V_{IN(ON)}$	Adjustable by the UVLO/SYNC resistors		7.5		V
Input voltage turn-off $V_{IN(OFF)}$			7.3		V
Output Characteristics					
Main output voltage V_o1			10		V
1st Isolated output voltage V_o2			20		V
2nd Isolated output voltage V_o3			20		V
3rd Isolated output voltage V_o4			20		V
Main output load current I_o1				0.250	A
1st Isolated output load current I_o2				0.075	A
2nd Isolated output load current I_o3				0.075	A
3rd Isolated output load current I_o4				0.150	A
System Characteristics					
Switching Frequency			250		kHz
Peak Efficiency	$V_{IN} = 12$ V, rated load at all output rails		91		%
Junction Temperature		-40		150	°C
Transformer Specifications (Coilcraft ZB1324-AL)					
Primary Inductances			8		µH
Turns Ratio	(2-4) : (1-3)		1		
	(5-6) : (1-3)		1.2		
	(8-7) : (1-3)		2.4		
	(10-9) : (1-3)		2.4		
	(12-11) : (1-3)		2.4		
Saturation Current	20% inductance reduction		5.5		A
Leakage Inductance				90	nH

3 Application Schematic

The LM5157EVM-FLY is capable of multiple configurations. [Figure 3-1](#) shows the standard configuration of the LM5157EVM-FLY for which the parameters in [Table 2-1](#) are valid.

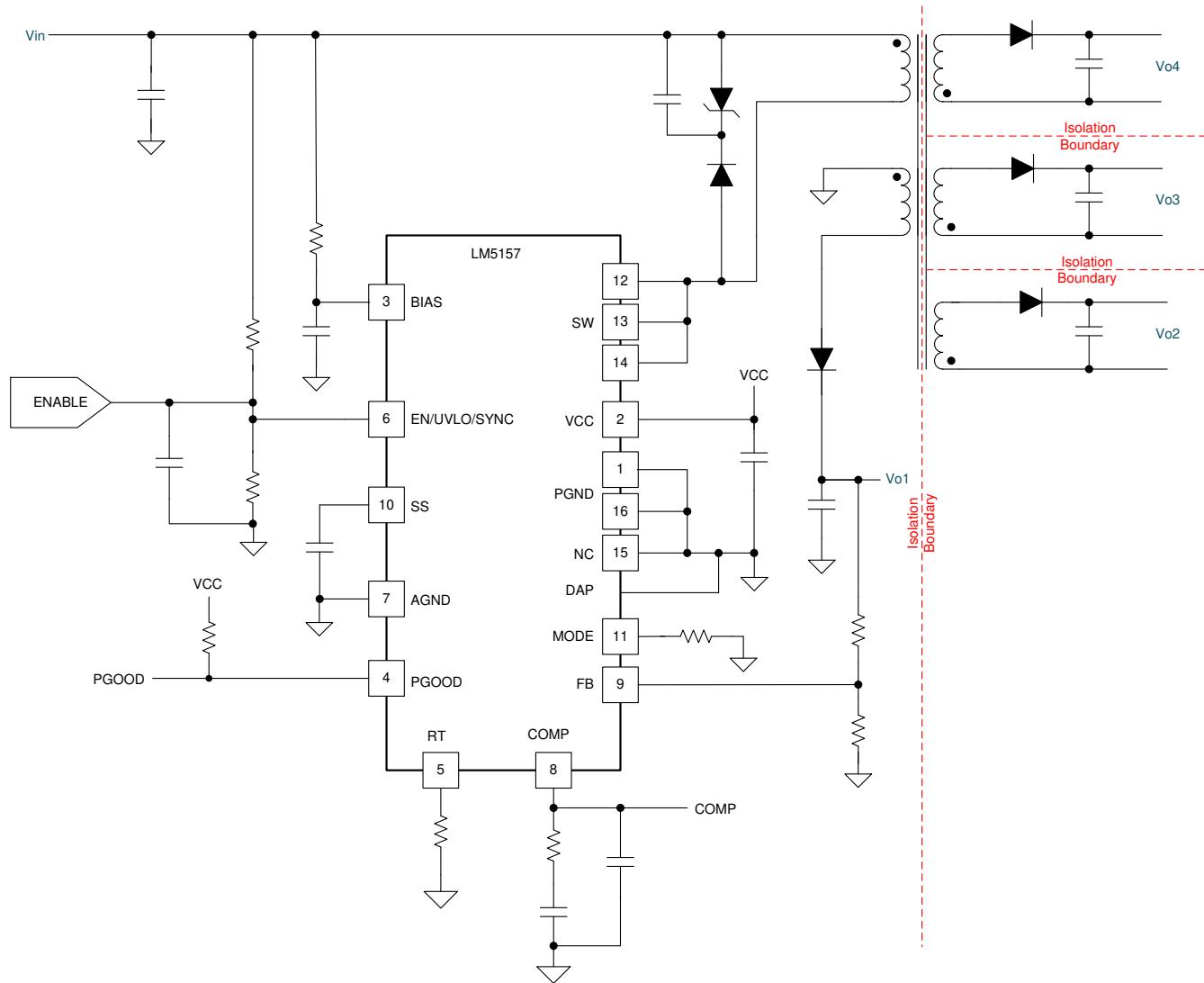


Figure 3-1. Application Circuit

4 EVM Picture

Figure 4-1 is a 3D printout of the EVM from the CAD tool. The actual color of the board may differ. The EVM also includes circuits for extra configurations. Not all components are populated on the EVM.

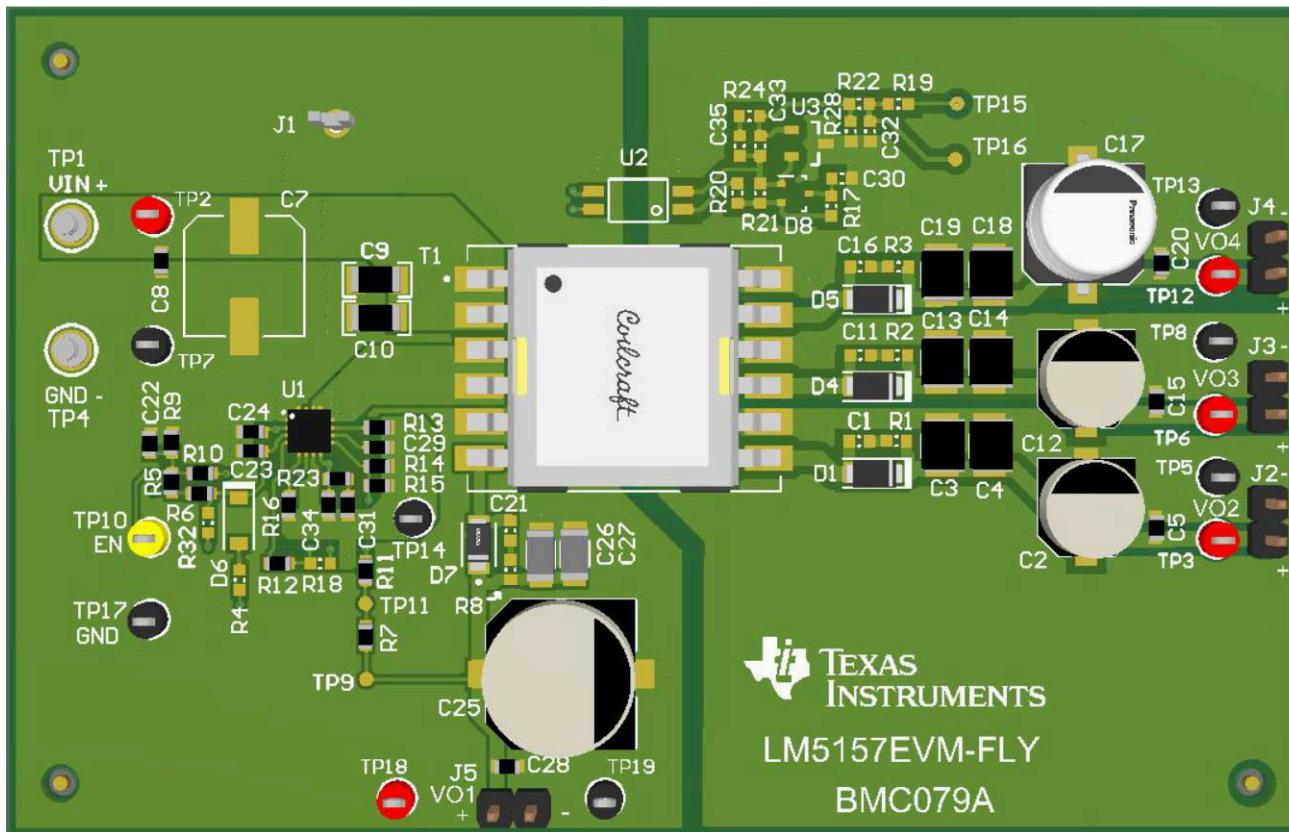


Figure 4-1. EVM Picture

5 Test Setup and Procedure

5.1 EVM Test Setup Schematic

Figure 5-1 shows the correct equipment connections and measurement points.

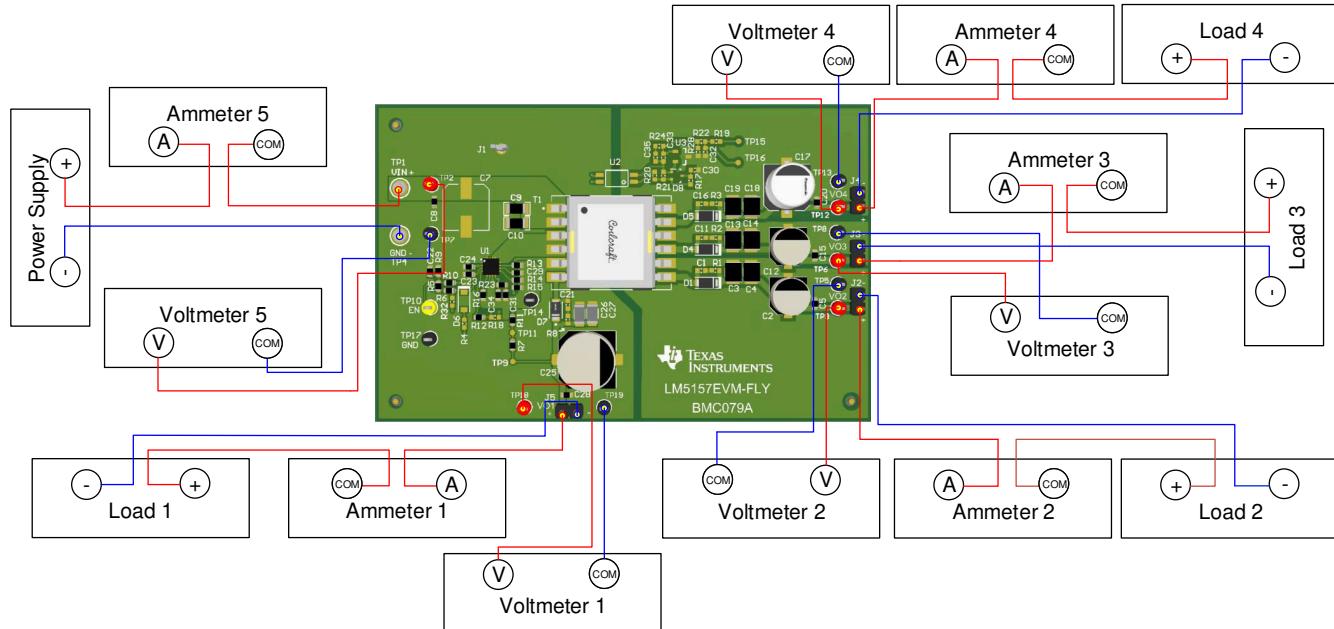


Figure 5-1. Test Setup

5.2 Test Equipment

Power Supply: Use an input voltage source that is a variable supply capable of 0 V to 16 V and sources at least 5 A.

Multimeters:

- Voltmeter 1: 10-V main output voltage Vo1, connect from TP18 (+) to TP19 (-)
- Voltmeter 2: Isolated 20-V output voltage Vo2, connect from TP3 (+) to TP5 (-)
- Voltmeter 3: Isolated 20-V output voltage Vo3, connect from TP6 (+) to TP8 (-)
- Voltmeter 4: Isolated 20-V output voltage Vo4, connect from TP12 (+) to TP13 (-)
- Voltmeter 5: Input voltage, connect from TP2 (+) to TP7 (-)
- Ammeter 1: 10-V main output current, must be able to handle 1 A
- Ammeter 2: Vo2 output current, must be able to handle 0.5 A
- Ammeter 3: Vo3 output current, must be able to handle 0.5 A
- Ammeter 4: Vo4 output current, must be able to handle 0.5 A
- Ammeter 5: Input current, must be able to handle 3 A

Loads: Electronic loads, pure resistive loads, or a combination of both, can be used at the four outputs.

- An electronic load can be used for each output rail. The electronic load should be constant resistance (CR) or constant current (CC) capable. It should safely handle 0.5 A at 20 V.
- Resistive load can also be used. Limit the minimum resistance for each output, which corresponds to the maximum power, with the following:
 - Load 1: 4.0Ω or 250 mA
 - Load 2: 266.7Ω or 75 mA
 - Load 3: 266.7Ω or 75 mA
 - Load 4: 133.3Ω or 150 mA

6 Test Data and Performance Curves

Figure 6-1 through Figure 6-27 present the typical performance of the LM5157EVM-FLY. Based on measurement techniques and environmental variables, measurements might differ slightly to the data presented.

6.1 Efficiency

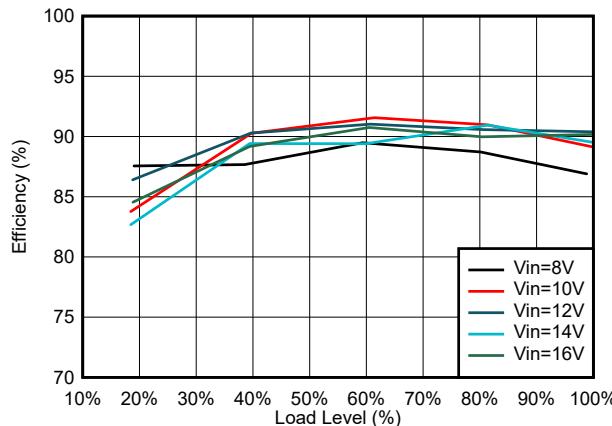


Figure 6-1. Efficiency Under Different Input Voltages and Load Levels

6.2 Output Regulation and Cross-Regulations

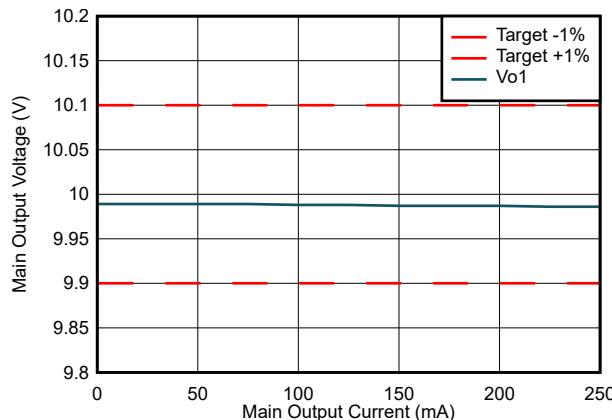


Figure 6-2. Main Output Voltage Regulation vs Main Output Load ($Io_2, Io_3, Io_4 = 100\%$)

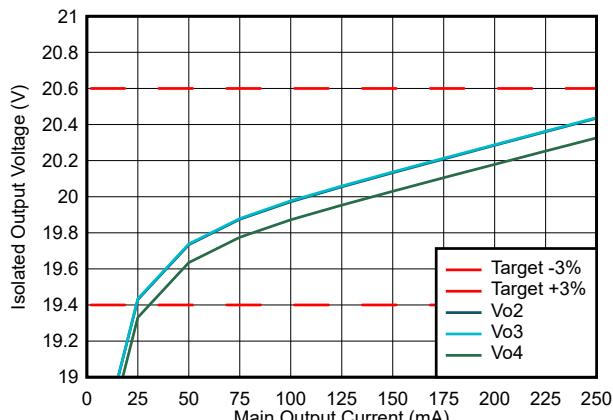


Figure 6-3. Cross Regulation of Isolated Outputs vs Main Output Load ($Io_2, Io_3, Io_4 = 10\%$)

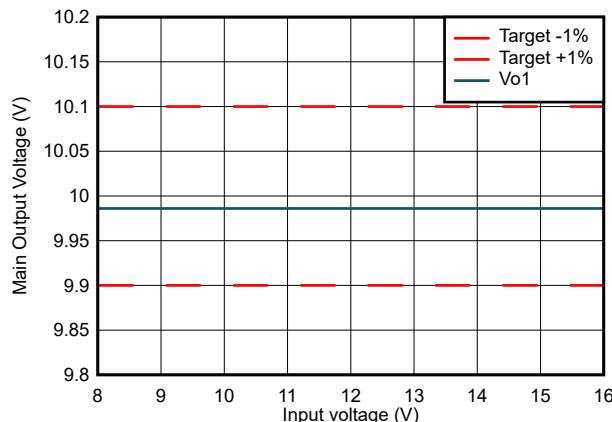


Figure 6-4. Main Output Voltage Regulation vs Input Voltage ($Io_2, Io_3, Io_4 = 100\%$)

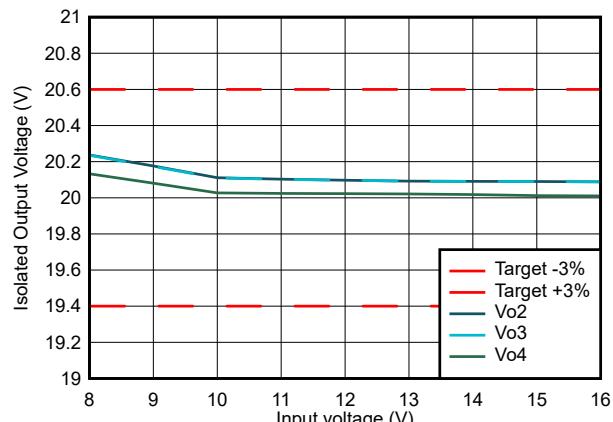


Figure 6-5. Voltage Regulation of Isolated Outputs vs Input Voltage at 10% load

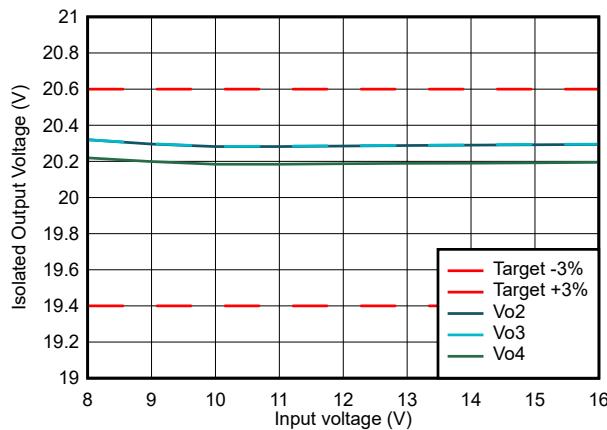


Figure 6-6. Voltage Regulation of Isolated Outputs vs Input Voltage at 50% load

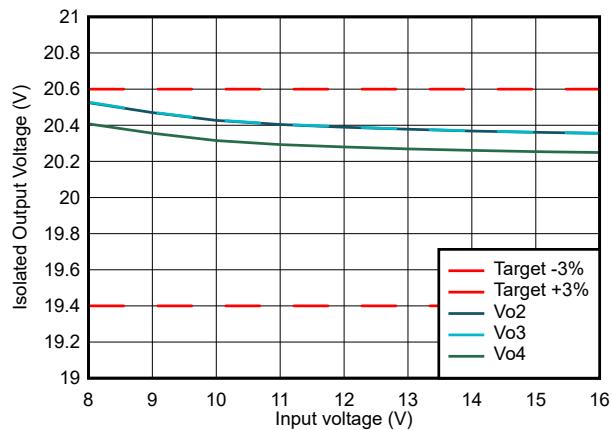


Figure 6-7. Voltage Regulation of Isolated Outputs vs Input Voltage at 100% load

6.3 Steady-State Waveforms

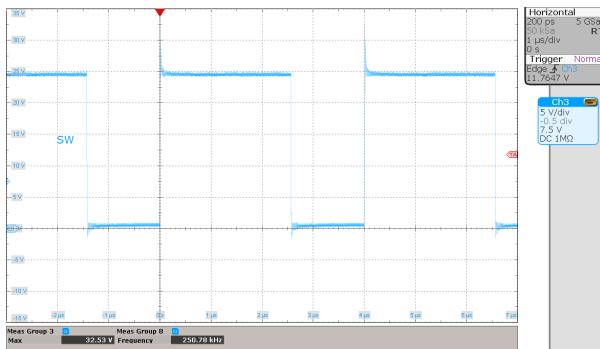


Figure 6-8. SW Waveform at 100% Load, Under 16-V Maximum V_{IN}

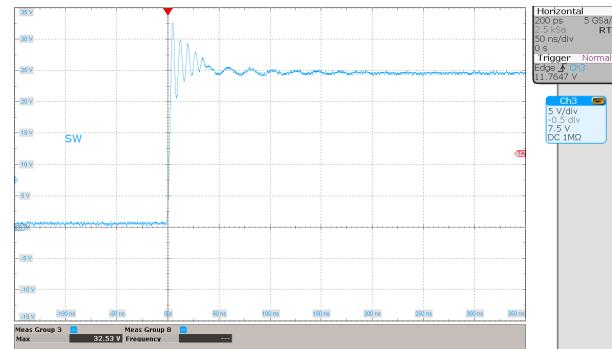


Figure 6-9. Zoomed SW Waveform at 100% Load, Under 16-V Maximum V_{IN}

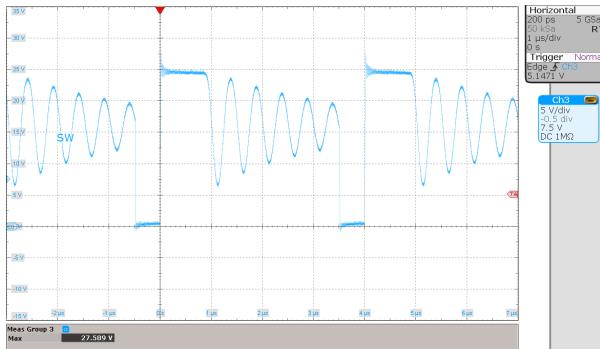


Figure 6-10. SW Waveform at 10% Load, Under 16-V Maximum V_{IN}

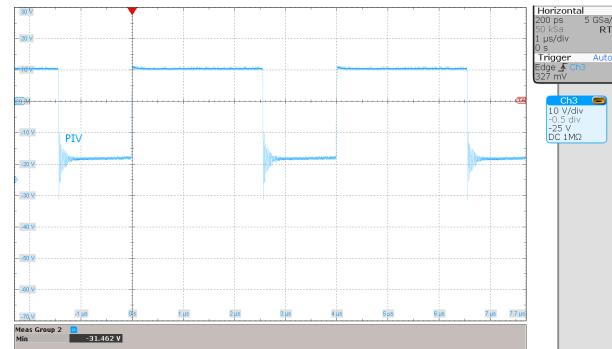


Figure 6-11. PIV of D7 (main Output Rectifier) Under 100% Load and 16-V Maximum V_{IN}

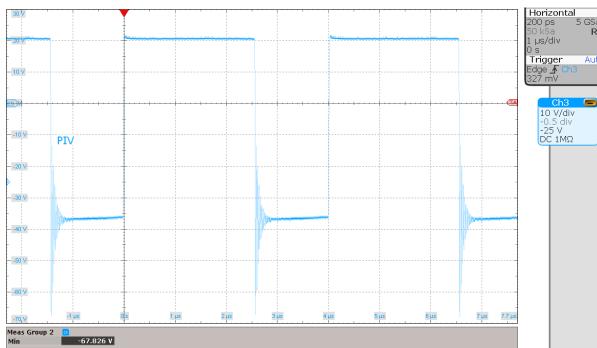


Figure 6-12. PIV of D1 (Vo2 Rectifier) Under 100% Load and 16-V Maximum V_{IN}

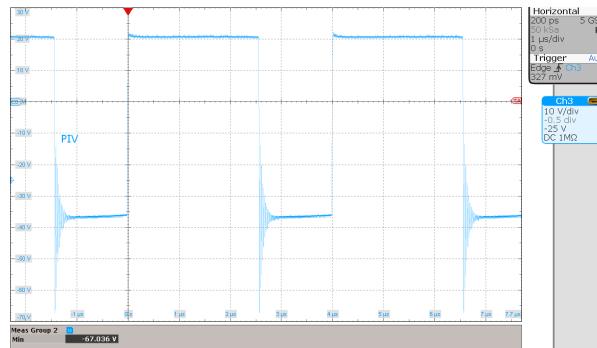


Figure 6-13. PIV of D4 (Vo3 Rectifier) Under 100% Load and 16-V Maximum V_{IN}

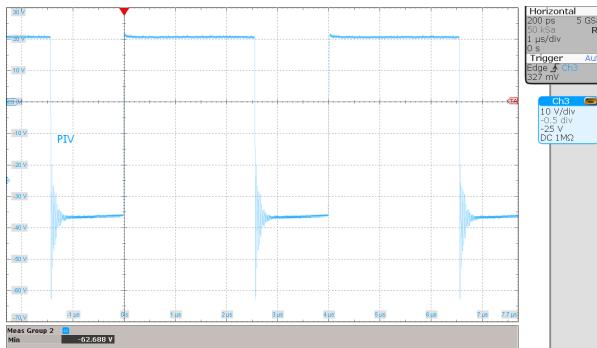


Figure 6-14. PIV of D5 (Vo3 Rectifier) Under 100% Load and 16-V Maximum V_{IN}

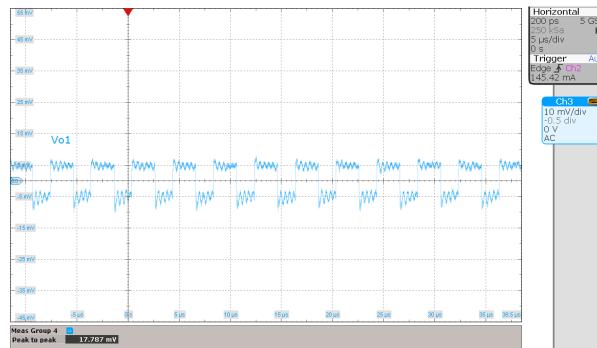


Figure 6-15. Main Output Vo1 Ripple Voltage Under 250-mA Load

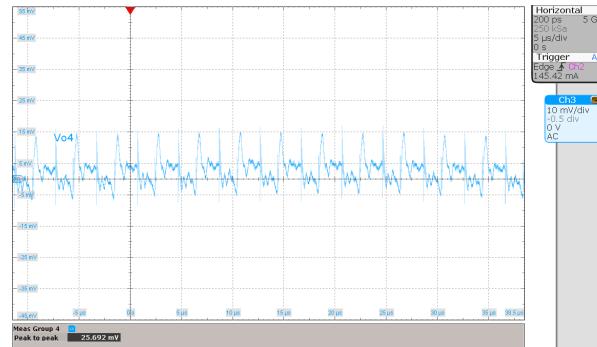


Figure 6-16. Isolated Output Vo4 Ripple Voltage Under 150-mA Load

6.4 Start-Up Waveforms

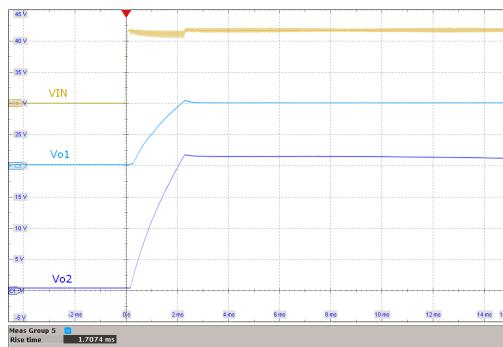


Figure 6-17. Start-Up at 10% Load on Each Output Rail

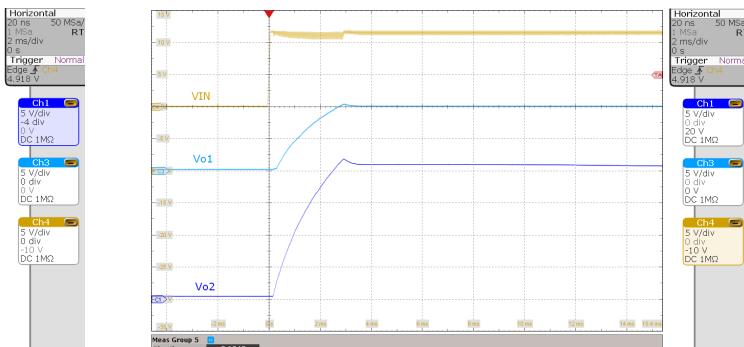


Figure 6-18. Start-Up at 100% Load on Each Output Rail

6.5 Dynamic Responses

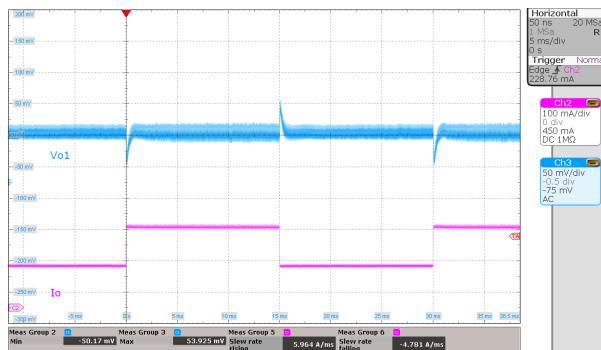


Figure 6-19. Vo1 Response Under Step Load Between 125 mA and 250 mA, $V_{IN} = 12$ V, $I_{O2-4} = 10\%$

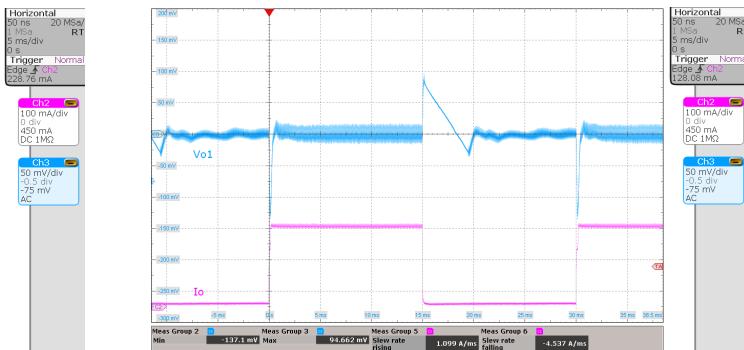


Figure 6-20. Vo1 Response Under Step Load Between 1 mA to 250 mA, $V_{IN} = 12$ V, $I_{O2-4} = 10\%$

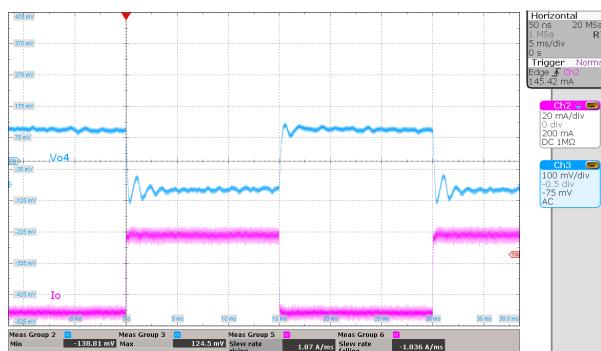


Figure 6-21. Vo4 Response Under Step Load Between 100 mA and 150 mA, $V_{IN} = 12$ V, $I_{O1-3} = 10\%$

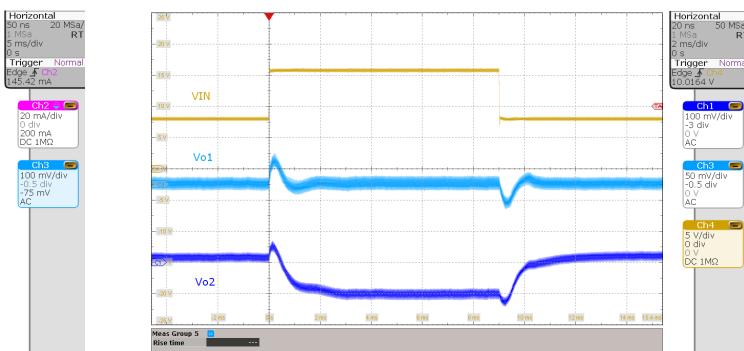


Figure 6-22. Vo1 Response Under Input Change From 8 V to 16 V, 10% Load

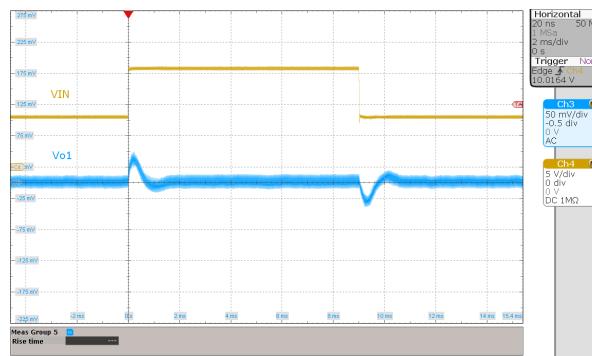


Figure 6-23. Vo1 Response Under Input Change From 16 V to 8 V, 10% Load

6.6 Short-Circuit Protection

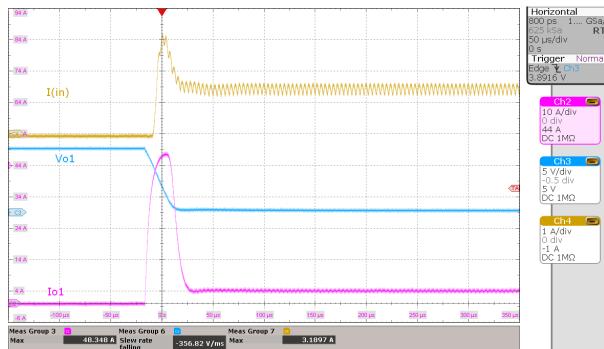


Figure 6-24. Short-Circuit Protection With Hiccup Mode Disabled, $V_{IN} = 12$ V ($R_{13} = 0 \Omega$)



Figure 6-25. Short-Circuit Protection With Hiccup Mode Enabled, $V_{IN} = 12$ V ($R_{13} = 62 \text{ k}\Omega$, $C_{29} = 47 \text{ nF}$)

6.7 Bode Plots

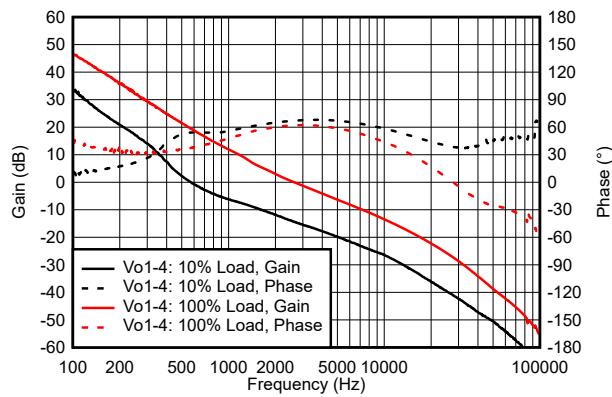


Figure 6-26. Bode Plot ($V_{IN} = 12$ V)

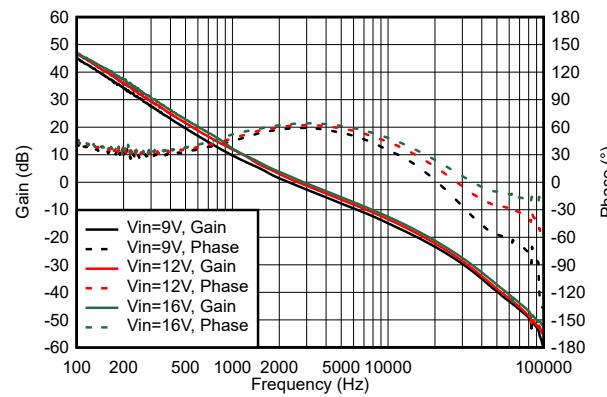


Figure 6-27. Bode Plot (Vo1-4: 100% Load)

6.8 Thermal Image

Figure 6-28 shows the thermal image of the EVM at 12 V_{IN} and 100% load.

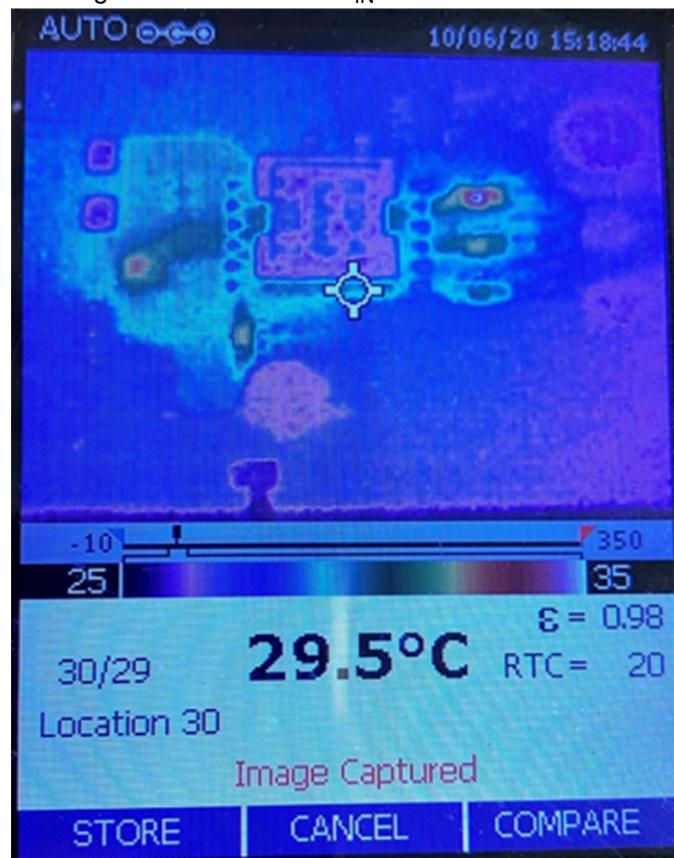
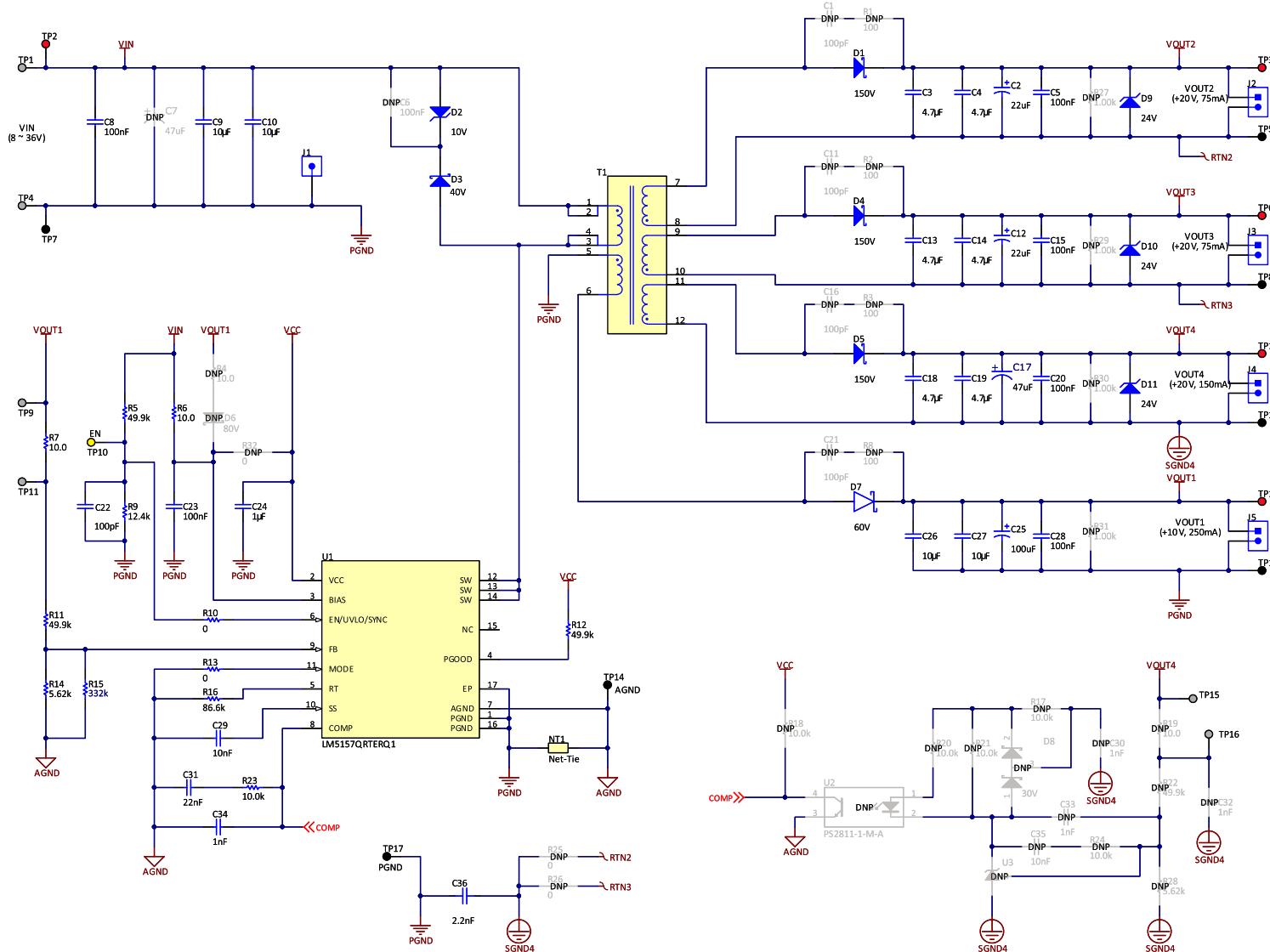


Figure 6-28. EVM Thermal Image at 12 V_{IN} and 100% Load

7 Schematic

Figure 7-1 shows the EVM schematic.



Non-populated components are crossed out.

Figure 7-1. EVM Complete Schematic

8 Bill of Materials

Table 8-1 details the EVM BOM.

Table 8-1. LM5157EVM-FLY Bill of Materials

Designator	Quantity	Value	Description	Package Reference	Part Number	Manufacturer
C2, C12	2	22uF	CAP, Polymer Hybrid, 22 uF, 50 V, ±20%, 0.08 ohm, AEC-Q200 Grade 1, D6.3xL5.8mm SMD	D6.3xL5.8mm	EEH-ZC1H220P	Panasonic
C3, C4, C13, C14, C18, C19	6	4.7uF	CAP, CERM, 4.7 µF, 50 V,±10%, X7R, AEC-Q200 Grade 1, 1210	1210	C1210C475K5RACAUTO	Kemet
C5, C8, C15, C20, C23, C28	6	0.1uF	CAP, CERM, 0.1 µF, 50 V,±10%, X7R, AEC-Q200 Grade 1, 0603	0603	C0603C104K5RACAUTO	Kemet
C7, C17	2	47µF	47µF 50V Aluminum - Polymer Capacitors Radial, Can - SMD 30mOhm 4000 Hrs @ 125°C	SMD2	EEH-ZC1H470P	Panasonic
C9, C10	2	10uF	CAP, CERM, 10 µF, 50 V,±10%, X7R, AEC-Q200 Grade 1, 1206	1206	CGA5L1X7R1H106K160AE	TDK
C22	1	100pF	CAP, CERM, 100 pF, 50 V, ±5%, C0G/NP0, AEC-Q200 Grade 0, 0603	0603	CGA3E2NP01H101J080AA	TDK
C24	1	1uF	CAP, CERM, 1 uF, 25 V, ±10%, X7R, 0603	0603	06033C105KAT2A	AVX
C25	1	100uF	CAP, Aluminum Polymer, 100 uF, 16 V, ±20%, 0.035 ohm, 10x10.3 SMD	10x10.3	16SVP100M	Panasonic
C26, C27	2	10uF	CAP, CERM, 10 µF, 16 V,±10%, X7R, AEC-Q200 Grade 1, 1206	1206	C1206C106K4RACAUTO	Kemet
C29	1	0.01uF	CAP, CERM, 0.01 uF, 16 V, ±10%, X7R, 0603	0603	C0603C103K4RACTU	Kemet
C31	1	0.022uF	CAP, CERM, 0.022 uF, 25 V,±10%, X7R, 0603	0603	C0603C223K3RACTU	Kemet
C34	1	1000pF	CAP, CERM, 1000 pF, 50 V,±5%, X7R, 0603	0603	C0603C102J5RACTU	Kemet
C36	1	2.2nF	2200pF ±10% 2000V (2kV) Ceramic Capacitor X7R (2R1) 1206 (3216 Metric)	1206	1206Y2K00222KET	Knowles Syfer
D1, D4, D5	3	150V	Diode, Schottky, 150 V, 1 A, PowerDI123	PowerDI123	DFLS1150-7	Diodes Inc.
D2	1	10V	Diode, Zener, 10 V, 1.5 W, SMA	SMA	1SMA5925BT3G	ON Semiconductor
D3	1	40V	Diode, Schottky, 40 V, 1 A, AEC-Q101, SMA	SMA	B140Q-13-F	Diodes Inc.
D7	1		DIODE SCHOTTKY 60V 1A POWERDI123	PowerDI123	DFLS160-7	Diodes
D9, D10, D11	3	24V	Diode, Zener, 24 V, 500 mW, SOD-123	SOD-123	DDZ24C-7	Diodes Inc.
R5, R11, R12	3	49.9k	RES, 49.9 k, 1%, 0.1 W, 0603	0603	RC0603FR-0749K9L	Yageo
R6, R7	2	10.0	RES, 10.0, 1%, 0.1 W, 0603	0603	RC0603FR-0710RL	Yageo
R9	1	12.4k	RES, 12.4 k, 1%, 0.1 W, 0603	0603	RC0603FR-0712K4L	Yageo
R10, R13	2	0	RES, 0, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	ERJ-3GEY0R00V	Panasonic
R14	1	5.62k	RES, 5.62 k, 1%, 0.1 W, 0603	0603	RC0603FR-075K62L	Yageo
R15	1	332k	RES, 332 k, 0.1%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	RC0603FR-07332KL	Yageo
R16	1	86.6k	RES, 86.6 k, 1%, 0.1 W, 0603	0603	RC0603FR-0786K6L	Yageo

Table 8-1. LM5157EVM-FLY Bill of Materials (continued)

Designator	Quantity	Value	Description	Package Reference	Part Number	Manufacturer
R23	1	10.0k	RES, 10.0 k, 1%, 0.1 W, 0603	0603	RC0603FR-0710KL	Yageo
T1	1		FLYBACK TRANSFORMER	SMT_TRANSFORMER_17MM20_21MM97	ZB1324-AL	Coilcraft
U1	1		2.2MHz Wide VIN Boost/Sepic/Flyback Converter with Dual Random Spread Spectrum, RTE0016K (WQFN-16)	RTE0016K	LM5157QRTERQ1	Texas Instruments
C1, C11, C16, C21	0	100pF	CAP, CERM, 100 pF, 100 V, ±1%, C0G/NP0, 0603	0603	C1608C0G2A101F080AA	TDK
C6	0	0.1uF	CAP, CERM, 0.1 µF, 50 V, ±10%, X7R, AEC-Q200 Grade 1, 0603	0603	C0603C104K5RACAUTO	Kemet
C30, C32, C33	0	1000pF	CAP, CERM, 1000 pF, 50 V, ±5%, X7R, 0603	0603	C0603C102J5RACTU	Kemet
C35	0	0.01uF	CAP, CERM, 0.01 uF, 16 V, ±10%, X7R, 0603	0603	C0603C103K4RACTU	Kemet
D6	0	80V	Diode, Schottky, 80 V, 0.5 A, SOD-123	SOD-123	MBR0580-TP	Micro Commercial Components
D8	0	30V	Diode, Schottky, 30 V, 0.2 A, SOT-323	SOT-323	BAT54SWT1G	Fairchild Semiconductor
R1, R2, R3, R8	0	100	RES, 100, 1%, 0.1 W, 0603	0603	RC0603FR-07100RL	Yageo
R4, R19	0	10.0	RES, 10.0, 1%, 0.1 W, 0603	0603	RC0603FR-0710RL	Yageo
R17, R18, R20, R21, R24	0	10.0k	RES, 10.0 k, 1%, 0.1 W, 0603	0603	RC0603FR-0710KL	Yageo
R22	0	49.9k	RES, 49.9 k, 1%, 0.1 W, 0603	0603	RC0603FR-0749K9L	Yageo
R25, R26	0	0	RES, 0, 5%, 0.25 W, AEC-Q200 Grade 0, 1206	1206	ERJ-8GEY0R00V	Panasonic
R27, R29, R30, R31	0	1.00k	RES, 1.00 k, 1%, 0.25 W, AEC-Q200 Grade 0, 1206	1206	ERJ-8ENF1001V	Panasonic
R28	0	5.62k	RES, 5.62 k, 1%, 0.1 W, 0603	0603	RC0603FR-075K62L	Yageo
R32	0	0	RES, 0, 5%, 0.1 W, AEC-Q200 Grade 0, 0603	0603	ERJ-3GEY0R00V	Panasonic
U2	0		Optocoupler, 2.5 kV, 100-200% CTR, SMT	PS2811-1	PS2811-1-M-A	California Eastern Laboratories
U3	0		Low-Voltage (1.24V) Adjustable Precision Shunt Regulators, 3-pin SOT-23, Pb-Free	DBZ0003A		Texas Instruments

9 EVM Layout

Figure 9-1 through Figure 9-4 illustrate the EVM

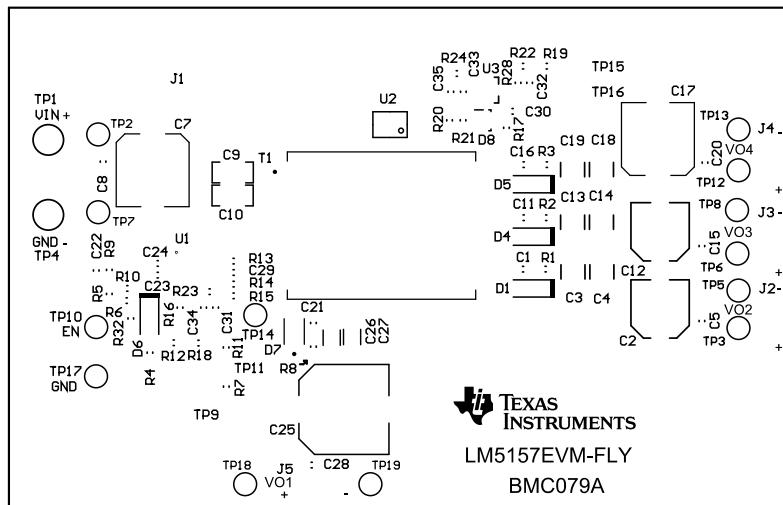


Figure 9-1. Top Silkscreen

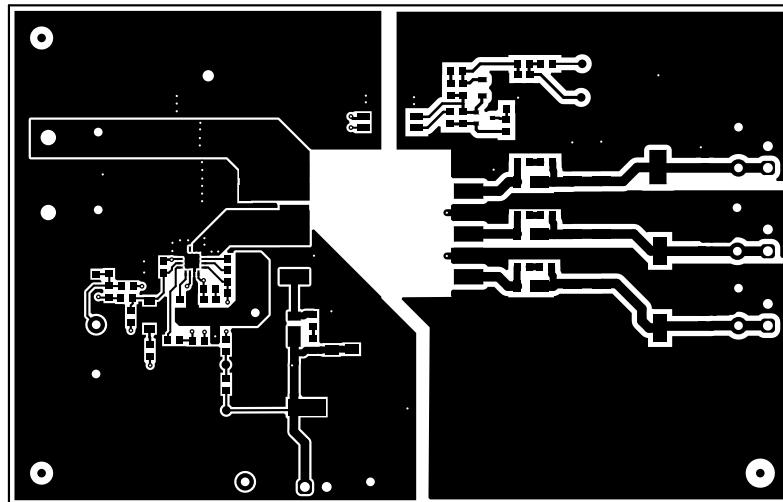


Figure 9-2. Top Layer

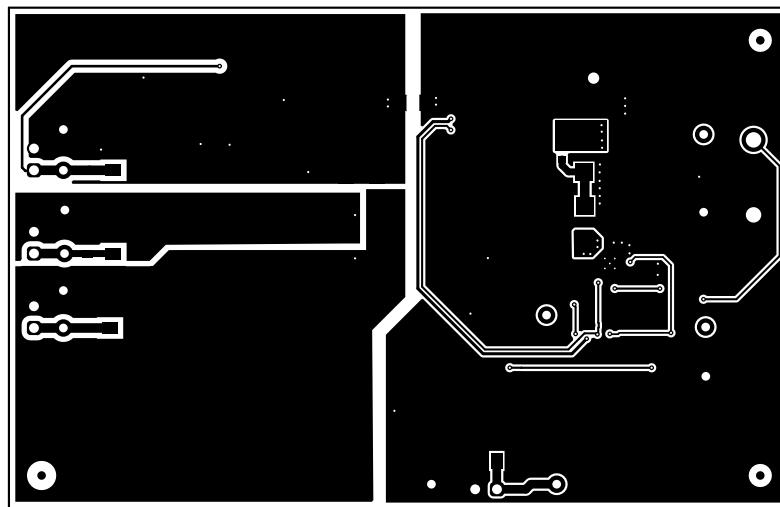


Figure 9-3. Bottom Layer Viewed From Bottom

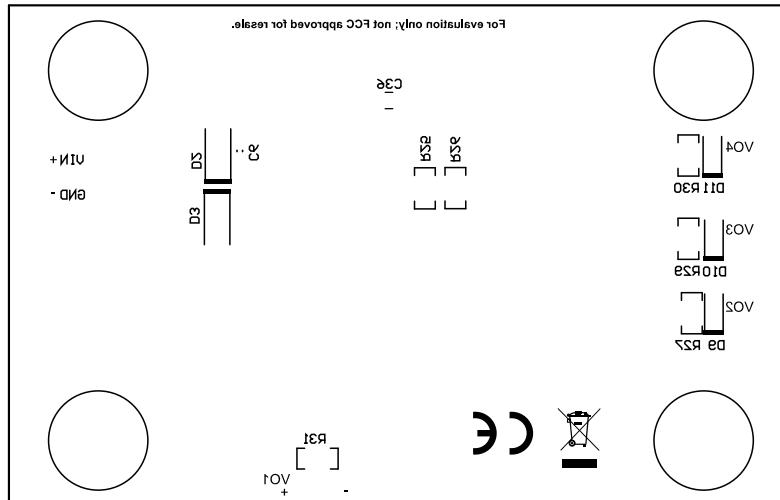


Figure 9-4. Bottom Silkscreen Viewed From Bottom

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