

N-Ch MOSFET

General Description

The WSK220N04 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSK220N04 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

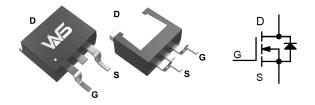
Product Summery

BVDSS	RDSON	ID
40V	$2.5 m\Omega$	220A

Applications

- Switching application
- Power Management for Inverter Systems.

TO-263-2L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit		
Common I	Ratings (T _C =25°C Unless Otherwise Noted)			•	
V _{DSS}	Drain-Source Voltage		40	V	
V_{GSS}	Gate-Source Voltage		±20		
TJ	Maximum Junction Temperature		175	℃	
T _{STG}	Storage Temperature Range		-55 to 175	°C	
I _S	Diode Continuous Forward Current	T _C =25℃	208	А	
Mounted o	on Large Heat Sink			•	
I _{DM}	Pulsed Drain Current *	T _C =25℃	760 ^{1,2}	А	
I _D	Continuous Brain Comment	T _C =25℃	220	А	
	Continuous Drain Current	T _C =100°C	139		
P _D	Maximum Dawar Disaination	T _C =25℃	218	W	
	Maximum Power Dissipation	T _C =100℃	109		
$R_{\theta JC}$	Thermal Resistance-Junction to Case		0.55	°C/W	
$R_{ heta JA}$	Thermal Resistance-Junction to Ambient	62.5			
Avalanche	Ratings			•	
E _{AS}	Avalanche Energy, Single Pulsed	L=0.5mH	1.4 ^{1,2}	J	

NOTE:

1,Repetitive rating; pulse width limited by junction temperatur

2,Drain current is limited by junction temperature



Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

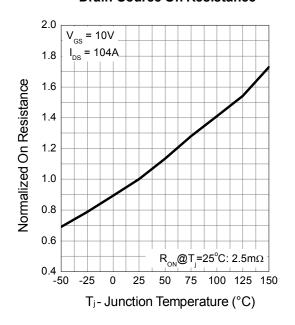
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Static Char	racteristics	•	<u>.</u>			
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	40	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =40V, V _{GS} =0V	-	-	1	^
		T _J =85℃	-	-	10	μΑ
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	2.0	3.0	4.0	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} *	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =104A	-	2.5	3.2	mΩ
Diode Cha	racteristics	•	·#			
V _{SD} *	Diode Forward Voltage	I _{SD} =104 A, V _{GS} =0V	-	0.8	1.2	V
t _{rr}	Reverse Recovery Time	I _{SD} =104A, dI _{SD} /	-	36	-	ns
Q _{rr}	Reverse Recovery Charge	dt=100A/μs	-	59	-	nC
Dynamic C	haracteristics	•				
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	1.0	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V,	-	5710	-	pF
C _{oss}	Output Capacitance	V _{DS} =25V,	-	1463	-	
C _{rss}	Reverse Transfer Capacitance	Frequency=1.0MHz	-	595	-	
t _{d(ON)}	Turn-on Delay Time		-	34	-	
Tr	Turn-on Rise Time	V_{DD} =20V, R_{G} =6 Ω , I_{DS} =104A, V_{GS} =10V,	-	19	-	ns
t _{d(OFF)}	Turn-off Delay Time	======================================	-	44	-	
T _f	Turn-off Fall Time		-	61	-	
Gate Charg	ge Characteristics		-	-	Ī	Ī
Qg	Total Gate Charge		-	156	-	
Q_{gs}	Gate-Source Charge	V_{DS} =32V, V_{GS} =10V, I_{DS} =104A	-	28	-	nC
Q_gd	Gate-Drain Charge		-	65	-	

Note * : Pulse test ; pulse width $\leq\!300\mu s,$ duty cycle $\!\leq\!2\%.$

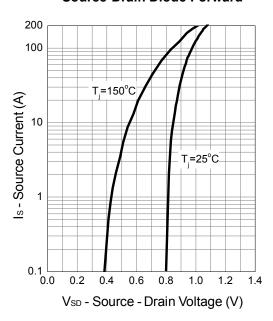


Typical Characteristics

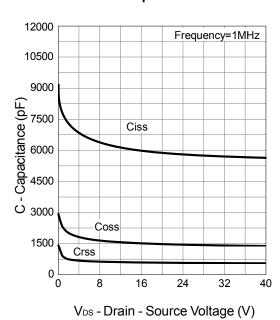
Drain-Source On Resistance



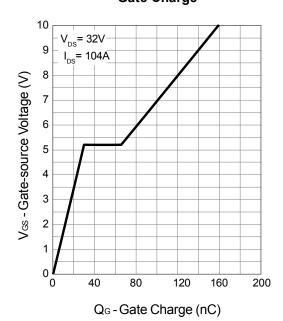
Source-Drain Diode Forward



Capacitance

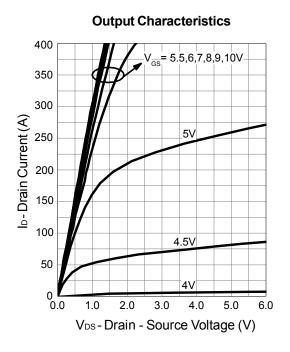


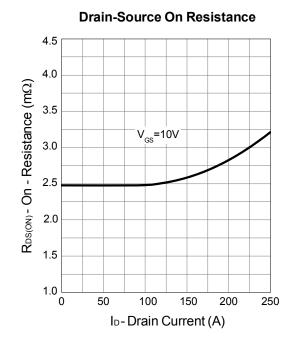
Gate Charge

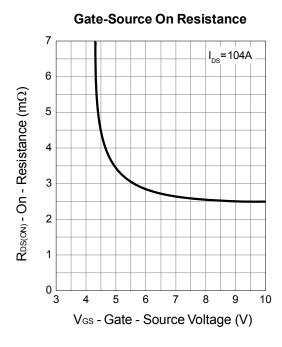


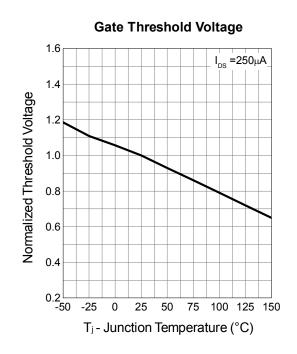


Typical Characteristics



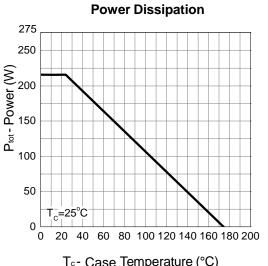


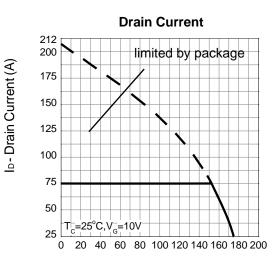






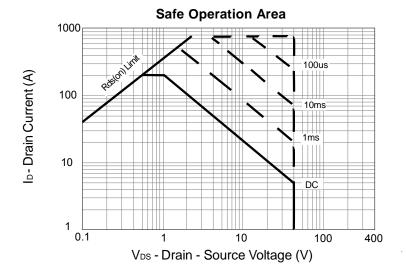
Typical Characteristics



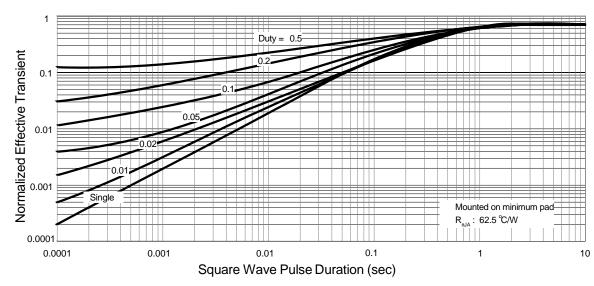




T_c-Case Temperature (°C)



Thermal Transient Impedance





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