



### **General Description**

The WST2N7002K is the highest performance trench N-Ch MOSFET with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The WST2N7002K meet the RoHS and Green Product requirement with full function reliability approved.

#### **Features**

- High-speed switching
- Green Device Available
- ESD Protected:2KV

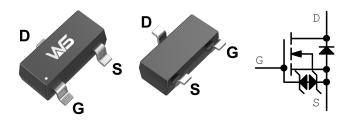
## **Product Summery**

BVDSS	RDSON	ID
60V	1Ω	300mA

## **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC
- Networking DC-DC Power System
- Load Switch

### **SOT-23N Pin Configuration**



## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units	
$V_{DS}$	Drain-Source Voltage	60	V	
$V_{GS}$	Gate-Source Voltage	±20	V	
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	300	mA	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	150	mA	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	1.2	А	
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>3</sup>	0.2	W	
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$ C	
$T_J$	Operating Junction Temperature Range	-55 to 150	$^{\circ}$	

## **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		625	°C/W

**N-Ch MOSFET** 

# Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	60			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.05		V/°C
В	Statia Drain Source On Begintanes <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =0.5A		1	3	0
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =0.2A		4	5	Ω
$V_{GS(th)}$	Gate Threshold Voltage		1		2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient			-3.7		mV/℃
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =60 $V$ , $V_{GS}$ =0 $V$ , $T_J$ =25 $^{\circ}$ C			1	uA
		$V_{DS}$ =60V , $V_{GS}$ =0V , $T_{J}$ =55 $^{\circ}$ C			5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}$ =0V			±10	uA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =0.3A		300		mS
T <sub>d(on)</sub>	Turn-On Delay Time			15	6	
Tr	Rise Time	$V_{DD}$ =30V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$ ,		35	3.3	no
$T_{d(off)}$	Turn-Off Delay Time	I <sub>D</sub> =0.5A		35	16	ns
T <sub>f</sub>	Fall Time			35	13.6	
C <sub>iss</sub>	Input Capacitance			32	56	
C <sub>oss</sub>	Output Capacitance	$V_{DS}$ =25V , $V_{GS}$ =0V , f=1MHz		7	17	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			3	10.6	

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,4</sup>	-V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			300	mA
I <sub>SM</sub>	Pulsed Source Current <sup>2,4</sup>				1.2	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	٧

## Note:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300 us$  , duty cycle  $\leq 2\%$  3.The power dissipation is limited by 150  $^\circ\!\!\!\!\!\!\mathrm{C}$  junction temperature.
- $\textbf{4.The data is theoretically the same as } \textbf{I}_{D} \text{ and } \textbf{I}_{DM} \text{ , in real applications , should be limited by total power dissipation.}$



## **Typical Characteristics**

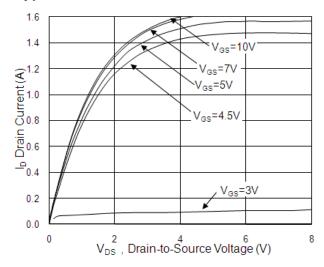


Fig.1 Typical Output Characteristics

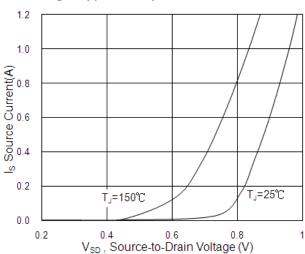


Fig.3 Forward Characteristics of Reverse

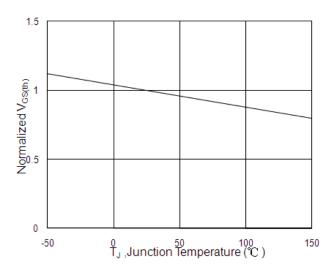


Fig.5 Normalized  $V_{\text{GS(th)}}$  vs.  $T_J$ 

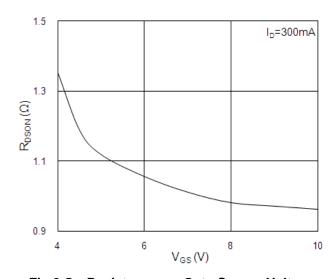


Fig.2 On-Resistance vs. Gate-Source Voltage

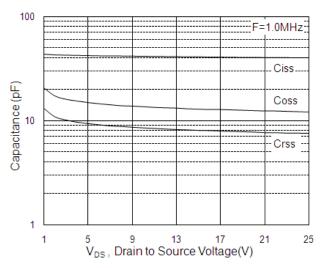


Fig.4 Capacitance

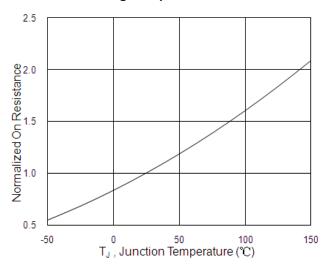
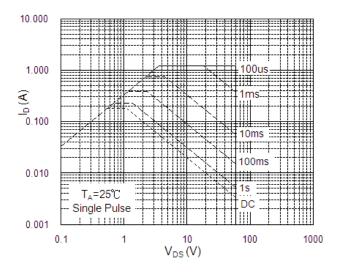


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>





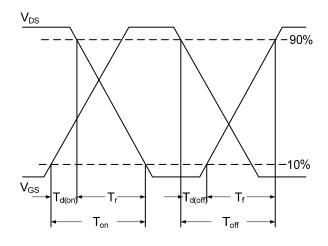


Fig.8 Safe Operating Area

Fig.10 Switching Time Waveform

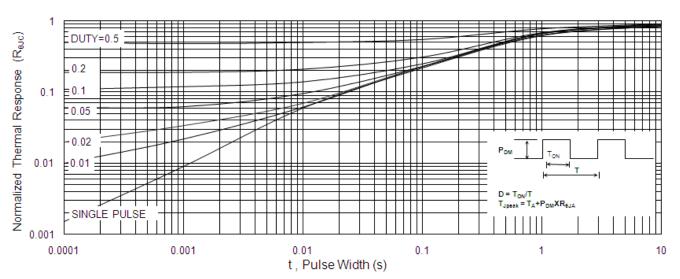


Fig.9 Normalized Maximum Transient Thermal Impedance



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