

isc Silicon PNP Power Transistor

2SA1396

DESCRIPTION

- · Low Collector Saturation Voltage-
 - : $V_{CE(sat)} = -0.6V(Max)@I_{C} = -5A$
- · High Switching Speed
- Complement to Type 2SC3568
- · Minimum Lot-to-Lot variations for robust device performance and reliable operation

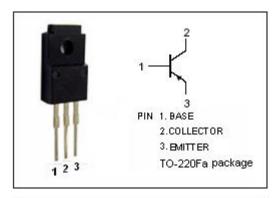


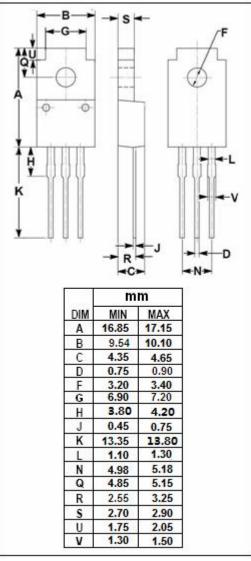
APPLICATIONS

• Designed for switching regulator, DC-DC converter and high frequency power amplifier applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-100	V
V _{CEO}	Collector-Emitter Voltage	-100	V
V _{EBO}	Emitter-Base Voltage	-7	V
Ic	Collector Current-Continuous	-10	Α
Ісм	Collector Current-Peak	-20	Α
I _B	Base Current-Continuous	-5	Α
Pc	Collector Power Dissipation @T _C =25℃	30	W
TJ	Junction Temperature	150	$^{\circ}$
T _{stg}	Storage Temperature	-55~150	$^{\circ}$







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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

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SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT			
V _{CEO(SUS)}	Collector-Emitter Sustaining Voltage	I _C = -5A; I _B = -0.5A, L= 1mH	-100		V			
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A		-0.6	V			
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -5A; I _B = -0.5A		-1.5	V			
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0		-10	μА			
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0		-10	μА			
h _{FE-1}	DC Current Gain	I _C = -0.5A; V _{CE} = -5V	40	200				
h _{FE-2}	DC Current Gain	I _C = -3A; V _{CE} = -5V	40	200				
h _{FE-3}	DC Current Gain	I _C = -5A; V _{CE} = -5V	20					

♦ h_{FE-2} Classifications

M	L	К
40-80	60-120	100-200

NOTICE:

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