



isc Silicon PNP Darlington Power Transistor

DESCRIPTION

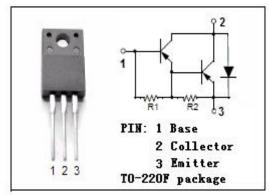
- · Collector-Emitter Breakdown Voltage-
- : V_{(BR)CEO}= -100V(Min)
- · High DC Current Gain-
 - : h_{FE} = 2000(Min)@ (V_{CE} = -2V, I_{C} = -2A)
- · Low Collector Saturation Voltage-
 - : $V_{CE(sat)} = -1.5V(Max)@ (I_C = -2A, I_B = -2mA)$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

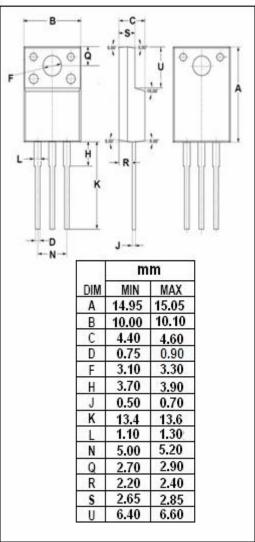
APPLICATIONS

 Designed for low-frequency power amplifiers and lowspeed switching applications.

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-100	V
Vceo	Collector-Emitter Voltage	-100	V
V _{EBO}	Emitter-Base Voltage	-7	V
Ic	Collector Current-Continuous	-5	А
I _{CM}	Collector Current-Peak	-10	А
I _B	Base Current-Continuous	-0.5	А
Pc	Collector Power Dissipation @T _a =25°C	2	10/
	Collector Power Dissipation @Tc=25°C	20	W
TJ	Junction Temperature	150	$^{\circ}$ C
T _{stg}	Storage Temperature	-55~150	$^{\circ}$ C







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ELECTRICAL CHARACTERISTICS

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -2A; I _B = -2mA			-1.5	V	
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -2A; I _B = -2mA			-2.0	V	
I _{CBO}	Collector Cutoff Current	V _{CB} = -100V; I _E = 0			-1.0	μА	
h _{FE-1}	DC Current Gain	I _C = -2A; V _{CE} = -2V	2000		20000		
h _{FE-2}	DC Current Gain	I _C = -4A; V _{CE} = -2V	500				
Сов	Output Capacitance	I _E = 0; V _{CB} = -10V; f _{test} = 1MHz		60		pF	
Switching Times							
t _{on}	Turn-on Time			0.5		μ \$	
t _{stg}	Storage Time	I_{C} = -2A, I_{B1} = - I_{B2} = -2mA, V_{CC} \approx -50V; R_{L} = 25 Ω		1.0		μ \$	
†f	Fall Time			1.0		μs	

♦ h_{FE-1} Classifications

М	L	К
2000-5000	4000-10000	8000-20000



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