

isc Silicon NPN Darlington Power Transistor

2SD1466

DESCRIPTION

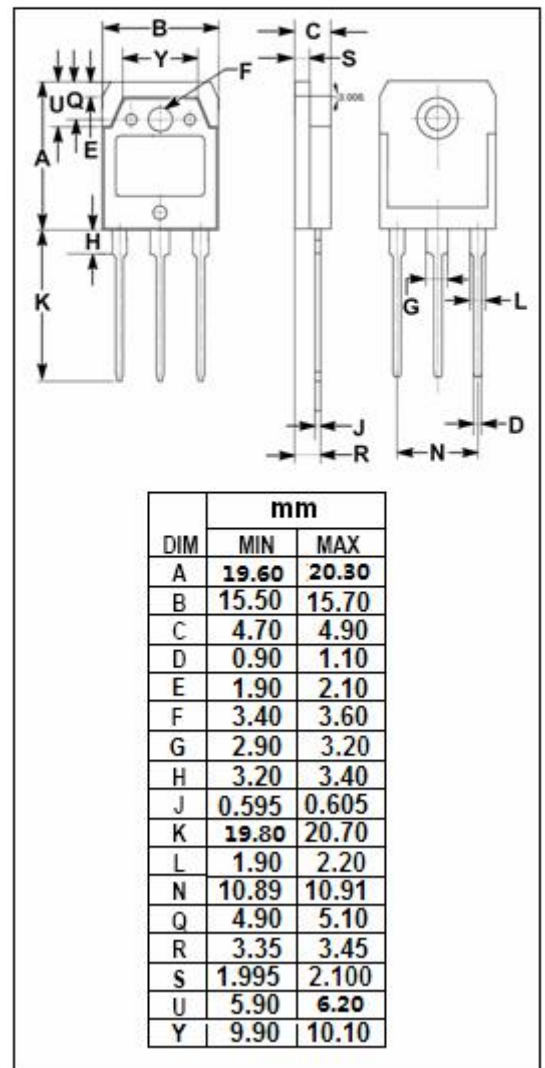
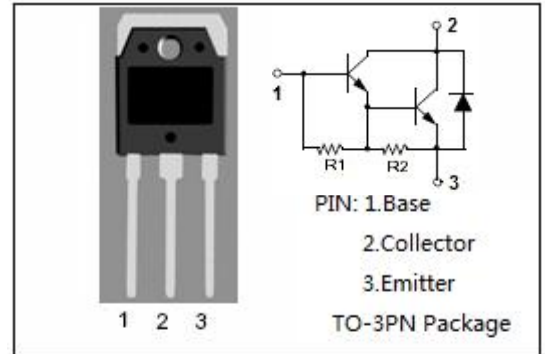
- High Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = 450V(\text{Min})$
- Low Collector-Emitter Saturation Voltage-
: $V_{CE(sat)} = 1.5V(\text{Max}) @ I_C = 8A$
- High DC Current Gain
: $h_{FE} = 200(\text{Min}) @ I_C = 15A, V_{CE} = 3V$
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Power amplification applications.

ABSOLUTE MAXIMUM RATINGS($T_a = 25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	500	V
V_{CEO}	Collector-Emitter Voltage	450	V
V_{EBO}	Emitter-Base Voltage	7	V
I_C	Collector Current-Continuous	15	A
I_{CM}	Collector Current-Peak	30	A
P_C	Collector Power Dissipation @ $T_C = 25^\circ\text{C}$	100	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon NPN Darlington Power Transistor**2SD1466****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = 30mA ; I _B = 0	450			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	I _E = 20mA ; I _C = 0	7			V
V _{CE(sat)-1}	Collector-Emitter Saturation Voltage	I _C = 8A; I _B = 0.1A			1.5	V
V _{CE(sat)-2}	Collector-Emitter Saturation Voltage	I _C = 10A; I _B = 0.25A			1.8	V
V _{CE(sat)-3}	Collector-Emitter Saturation Voltage	I _C = 12A; I _B = 0.3A			2.0	V
V _{BE(sat)-1}	Base-Emitter Saturation Voltage	I _C = 8A; I _B = 0.1A			2.2	V
V _{BE(sat)-2}	Base-Emitter Saturation Voltage	I _C = 10A; I _B = 0.25A			2.5	V
I _{CBO}	Collector Cutoff Current	V _{CB} = 500V; I _E = 0			100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = 5V; I _C = 0			20	mA
h _{FE-1}	DC Current Gain	I _C = 5A ; V _{CE} = 3V	1000			
h _{FE-2}	DC Current Gain	I _C = 15A ; V _{CE} = 3V	200			

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