

# **isc** Thyristors

# 40TPS12A

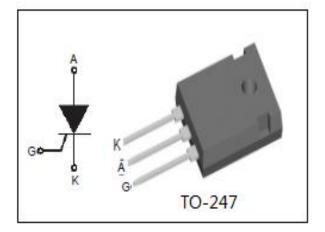
## **DESCRIPTION**

- · With TO-247 packaging
- Long-term stability
- Thyristor for line frequency
- Planar passivated chip
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

## **APPLICATIONS**

- Switching applications
- Line rectifying 50/60 Hz





## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	MIN	UNIT
V <sub>DRM</sub>	Repetitive peak off-state voltage	1200	V
$V_{RRM}$	Repetitive peak reverse voltage	1200	V
I <sub>T(AV)</sub>	Average forward current @Tc=79°C	35	Α
I <sub>T(RMS)</sub>	RMS on-state current	55	Α
I <sub>TSM</sub>	Surge non-repetitive on-state current 50HZ (1/2 cycle,sine wave ) 60HZ	500 600	Α
P <sub>G(AV)</sub>	Average gate power dissipation	2.5	W
T <sub>j</sub>	Operating junction temperature	-40~125	$^{\circ}$
T <sub>stg</sub>	Storage temperature	-40~150	$^{\circ}$



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## **ELECTRICAL CHARACTERISTICS (Tc=25℃ unless otherwise specified)**

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I <sub>RRM</sub>	Repetitive peak reverse current	V <sub>RM</sub> =V <sub>RRM</sub> V <sub>DM</sub> =V <sub>DRM</sub>	T <sub>j</sub> =25℃ T <sub>j</sub> =125℃		0.5 10	mA
I <sub>DRM</sub>	Repetitive peak off-state current					
V <sub>TM</sub>	On-state voltage	I <sub>TM</sub> = 110A			1.85	V
I <sub>GT</sub>	Gate-trigger current	$V_D = 6V$			150	mA
V <sub>GT</sub>	Gate-trigger voltage	$V_D = 6V$			2.5	V
$R_{th(j-c)}$	Thermal resistance	Junction to case			0.6	°C/W

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